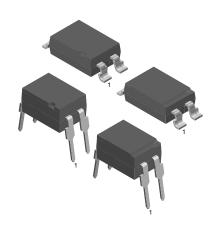
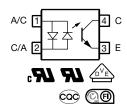


# Optocoupler, Phototransistor Output, AC Input, Low Input Current





### **LINKS TO ADDITIONAL RESOURCES**







#### **DESCRIPTION**

The SFH628A (DIP) and SFH6286 (SMD) feature a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared emitting diode, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 or SMD package.

The coupling devices are designed for signal transmission between two electrically separated circuits.

The couplers are end-stackable with 2.54 mm lead spacing. Creepage and clearance distances of > 8 mm are achieved with option 6. This version complies with IEC 60950 (DIN VDE 0805) for reinforced insulation to an operation voltage of 400  $V_{RMS}$  or DC.

### **FEATURES**

- High common mode interference immunity
- Isolation rated voltage 4420 V<sub>RMS</sub>
- Low coupling capacitance
- Good CTR linearity depending on forward current



- Low CTR degradation
- High collector emitter voltage, V<sub>CEO</sub> = 55 V
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **APPLICATIONS**

- Telecom
- Industrial controls
- Battery powered equipment
- Office machines

### **AGENCY APPROVALS**

- <u>UL</u>
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- BSI
- CQC
- FIMKO



ORDERING INFORMATION								
S F H 6 2 8 X - # X 0 # # T  PART NUMBER CTR PACKAGE OPTION TAPE AND REEL  Option 6 Option 7  Option 8 SPH6286 Option  9.27 mm								
AGENCY CERTIFIED /			CTF	२ (%)				
PACKAGE		SFH628A			SFH6286			
UL, cUL, BSI, FIMKO, CQC	63 to 200	100 to 320	160 to 500	63 to 200	100 to 320	160 to 500		
DIP-4	SFH628A-2	SFH628A-3	SFH628A-4	-	-	-		
SFH6286 option	-	-	-	SFH6286-2T (1)	SFH6286-3T <sup>(1)</sup>	SFH6286-4T (1)		
UL, cUL, BSI, FIMKO, CQC, VDE (option 1) 63 to 200 100 to 320 160 to 500 63 to 200 100 to 320 160 to 500						160 to 500		
DIP-4	-	SFH628A-3X001	-	-	-	-		
DIP-4, 400 mil, option 6	SFH628A-2X016	SFH628A-3X016	SFH628A-4X016	-	-	-		
SMD-4, option 7	-	SFH628A-3X017T (1)	-	_	-	-		
SMD-4, option 8	SFH628A-2X018T	-	-	-	-	-		
SFH6286 option	-	-	-	SFH6286-2X001T	SFH6286-3X001T	SFH6286-4X001T (1)		

### Notes

<sup>(1)</sup> Also available in tubes; do not add T to end

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
DC forward current		I <sub>F</sub>	± 50	mA			
Surge forward current	t ≤ 10 µs	I <sub>FSM</sub>	± 2.5	А			
Power dissipation		P <sub>diss</sub>	76	mW			
OUTPUT							
Collector emitter voltage		$V_{CEO}$	55	V			
Emitter collector voltage		V <sub>ECO</sub>	7	V			
Callantaria		Ic	50	mA			
Collector current	t <sub>p</sub> ≤ 1 ms	I <sub>C</sub>	100	mA			
Power dissipation		P <sub>diss</sub>	150	mW			
COUPLER							
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C			
Ambient temperature range		T <sub>amb</sub>	-55 to +100	°C			
Soldering temperature (1)	Max. 10 s, dip soldering distance to seating plane ≥ 1.5 mm	T <sub>sld</sub>	260	°C			

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

<sup>·</sup> Additional options may be possible, please contact sales office



<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT									
Forward voltage	$I_F = \pm 5 \text{ mA}$		$V_{F}$	-	1.1	1.5	V		
Capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		Co	-	45	-	pF		
Thermal resistance			R <sub>thja</sub>	-	1070	-	K/W		
OUTPUT	OUTPUT								
Collector emitter leakage current	V <sub>CE</sub> = 10 V		I <sub>CEO</sub>	-	10	200	nA		
Collector emitter capacitance	$V_{CE} = 5 \text{ V}, f = 1 \text{ MHz}$		C <sub>CE</sub>	-	7	-	pF		
Thermal resistance			R <sub>thja</sub>	-	500	-	K/W		
COUPLER									
	$I_F = \pm 1 \text{ mA}, I_C = 0.5 \text{ mA}$	SFH628A-2	V <sub>CEsat</sub>	-	0.25	0.4	<b>V</b>		
		SFH6286-2	V <sub>CEsat</sub>	-	0.25	0.4	V		
Collector emitter saturation voltage	$I_F = \pm 1 \text{ mA}, I_C = 0.8 \text{ mA}$	SFH628A-3	V <sub>CEsat</sub>	-	0.25	0.4	٧		
		SFH6286-3	V <sub>CEsat</sub>	-	0.25	0.4	V		
	$I_{\rm F} = \pm 1 \text{ mA}, I_{\rm C} = 1.25 \text{ mA}$	SFH628A-4	V <sub>CEsat</sub>	-	0.25	0.4	<b>V</b>		
	1 = 1 1114, IC = 1.23 1114	SFH6286-4	V <sub>CEsat</sub>	-	0.25	0.4	V		

### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<b>CURRENT TRANSFER RATIO</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$I_F = \pm 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	SFH628A-2	CTR	63	-	200	%
		SFH6286-2	CTR	63	-	200	%
	$I_F = \pm 0.5 \text{ mA}, V_{CE} = 1.5 \text{ V}$	SFH628A-2	CTR	32	100	-	%
		SFH6286-2	CTR	32	100	-	%
	$I_F = \pm 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	SFH628A-3	CTR	100	-	320	%
1 /1		SFH6286-3	CTR	100	-	320	%
I <sub>C</sub> /I <sub>F</sub>	$I_F = \pm 0.5 \text{ mA}, V_{CE} = 1.5 \text{ V}$	SFH628A-3	CTR	50	160	-	%
		SFH6286-3	CTR	50	160	-	%
F = 7	1 4 1 0.5	SFH628A-4	CTR	160	-	500	%
	$I_F = \pm 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	SFH6286-4	CTR	160	-	500	%
	1 05 00 15 1	SFH628A-4	CTR	80	250	-	%
	$I_F = \pm 0.5 \text{ mA}, V_{CE} = 1.5 \text{ V}$	SFH6286-4	CTR	80	250	-	%

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$V_{CC}$ = 5 V, $I_C$ = 2 mA, $R_L$ = 100 $\Omega$	t <sub>on</sub>	-	6	-	μs
Rise time	$V_{CC}$ = 5 V, $I_{C}$ = 2 mA, $R_{L}$ = 100 $\Omega$	t <sub>r</sub>	-	3.5	-	μs
Turn-off time	$V_{CC}$ = 5 V, $I_{C}$ = 2 mA, $R_{L}$ = 100 $\Omega$	t <sub>off</sub>	ı	5.5	-	μs
Fall time	$V_{CC}$ = 5 V, $I_{C}$ = 2 mA, $R_{L}$ = 100 $\Omega$	t <sub>f</sub>	-	5	-	μs



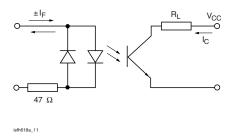


Fig. 1 - Test Circuit

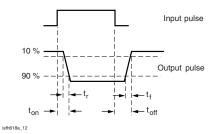


Fig. 2 - Test Circuit and Waveforms

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55 / 100 / 21	
Comparative tracking index		CTI	175	
Maximum rated withstanding isolation voltage	t = 1 min	V <sub>ISO</sub>	4420	V <sub>RMS</sub>
Maximum transient isolation voltage		V <sub>IOTM</sub>	10 000	V <sub>peak</sub>
Maximum repetitive peak isolation voltage		V <sub>IORM</sub>	890	V <sub>peak</sub>
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
Output safety power		P <sub>SO</sub>	400	mW
Input safety current		I <sub>SI</sub>	275	mA
Safety temperature		T <sub>S</sub>	175	°C
Creepage distance	Standard DIP-4		≥ 7	mm
Clearance distance	Standard DIP-4		≥ 7	mm
Creepage distance	400 mil DIP-4		≥ 8	mm
Clearance distance	400 mil DIP-4		≥8	mm
Insulation thickness		DTI	≥ 0.4	mm

### Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

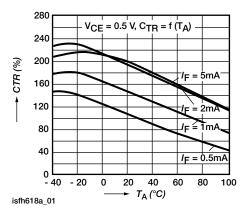


Fig. 3 - Current Transfer Ratio (typ.)

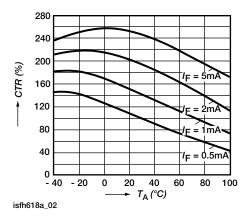


Fig. 4 - Current Transfer Ratio (typ.)

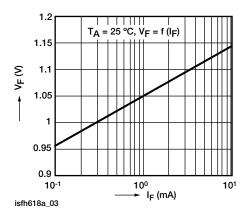


Fig. 5 - Diode Forward Voltage (typ.)

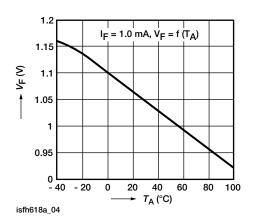


Fig. 6 - Diode Forward Voltage (typ.)

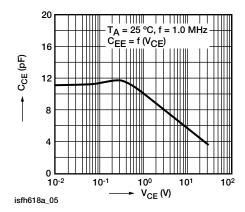


Fig. 7 - Transistor Capacitance

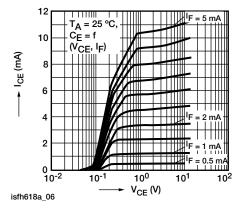


Fig. 8 - Output Characteristics



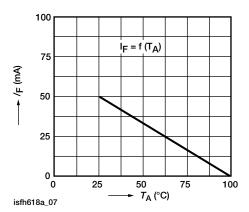


Fig. 9 - Permissible Forward Current Diode

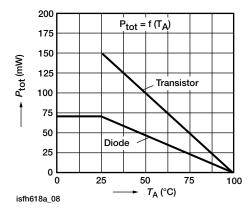


Fig. 10 - Permissible Power Dissipation

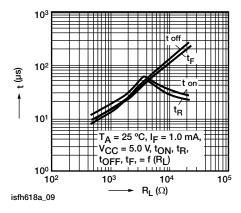
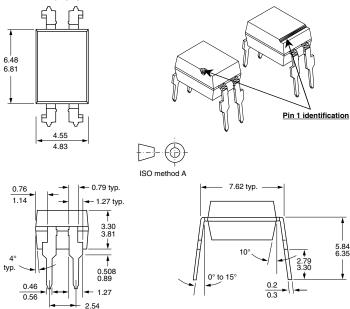
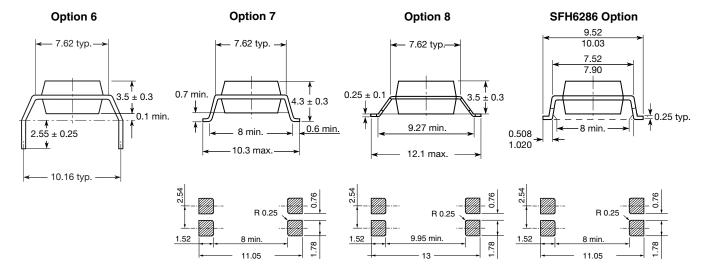


Fig. 11 - Switching Times (Typ.)

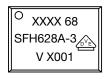


### **PACKAGE DIMENSIONS** in millimeters





### **PACKAGE MARKING** (example)



### **Notes**

- XXXX = LMC (lot marking code)
- Only options 1, 7, and 8 are reflected in the package marking
- The VDE logo is only marked on option1 parts
- · Tape and reel suffix (T) is not part of the package marking



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