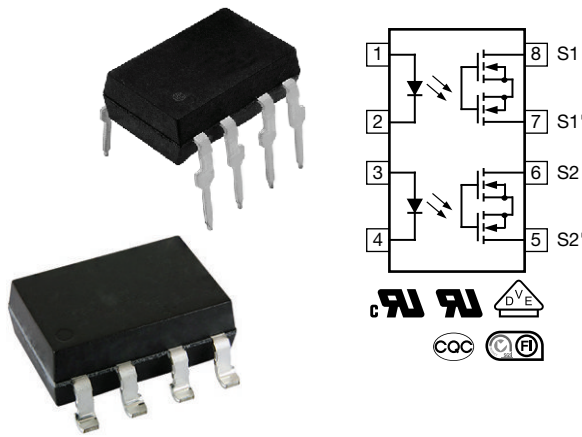


## Dual 1 Form A Solid-State Relay (Normally Open)



### FEATURES

- Isolation test voltage 5300 V<sub>RMS</sub>
- Typical R<sub>ON</sub> 12 Ω
- Load voltage 250 V
- Load current 120 mA
- Clean bounce free switching
- Low power consumption
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



### APPLICATIONS

- General telecom switching
- Security equipment
- Instrumentation
- Industrial controls
- Automatic test equipment

### LINKS TO ADDITIONAL RESOURCES



### DESCRIPTION

The LH1505 contains two normally open switches (dual 1 Form A) that can be used as two independent SPST relays or as one DPST relay. It can replace electromechanical relays in many applications. They are constructed using a GaAlAs LED for actuation control and MOSFET switches for the output.

### AGENCY APPROVALS

- [UL 1577](#)
- [cUL](#)
- [DIN EN 60747-5-5 \(VDE 0884-5\)](#)
- [CQC GB4943.1](#)
- [CQC GB8898](#)
- [FIMKO](#)

ORDERING INFORMATION	
<div style="display: flex; justify-content: space-around; align-items: center;"> <div style="border: 1px solid black; padding: 2px 5px;">L</div> <div style="border: 1px solid black; padding: 2px 5px;">H</div> <div style="border: 1px solid black; padding: 2px 5px;">1</div> <div style="border: 1px solid black; padding: 2px 5px;">5</div> <div style="border: 1px solid black; padding: 2px 5px;">0</div> <div style="border: 1px solid black; padding: 2px 5px;">5</div> <div style="border: 1px solid black; padding: 2px 5px;">A</div> <div style="border: 1px solid black; padding: 2px 5px;">#</div> <div style="border: 1px solid black; padding: 2px 5px;">#</div> <div style="border: 1px solid black; padding: 2px 5px;">T</div> <div style="border: 1px solid black; padding: 2px 5px;">R</div> </div> <p style="text-align: center;"> <span style="margin-right: 100px;">PART NUMBER</span> <span style="margin-right: 100px;">ELECTR. VARIATION</span> <span style="margin-right: 100px;">PACKAGE CONFIG.</span> <span style="margin-right: 100px;">TAPE AND REEL</span> </p>	
<b>PACKAGE</b>	<b>UL, cUL, FIMKO, CQC, VDE</b>
SMD-8, tape and reel	LH1505AACTR
SMD-8, tube	LH1505AAC
DIP-8, tube	LH1505AB



ABSOLUTE MAXIMUM RATINGS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)				
PARAMETER	CONDITION	SYMBOL	VALUE	UNIT
<b>INPUT</b>				
IRED continuous forward current		$I_F$	50	mA
IRED reverse voltage		$V_R$	5	V
Input power dissipation		$P_{diss}$	80	mW
<b>OUTPUT</b>				
DC or peak AC load voltage		$V_L$	250	V
Continuous DC load current at 25 °C, one channel		$I_L$	130	mA
Continuous DC load current at 25 °C, two channels		$I_L$	120	mA
SSR output power dissipation		$P_{diss}$	550	mW
<b>SSR</b>				
Ambient temperature range		$T_{amb}$	-40 to +85	°C
Storage temperature range		$T_{stg}$	-40 to +150	°C
Soldering temperature	$t = 10\text{ s max.}$	$T_{sld}$	260	°C

**Note**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability

ELECTRICAL CHARACTERISTICS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>INPUT</b>						
IRED forward current, switch turn-on	$I_L = 100\text{ mA}, t = 10\text{ ms}$	$I_{Fon}$	-	0.4	2	mA
IRED forward current, switch turn-off	$V_L = \pm 200\text{ V}$	$I_{Foff}$	0.05	0.35	-	mA
IRED forward voltage	$I_F = 10\text{ mA}$	$V_F$	1.15	1.4	1.6	V
IRED reverse current	$V_R = 5\text{ V}$	$I_R$	-	-	10	$\mu\text{A}$
<b>OUTPUT</b>						
On-resistance	$I_F = 5\text{ mA}, I_L = 50\text{ mA}$	$R_{ON}$	6	12	20	$\Omega$
Off-resistance	$I_F = 0\text{ mA}, V_L = \pm 100\text{ V}$	$R_{OFF}$	0.5	5000	-	G $\Omega$
Off-state leakage current	$I_F = 0\text{ mA}, V_L = \pm 100\text{ V}$	$I_O$	-	< 1	200	nA
	$I_F = 0\text{ mA}, V_L = \pm 200\text{ V}$	$I_O$	-	< 1	1000	nA
Output capacitance	$I_F = 0\text{ mA}, V_L = 1\text{ V}, 1\text{ MHz}$	$C_O$	-	39	-	pF
	$I_F = 0\text{ mA}, V_L = 50\text{ V}, 1\text{ MHz}$	$C_O$	-	6	-	pF
<b>TRANSFER</b>						
Capacitance (input to output)	$V_{IO} = 1\text{ V}$	$C_{IO}$	-	0.4	-	pF

**Note**

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements

SWITCHING CHARACTERISTICS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_F = 5\text{ mA}, I_L = 50\text{ mA}$	$t_{on}$	-	0.20	4	ms
Turn-off time	$I_F = 5\text{ mA}, I_L = 50\text{ mA}$	$t_{off}$	-	0.03	4	ms

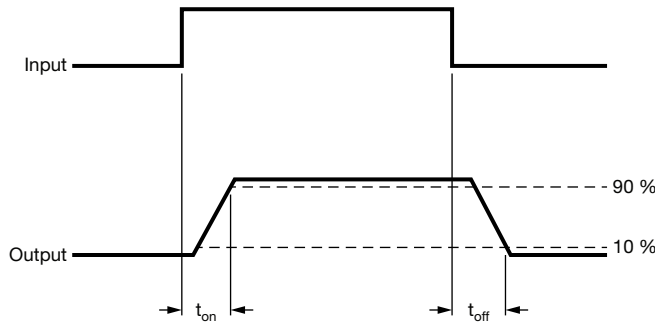
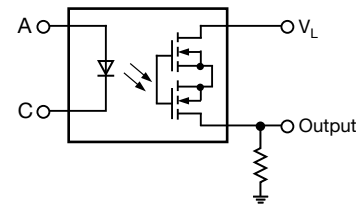


Fig. 1 - Timing Schematic



SAFETY AND INSULATION RATINGS				
PARAMETER	CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		40 / 85 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	$V_{ISO}$	5300	$V_{RMS}$
Maximum transient isolation voltage	According to DIN EN 60747-5-5	$V_{IOTM}$	8000	$V_{peak}$
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	$V_{IORM}$	890	$V_{peak}$
Insulation resistance	$V_{IO} = 500\text{ V}, T_{amb} = 25\text{ }^{\circ}\text{C}$	$R_{IO}$	$\geq 10^{12}$	$\Omega$
	$V_{IO} = 500\text{ V}, T_{amb} = 100\text{ }^{\circ}\text{C}$	$R_{IO}$	$\geq 10^{11}$	$\Omega$
Output safety power	One channel	$P_{SO}$	640	mW
	Two channels		480	
Input safety current	One channel	$I_{SI}$	240	mA
	Two channels		200	
Safety temperature		$T_S$	175	$^{\circ}\text{C}$
Creepage distance			$\geq 7$	mm
Clearance distance			$\geq 7$	mm
Insulation thickness		DTI	$\geq 0.4$	mm
Input to output test voltage, method B	$V_{IORM} \times 1.875 = V_{PR}$ , 100 % production test with $t_M = 1\text{ s}$ , partial discharge $< 5\text{ pC}$	$V_{PR}$	1669	$V_{peak}$
Input to output test voltage, method A	$V_{IORM} \times 1.6 = V_{PR}$ , 100 % sample test with $t_M = 10\text{ s}$ , partial discharge $< 5\text{ pC}$	$V_{PR}$	1424	$V_{peak}$

**Note**

- As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for “safe electrical insulation” only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits

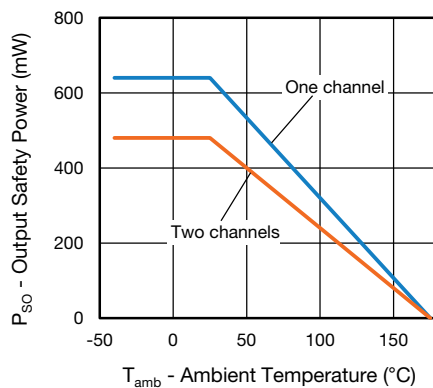


Fig. 2 - Output Safety Power vs. Ambient Temperature

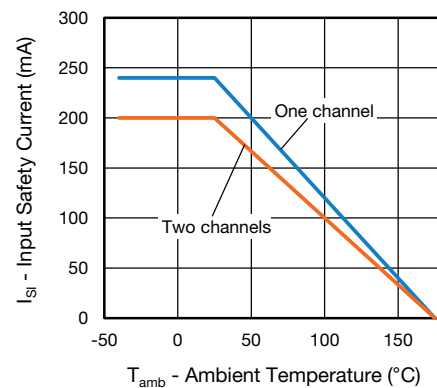


Fig. 3 - Input Safety Current vs. Ambient Temperature



## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

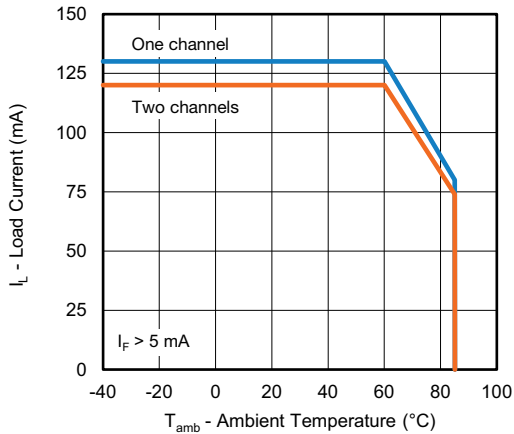


Fig. 4 - Load Current vs. Ambient Temperature

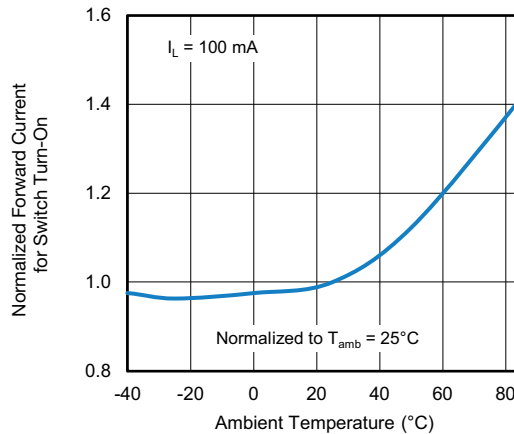


Fig. 7 - Normalized Forward Current for Switch Turn-On vs. Ambient Temperature

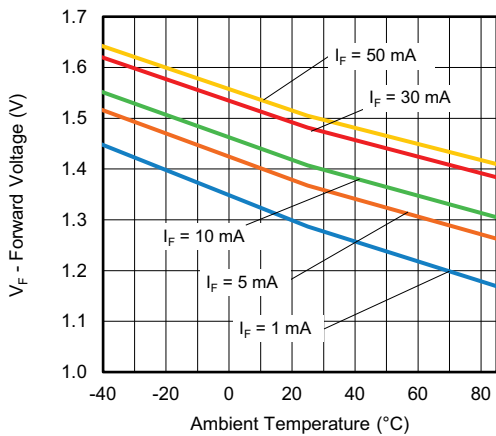


Fig. 5 - Forward Voltage vs. Ambient Temperature

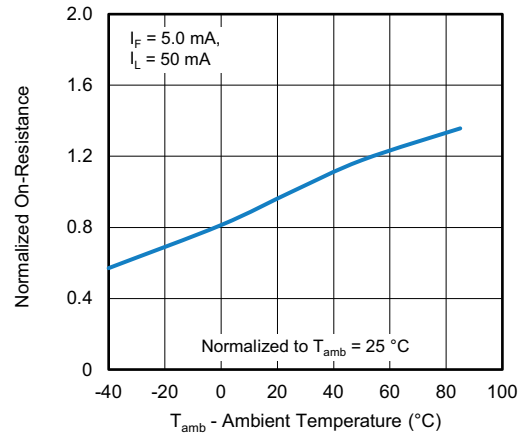


Fig. 8 - Normalized On-Resistance vs. Ambient Temperature

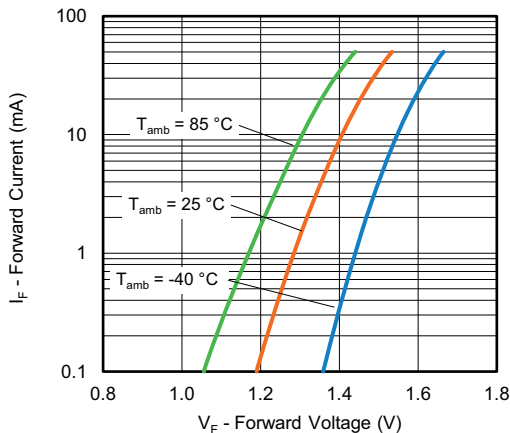


Fig. 6 - Forward Current vs. Forward Voltage

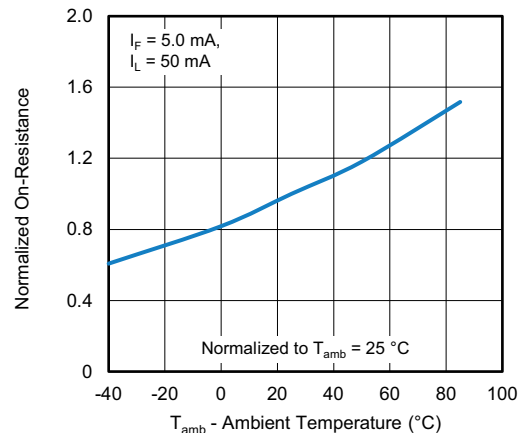


Fig. 9 - Normalized On-Resistance vs. Ambient Temperature

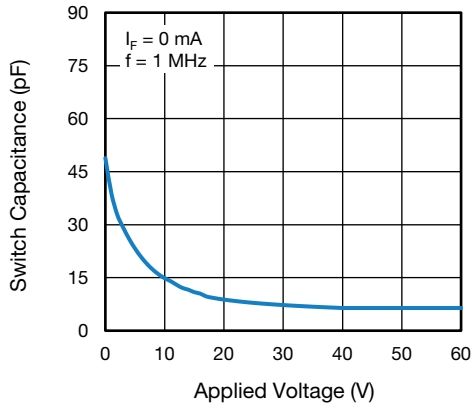


Fig. 10 - Switch Capacitance vs. Load Voltage

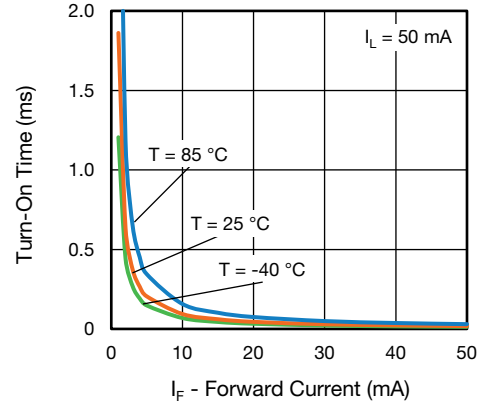


Fig. 13 - Turn-On Time vs. Forward Current

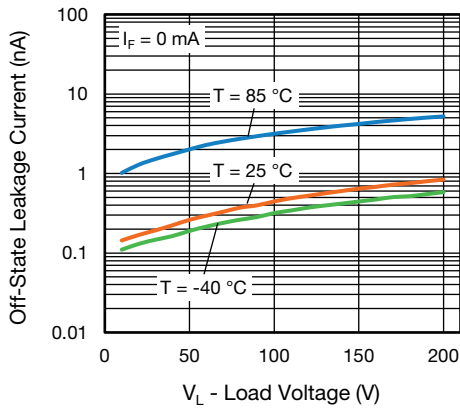


Fig. 11 - Leakage Current vs. Load Voltage

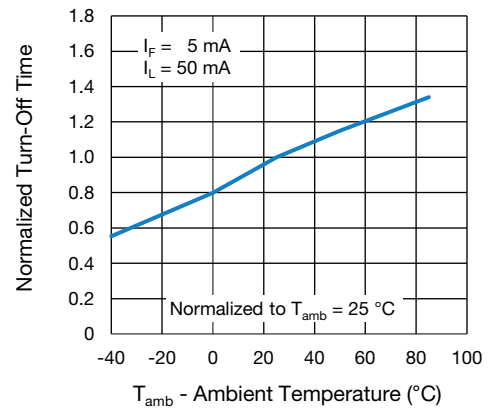


Fig. 14 - Normalized Turn-Off Time vs. Ambient Temperature

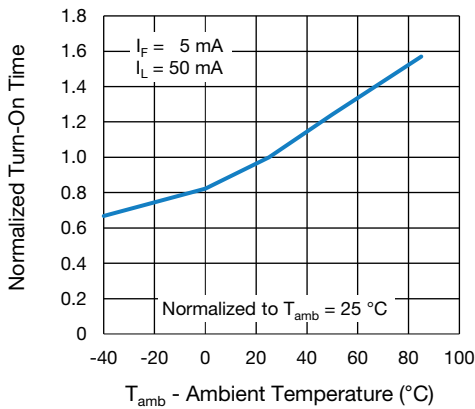


Fig. 12 - Normalized Turn-On Time vs. Ambient Temperature

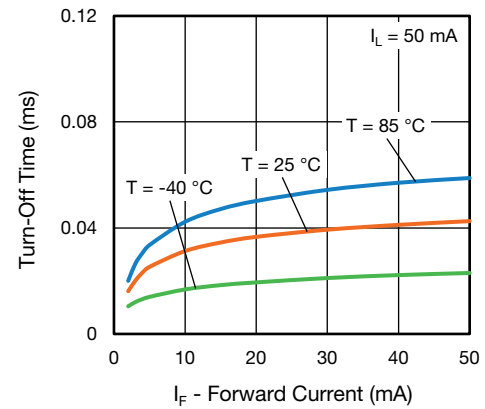
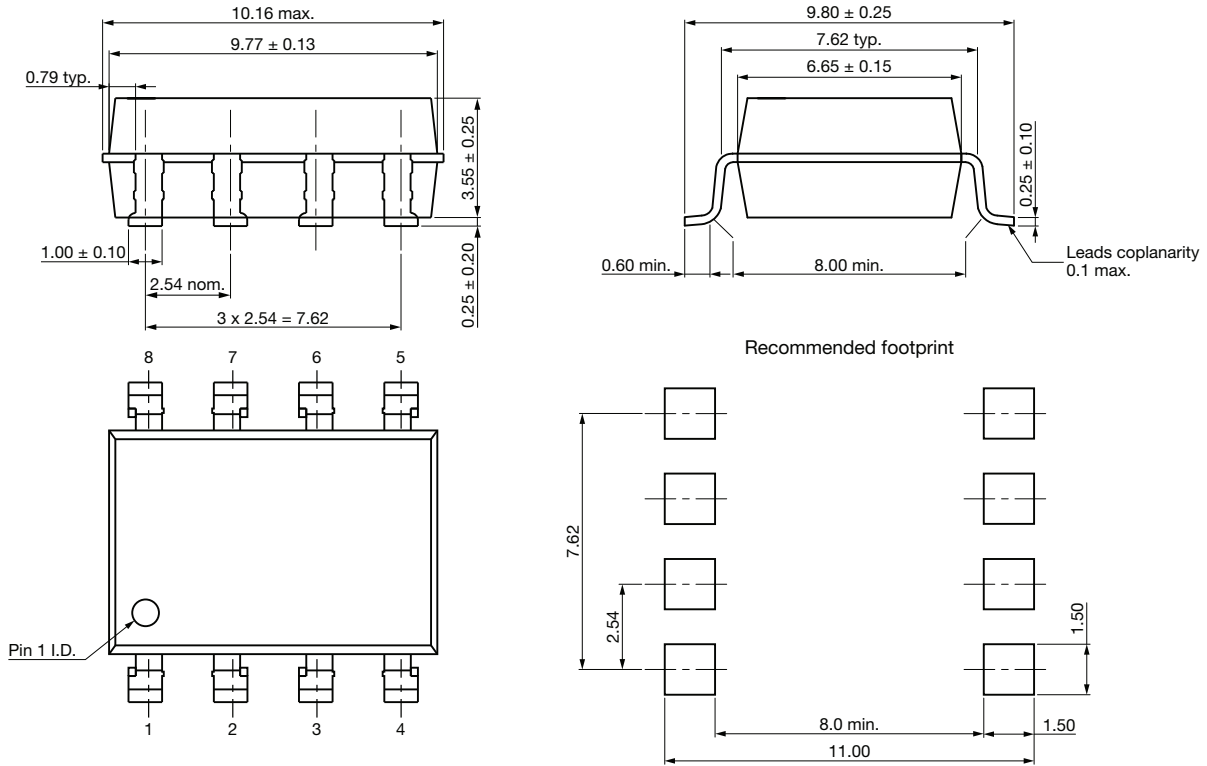


Fig. 15 - Turn-Off Time vs. Forward Current

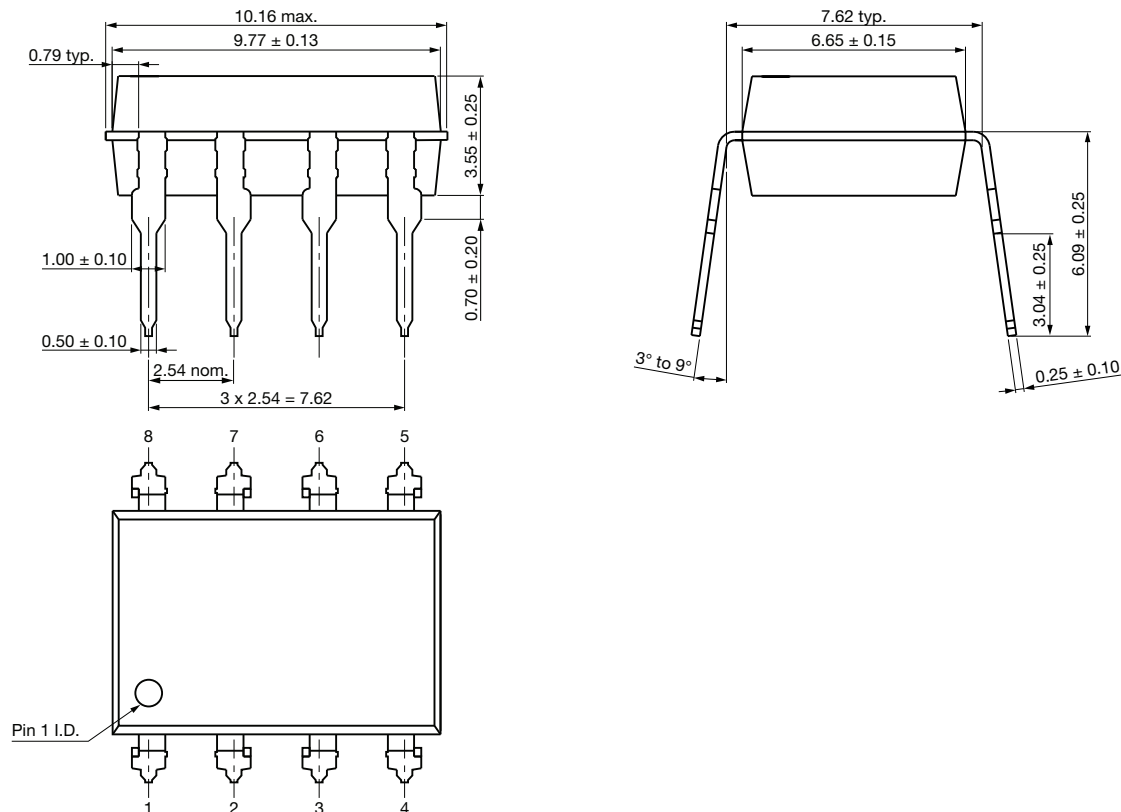


## PACKAGE DIMENSIONS in millimeters

### SMD-8



### DIP-8



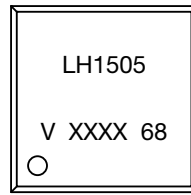
**PACKAGE MARKING** (example)


Fig. 16 - LH1505

**Notes**

- XXXX = LMC (lot marking code)
- Tape and reel suffix (TR) is not part of the package marking

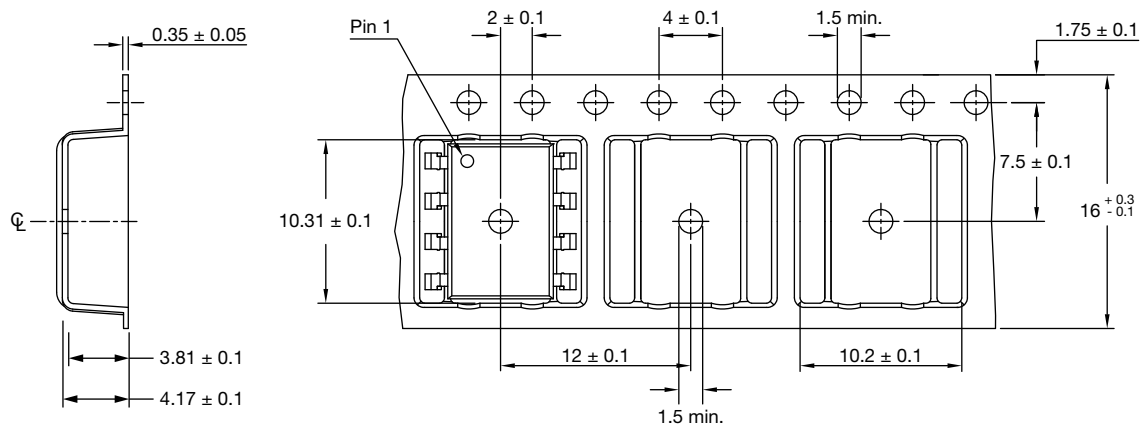
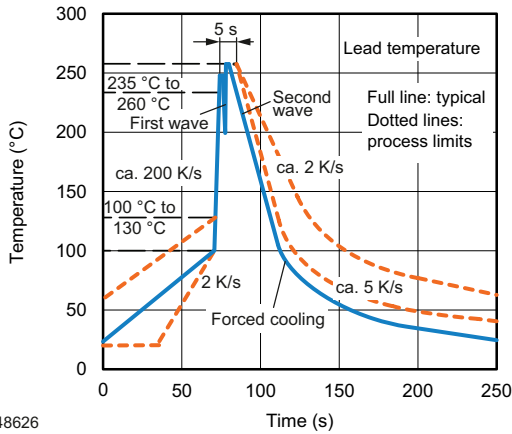
**PACKING INFORMATION** (in millimeters)


Fig. 17 - Tape and Reel Packing

TAPE AND REEL PACKING	
TYPE	UNITS/REEL
SMD-8	1000

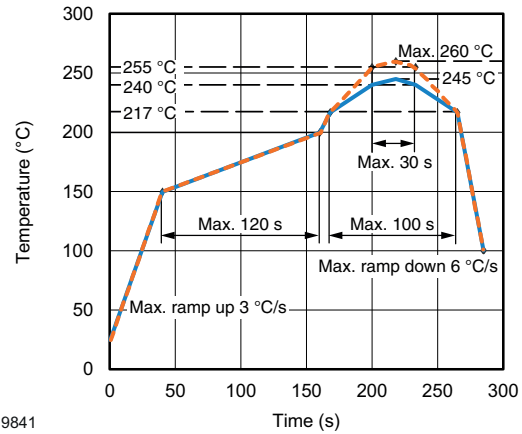
TUBE PACKING			
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX
SMD-8	50	40	2000
DIP-8	50	40	2000

**SOLDER PROFILES**



948626

Fig. 18 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices



19841

Fig. 19 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

**HANDLING AND STORAGE CONDITIONS**

ESD level: HBM class 2

Floor life: unlimited

Conditions:  $T_{amb} < 30\text{ °C}$ , RH < 60 %

Moisture sensitivity level 1, according to J-STD-020





## Footprint and Schematic Information for LH1505AAC, LH1505AACTR, LH1505AB

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC
LH1505AAC	<a href="http://www.snapeda.com/parts/LH1505AAC/Vishay/view-part">www.snapeda.com/parts/LH1505AAC/Vishay/view-part</a>
LH1505AACTR	<a href="http://www.snapeda.com/parts/LH1505AACTR/Vishay/view-part">www.snapeda.com/parts/LH1505AACTR/Vishay/view-part</a>
LH1505AB	<a href="http://www.snapeda.com/parts/LH1505AB/Vishay/view-part">www.snapeda.com/parts/LH1505AB/Vishay/view-part</a>

For technical issues and product support, please contact [optocoupleranswers@vishay.com](mailto:optocoupleranswers@vishay.com).

DIP



SMD



i179034\_2



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