VSMB10940



Vishay Semiconductors

High Speed Infrared Emitting Diode, 940 nm, GaAIAs, MQW



DESCRIPTION

VSMB10940 is an infrared, 940 nm side looking emitting diode in GaAlAs multi guantum well (MQW) technology with high radiant power and high speed, molded in clear, untinted plastic package (with lens) for surface mounting (SMD).

FEATURES

- Package type: Surface mount
- · Package form: Side view
- Dimensions (L x W x H in mm): 3 x 2 x 1
- Peak wavelength: $\lambda_p = 940 \text{ nm}$
- High reliability
- · High radiant power
- · High radiant intensity
- High speed
- Angle of half sensitivity: $\phi = \pm 75^{\circ}$
- · Low forward voltage
- Package matches with detector VEMD10940F
- Floor life: 168 h, MSL 3, acc. J-STD-020
- · Lead (Pb)-free reflow soldering
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- IR touch panel
- High power emitter for low space applications
- · High performance transmissive or reflective sensors

PRODUCT SUMMARY					
COMPONENT	l _e (mW/sr), 20 mA	φ (deg)	λ _p (nm)	t _r (ns)	
VSMB10940	1	± 75	940	15	

Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMB10940	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	side view	

Note

· MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	5	V	
Forward current		I _F	65	mA	
Peak forward current	$t_p/T = 0.5, t_p = 100 \ \mu s$	I _{FM}	130	mA	
Surge forward current	t _p = 100 μs	I _{FSM}	500	mA	
Power dissipation		Pv	104	mW	
Junction temperature		Тj	100	°C	
Operating temperature range		T _{amb}	- 40 to + 85	°C	
Storage temperature range		T _{stg}	- 40 to + 100	°C	
Soldering temperature	according to fig. 9, J-STD-020	T _{sd}	260	°C	
Thermal resistance junction/ambient	J-STD-051, leads 7 mm, soldered on PCB	R _{thJA}	450	K/W	

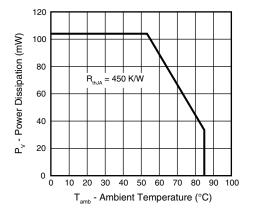
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RoHS COMPLIANT HALOGEN FREE GREEN (5-2008)



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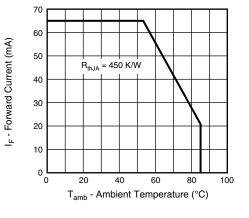


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _a PARAMETER	TEST CONDITION	SYMBOL	, MIN.	TYP.	MAX.	UNIT
	$I_F = 20 \text{ mA}, t_p = 20 \text{ ms}$	V _F	1.1	1.3	1.5	V
Forward voltage	$I_F = 65 \text{ mA}, t_p = 20 \text{ ms}$	V _F		1.35		V
5	I _F = 500 mA, t _p = 100 μs	V _F		1.8		V
Temperature coefficient of V _F	I _F = 1 mA	TK _{VF}		- 1.5		mV/K
Reverse current	V _R = 5 V	I _R			10	μA
Junction capacitance	$V_R = 0 V$, f = 1 MHz, E = 0 mW/cm ²	CJ		21		pF
	$I_{\rm F} = 20 \text{ mA}, t_{\rm p} = 20 \text{ ms}$	l _e	0.5	1	1.5	mW/sr
Radiant intensity	I _F = 65 mA, t _p = 20 ms	l _e		3.05		mW/sr
	I _F = 500 mA, t _p = 100 μs	l _e		13		mW/sr
Radiant power	I _F = 100 mA, t _p = 20 ms	фе		35		mW
Temperature coefficient of radiant power	I _F = 100 mA	ΤΚφ _e		- 0.47		%/K
Angle of half intensity - horizontal		Φh		± 77.5		deg
Angle of half intensity - vertical		φν		± 72.5		deg
Peak wavelength	I _F = 30 mA	λρ		940		nm
Spectral bandwidth	I _F = 30 mA	Δλ		25		nm
Temperature coefficient of λ_p	I _F = 30 mA	TK _{λp}		0.3		nm
Rise time	I _F = 100 mA, 20 % to 80 %	tr		15		ns
Fall time	I _F = 100 mA, 20 % to 80 %	t _f		15		ns

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BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

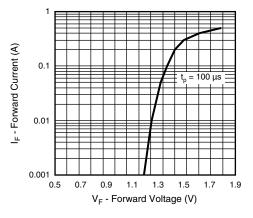


Fig. 3 - Forward Current vs. Forward Voltage

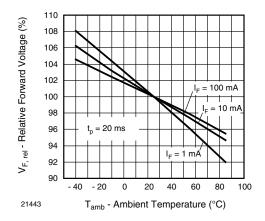


Fig. 4 - Relative Forward Voltage vs. Ambient Temperature

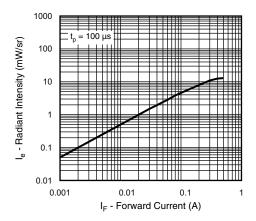


Fig. 5 - Radiant Intensity vs. Forward Current

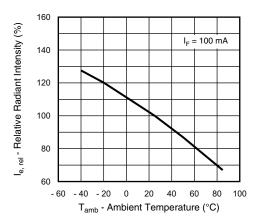


Fig. 6 - Relative Radiant Intensity vs. Ambient Temperature

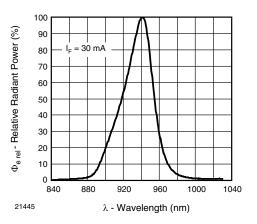


Fig. 7 - Relative Radiant Power vs. Wavelength

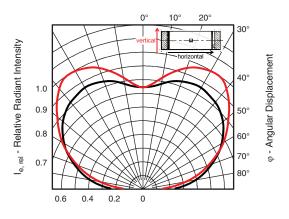


Fig. 8 - Relative Radiant Intensity vs. Angular Displacement

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REFLOW SOLDER PROFILE

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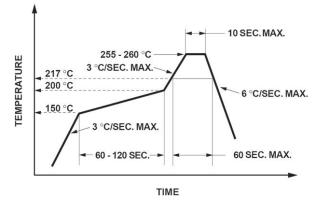


Fig. 9 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

PACKAGE DIMENSIONS in millimeters

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

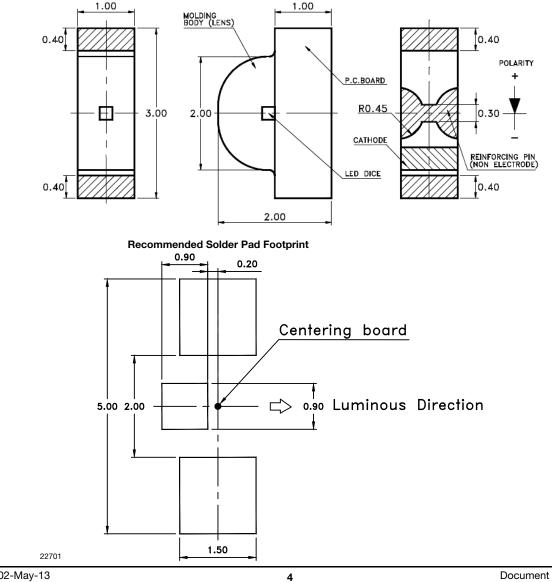
Moisture sensitivity: level 3

Floor life: 168 h

Conditions: $T_{amb} < 30$ °C, RH < 60 %

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.



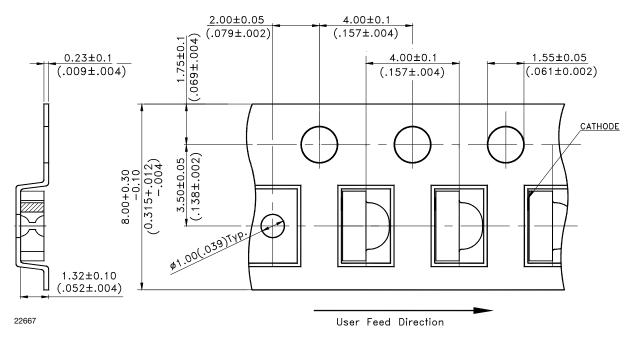
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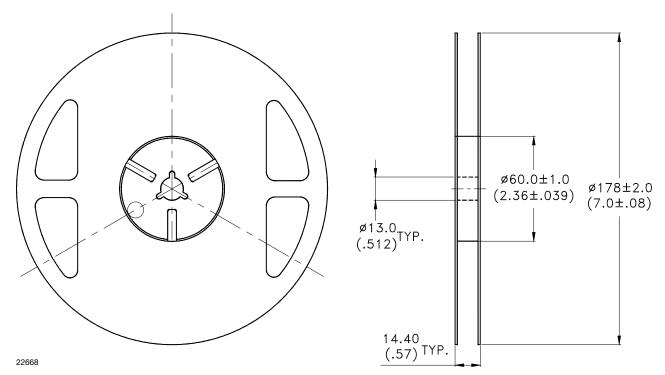
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BLISTER TAPE DIMENSIONS in millimeters







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