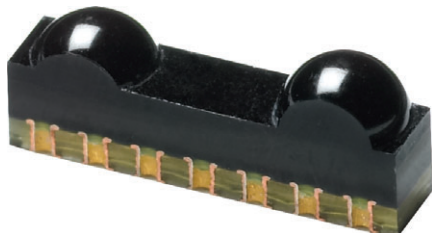




# Infrared Transceiver, 9.6 kbit/s to 115.2 kbit/s (SIR) for IrDA® Applications



20206-1

## DESCRIPTION

TFBS4652 is an infrared transceiver that supports data rates up to 115 kbit/s per the IrDA standard. The link distance is up to 1 meter. The transceiver includes a PIN photodiode, an infrared emitter, and a low power control IC. These components have not been qualified according to automotive specifications.

## FEATURES

- Compliant to the IrDA physical layer specification
- Standard IrDA link distance of 1 m
- Low power consumption, typically less than 70  $\mu$ A
- Less than 1  $\mu$ A in shutdown mode
- Adjustable to logic I/O voltage swing from 1.5 V to 5.5 V
- Material categorization:  
for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
**GREEN**  
(5-2008)

## APPLICATIONS

- Short-distance wireless communication and data transfer
- Use in environments where RF is problematic

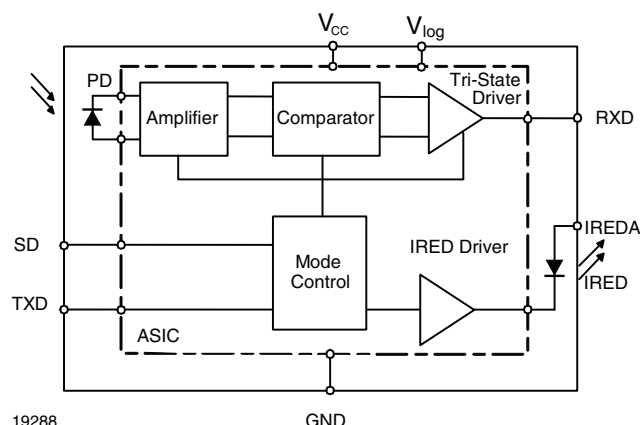
## LINKS TO ADDITIONAL RESOURCES



## DESIGN SUPPORT TOOLS

- [3D model](#)
- [Window size calculator](#)
- [Symbols and terminology](#)
- [IRDC protocol](#)
- [Reference layouts and circuit diagrams](#)

## FUNCTIONAL BLOCK DIAGRAM





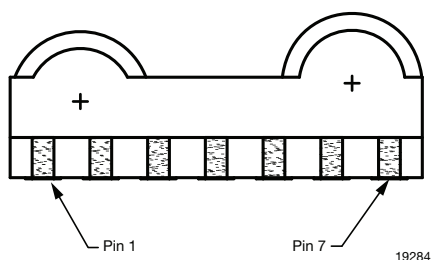
PRODUCT SUMMARY					
PART NUMBER	DATA RATE (kbit/s)	DIMENSIONS H x L x W (mm x mm x mm)	LINK DISTANCE (m)	OPERATING VOLTAGE (V)	IDLE SUPPLY CURRENT (mA)
TFBS4652	115.2	1.6 x 6.8 x 2.8	0 to ≥ 1	2.4 to 5.5	0.07

PARTS TABLE		
PART	DESCRIPTION	QTY/REEL
TFBS4652-TR1	Oriented in carrier tape for side view surface mounting	1000 pcs
TFBS4652-TR3	Oriented in carrier tape for side view surface mounting	2500 pcs
TFBS4652-TT1	Oriented in carrier tape for top view surface mounting	1000 pcs

PIN DESCRIPTION				
PIN NUMBER	SYMBOL	DESCRIPTION	I/O	ACTIVE
1	IREDA	IREDA anode, connected via a current limiting resistor to V <sub>CC2</sub> . A separate unregulated power supply can be used.		
2	RXD	Receiver output. Normally high, goes low for a defined pulse duration with the rising edge of the optical input signal. Output is a CMOS tri-state driver, which swings between ground and V <sub>logic</sub> . Receiver echoes transmitter output.	O	Low
3	TXD	Transmitter data input. Setting this input above the threshold turns on the transmitter. This input switches the IRED with the maximum transmit pulse width of about 100 μs.	I	High
4	SD	Shutdown. Logic low at this input enables the receiver, enables the transmitter, and un-tri-states the receiver output. It must be driven high for shutting down the transceiver.	I	High
5	V <sub>logic</sub>	Reference for the logic swing of the output and the input logic levels.	I	
6	V <sub>CC</sub>	Power supply, 2.4 V to 5.5 V. This pin provides power for the receiver and transmitter drive section. Connect V <sub>CC1</sub> via an optional filter.		
7	GND	Ground		

**PINOUT**

TFBS4652, bottom view  
weight 0.04 g





ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage range, transceiver	-0.3 V < V <sub>CC2</sub> < 6 V	V <sub>CC1</sub>	-0.5	-	6	V
Supply voltage range, transmitter	-0.5 V < V <sub>CC1</sub> < 6 V	V <sub>CC2</sub>	-0.5	-	6	V
Supply voltage range, digital supply	-0.5 V < V <sub>CC1</sub> < 6 V	V <sub>logic</sub>	-0.5	-	6	V
Voltage at RXD	All states	V <sub>IN</sub>	-0.5	-	V <sub>logic</sub> + 0.5	V
Input voltage range, transmitter TXD	Independent of V <sub>dd</sub> or V <sub>logic</sub>	V <sub>IN</sub>	-0.5	-	6	V
Input currents	For all pins, except IRED anode pin	I <sub>CC</sub>	-	-	10	mA
Output sinking current			-	-	25	mA
Power dissipation		P <sub>D</sub>	-	-	250	mW
Junction temperature		T <sub>J</sub>	-	-	125	°C
Ambient temperature range (operating)		T <sub>amb</sub>	-25	-	+85	°C
Storage temperature		T <sub>stg</sub>	-25	-	+85	°C
Soldering temperature	See section "Recommended Solder Profile"		-	-	260	°C
Repetitive pulse output current	< 90 μs, t <sub>on</sub> < 20 %	I <sub>IRED</sub> (RP)	-	-	430	mA
Average output current (transmitter)		I <sub>IRED</sub> (DC)	-	-	85	mA
ESD protection	JESD22-A114	V <sub>ESD</sub>	2	-	-	kV
Latchup	JESD78		± 100	-	-	mA
Thermal resistance junction to ambient	JESD51	R <sub>thJA</sub>	-	300	-	K/W

**Note**

- Reference point ground unless otherwise noted. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing

EYE SAFETY INFORMATION	
STANDARD	CLASSIFICATION
IEC/EN 60825-1 (2007-03), DIN EN 60825-1 (2008-05) "SAFETY OF LASER PRODUCTS - Part 1: equipment classification and requirements", simplified method	Class 1
IEC 62471 (2006), CIE S009 (2002) "Photobiological Safety of Lamps and Lamp Systems"	Exempt
DIRECTIVE 2006/25/EC OF THE EUROPEAN PARLIAMENT AND OF THE COUNCIL of 5 <sup>th</sup> April 2006 on the minimum health and safety requirements regarding the exposure of workers to risks arising from physical agents (artificial optical radiation) (19 <sup>th</sup> individual directive within the meaning of article 16(1) of directive 89/391/EEC)	Exempt

**Note**

- Vishay transceivers operating inside the absolute maximum ratings are classified as eye safe according the above table



<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , $V_{CC} = 2.4\text{ V to } 5.5\text{ V}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>TRANSCEIVER</b>						
Supply voltage range		$V_{CC}$	2.4	-	5.5	V
Dynamic supply current						
Idle supply current at $V_{CC1}$ (receive mode, no signal)	SD = low, $T_{amb} = -25\text{ }^{\circ}\text{C to } +85\text{ }^{\circ}\text{C}$ independent of ambient light, $V_{CC1} = V_{CC2} = 2.4\text{ V to } 5.5\text{ V}$	$I_{CC1}$	40	70	110	$\mu\text{A}$
Peak supply current during transmission	SD = low, TXD = high	$I_{ccpk}$	-	2	3	mA
Idle, dark ambient at $V_{logic}$ - pin	SD = low ( $< 0.8\text{ V}$ ), $E_{eamb} = 0\text{ klx}$ , $E_e < 4\text{ mW/m}^2$	$I_{logic}$	-	-	1	$\mu\text{A}$
Shutdown supply current, dark ambient	SD = high ( $> V_{logic} - 0.5\text{ V}$ ), $T = 25\text{ }^{\circ}\text{C}$ , $E_e = 0\text{ klx}$	$I_{SD}$	-	-	0.1	$\mu\text{A}$
	SD = high ( $> V_{logic} - 0.5\text{ V}$ ), $T = 85\text{ }^{\circ}\text{C}$ , $E_e = 0\text{ klx}$	$I_{SD}$	-	-	1	$\mu\text{A}$
Output voltage low	$I_{OL} = 0.2\text{ mA}$ , $V_{CC} = 2.4\text{ V}$ , $C_{load} = 15\text{ pF}$	$V_{OL}$	-	-	$0.15 \times V_{CC1}$	V
Output voltage high	$I_{OL} = 0.2\text{ mA}$ , $V_{CC} = 2.4\text{ V}$ , $C_{load} = 15\text{ pF}$	$V_{OH}$	$0.8 \times V_{logic}$	-	-	V
RXD to $V_{logic}$ pull-up impedance	SD = high ( $> V_{logic} - 0.5\text{ V}$ )	$R_{RXD}$	-	500	-	$\text{k}\Omega$
Input voltage low (TXD, SD)		$V_{ILo}$	-0.3	-	0.4	V
Input voltage high (TXD, SD)	$V_{CC} = 2.4\text{ V to } 5.5\text{ V}$	$V_{IHl}$	$V_{logic} - 0.5$	-	6	V
Input voltage threshold SD	$V_{CC} = 2.4\text{ V to } 5.5\text{ V}$		0.9	$0.5 \times V_{logic}$	$0.66 \times V_{logic}$	V
Input capacitance (TXD, SD)		$C_I$	-	-	6	pF

**Note**

- Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing



OPTOELECTRONIC CHARACTERISTICS						
PARAMETER	TEST CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>RECEIVER</b>						
Sensitivity: minimum irradiance $E_e$ in angular range <sup>(1)</sup>	9.6 kbit/s to 115.2 kbit/s $\lambda = 850 \text{ nm to } 900 \text{ nm}$	$E_e$	-	40	81	mW/m <sup>2</sup>
Maximum irradiance $E_e$ in angular range <sup>(2)</sup>	$\lambda = 850 \text{ nm to } 900 \text{ nm}$	$E_e$	2	-	-	kW/m <sup>2</sup>
No output receiver input irradiance <sup>(3)</sup>	According to IrDA IrPHY 1.4, appendix A1, fluorescent light specification	$E_e$	4	-	-	mW/m <sup>2</sup>
Rise time of output signal	10 % to 90 %, $C_L = 15 \text{ pF}$	$t_{r(RXD)}$	20	-	100	ns
Fall time of output signal	90 % to 10 %, $C_L = 15 \text{ pF}$	$t_{f(RXD)}$	20	-	100	ns
RXD pulse width of output signal, 50 % <sup>(4)</sup>	Input pulse width 1.63 $\mu\text{s}$	$t_{PW}$	1.7	-	2.9	$\mu\text{s}$
Receiver start up time	Power on delay		-	100	150	$\mu\text{s}$
Latency		$t_L$	30	50	100	$\mu\text{s}$
<b>TRANSMITTER</b>						
IRED operating current, current controlled	The IRED current is internally controlled but also can be reduced by an external resistor $R_1$ <sup>(5)</sup>	$I_D$	200	300	430	mA
Forward voltage of built-in IRED	$I_F = 300 \text{ mA}$	$V_F$	1.4	1.8	1.9	V
Output leakage IRED current	TXD = 0 V, $0 < V_{CC1} < 5.5 \text{ V}$	$I_{IRED}$	-1	0.01	1	$\mu\text{A}$
Output radiant intensity <sup>(5)</sup>	$a = 0^\circ, 15^\circ$ , TXD = high, SD = low, $R_1 = 0 \Omega$	$I_e$	40	250	400	mW/sr
Output radiant intensity	$V_{CC1} = 5 \text{ V}$ , $a = 0^\circ, 15^\circ$ TXD = low or SD = high (receiver is inactive as long as SD = high)	$I_e$	-	-	0.04	mW/sr
Saturation voltage of IRED driver	$V_{CC} = 3 \text{ V}$ , $I_F = 50 \text{ mA}$	$V_{CEsat}$	-	0.4	-	V
Peak - emission wavelength		$\lambda_p$	870	-	910	nm
Spectral bandwidth		$\Delta\lambda$	-	45	-	nm
Optical rise time, optical fall time		$t_{ropt}$ , $t_{fopt}$	10	50	300	ns
Optical output pulse duration	Input pulse width $1.6 < t_{TXD} < 23 \mu\text{s}$	$t_{opt}$	$t_{TXD} - 0.15$	-	$t_{TXD} + 0.15$	$\mu\text{s}$
	Input pulse width $t_{TXD} \geq 23 \mu\text{s}$	$t_{opt}$	23	50	100	$\mu\text{s}$
Optical overshoot			-	-	25	%

**Notes**

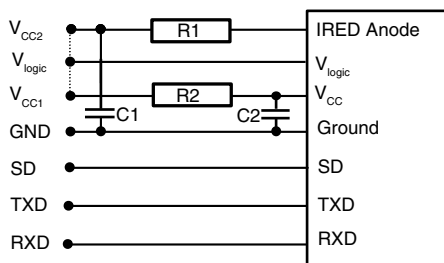
- $T_{amb} = 25 \text{ }^\circ\text{C}$ ,  $V_{CC} = 2.4 \text{ V to } 5.5 \text{ V}$  unless otherwise noted. Typical values are for design aid only, not guaranteed nor subject to production testing
- (1) IrDA sensitivity definition: minimum irradiance  $E_e$  in angular range, power per unit area. The receiver must meet the BER specification while the source is operating at the minimum intensity in angular range into the minimum half-angular range at the maximum link length
- (2) Maximum irradiance  $E_e$  in angular range, power per unit area. The optical delivered to the detector by a source operating at the maximum intensity in angular range at minimum link length must not cause receiver overdrive distortion and possible related link errors. If placed at the active output interface reference plane of the transmitter, the receiver must meet its bit error ratio (BER) specification. For more definitions see the document "Symbols and Terminology" on the Vishay website
- (3) This parameter reflects the backlight test of the IrDA physical layer specification to guarantee immunity against light from fluorescent lamps
- (4) RXD output is edge triggered by the rising edge of the optical input signal. The output pulse duration is independent of the input pulse duration
- (5) The radiant intensity can be adjusted by the external current limiting resistor to adapt the intensity to the desired value. The given value is for maximum link distance operating the transceiver without current control resistor ( $R_1 = 0 \Omega$ ) and using the internal current control



TRUTH TABLE				
INPUTS			OUTPUTS	
SD	TXD	OPTICAL INPUT IRRADIANCE mW/m <sup>2</sup>	RXD	TRANSMITTER
High	x	x	Tri-state floating with a weak pull-up to the supply voltage	0
Low	High	x	Low (echo on)	I <sub>e</sub>
Low	High > 100 μs	x	High	0
Low	Low	< 2	High	0
Low	Low	> min. irradiance E <sub>e</sub> < max. irradiance E <sub>e</sub>	Low (active)	0
Low	Low	> max. irradiance E <sub>e</sub>	x	0

**RECOMMENDED CIRCUIT DIAGRAM**

Operated at a clean low impedance power supply the TFBS4652 needs only one additional external component when the IRED drive current should be minimized for minimum current consumption according the low power IrDA standard. When combined operation in IrDA and remote control is intended no current limiting resistor is recommended. When long wires are used for bench tests, the capacitors are mandatory for testing rise/fall time correctly.



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Fig. 1 - Recommended Application Circuit

The capacitor C1 is buffering the supply voltage V<sub>CC2</sub> and eliminates the inductance of the power supply line. This one should be a small ceramic version or other fast capacitor to guarantee the fast rise time of the IRED current. The resistor R1 is necessary for controlling the IRED drive current when the internally controlled current is too high for the application.

Vishay transceivers integrate a sensitive receiver and a built-in power driver. The combination of both needs a careful circuit board layout. The use of thin, long, resistive and inductive wiring should be avoided. The inputs (TXD, SD) and the output RXD should be directly (DC) coupled to the I/O circuit.

The capacitor C2 combined with the resistor R2 is the low pass filter for smoothing the supply voltage.

As already stated above R2, C1 and C2 are optional and depend on the quality of the supply voltages V<sub>CCx</sub> and injected noise. An unstable power supply with dropping voltage during transmission may reduce the sensitivity (and transmission range) of the transceiver.

The placement of these parts is critical. It is strongly recommended to position C2 as close as possible to the transceiver power supply pins.

When extended wiring is used as in bench tests the inductance of the power supply can cause dynamically a voltage drop at V<sub>CC2</sub>. Often some power supplies are not able to follow the fast current rise time. In that case another 4.7 μF (type, see table under C1) at V<sub>CC2</sub> will be helpful.

Under extreme EMI conditions as placing an RF-transmitter antenna on top of the transceiver, we recommend to protect all inputs by a low-pass filter, as a minimum a 12 pF capacitor, especially at the RXD port.

RECOMMENDED APPLICATION CIRCUIT COMPONENTS	
COMPONENT	RECOMMENDED VALUE
C1, C2	0.1 μF, ceramic Vishay part# VJ 1206 Y 104 J XXMT
R1	See table below
R2	47 Ω, 0.125 W (V <sub>CC1</sub> = 3 V)

**I/O AND SOFTWARE**

In the description, already different I/Os are mentioned. Different combinations are tested and the function verified with the special drivers available from the I/O suppliers. In special cases refer to the I/O manual, the Vishay application notes, or contact directly Vishay Sales, Marketing or Application.

For operating at RS232 ports we recommend to use an encoder / decoder-module.

**Note**

- TFBS4652 echoes the TXD signal at the RXD output during transmission. For communication this signal is to be correctly ignored by the controller or the software. The echo signal is implemented for test purposes in mass production



**ASSEMBLY INSTRUCTIONS**

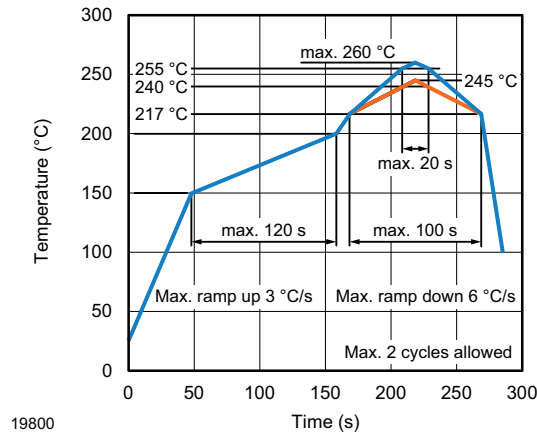
**Reflow Soldering**

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

**Manual Soldering**

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off

**VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE**





PACKAGE DIMENSIONS in millimeters

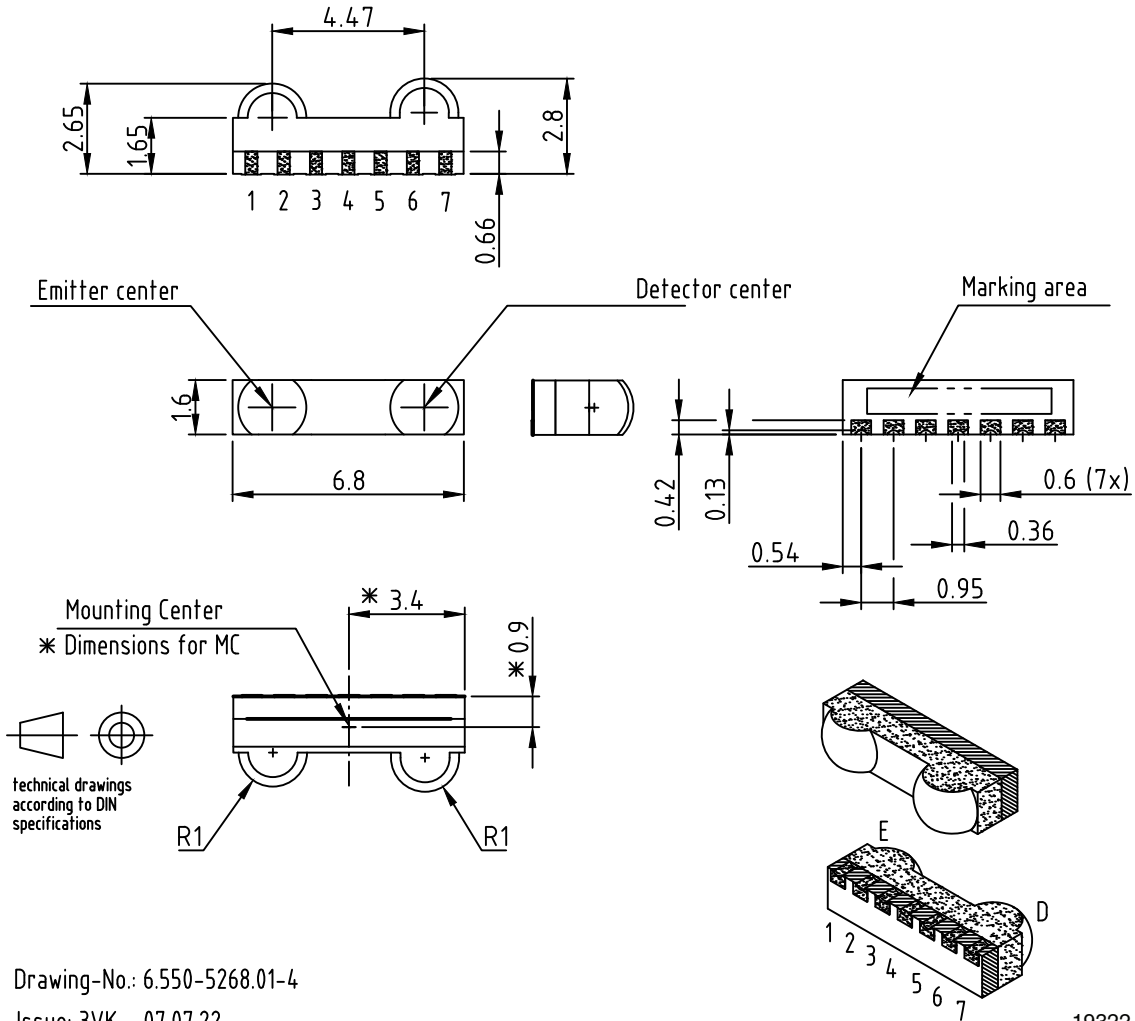


Fig. 2 - TFBS4652 Mechanical Dimensions, Tolerance  $\pm 0.2$  mm, if not Otherwise Mentioned

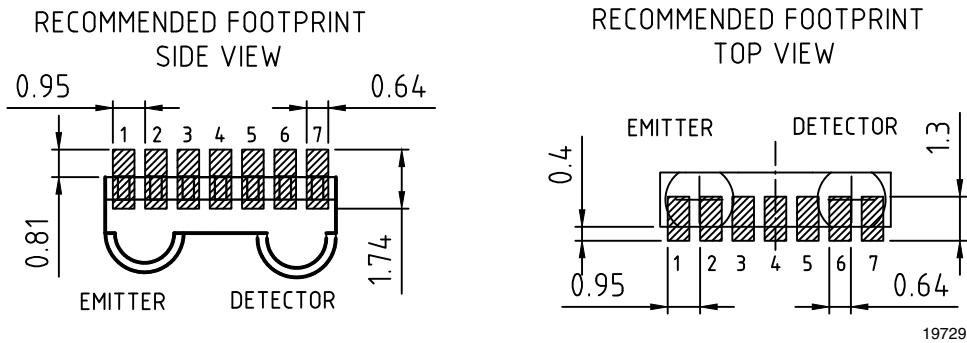
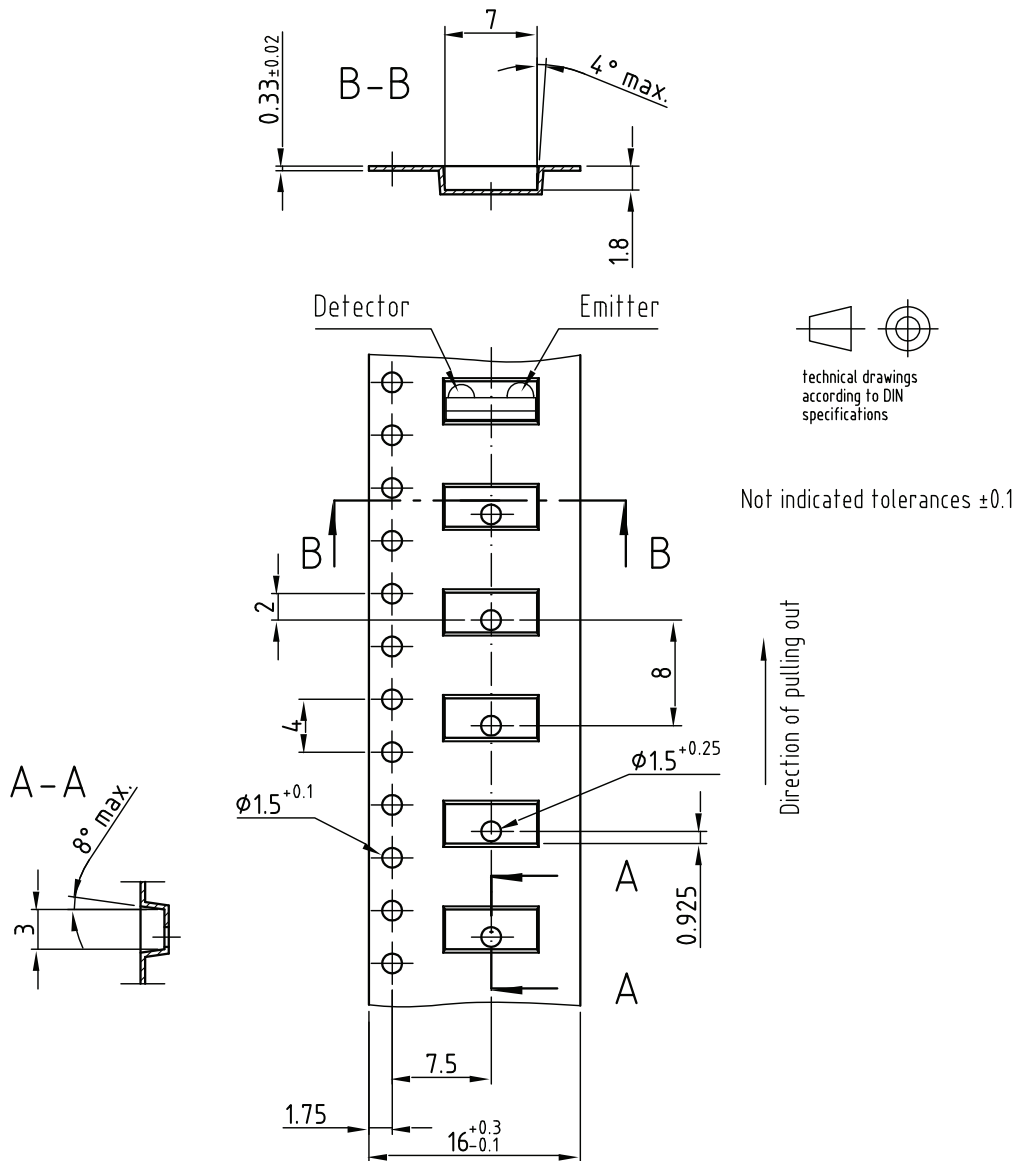


Fig. 3 - TFBS4652 Soldering Footprint, Tolerance  $\pm 0.2$  mm, if not Otherwise Mentioned



**TAPE DIMENSIONS FOR TR1 AND TR3** in millimeters

Tape for Side View Oriented Parts



Drawing-No.: 9.700-5296.01-4

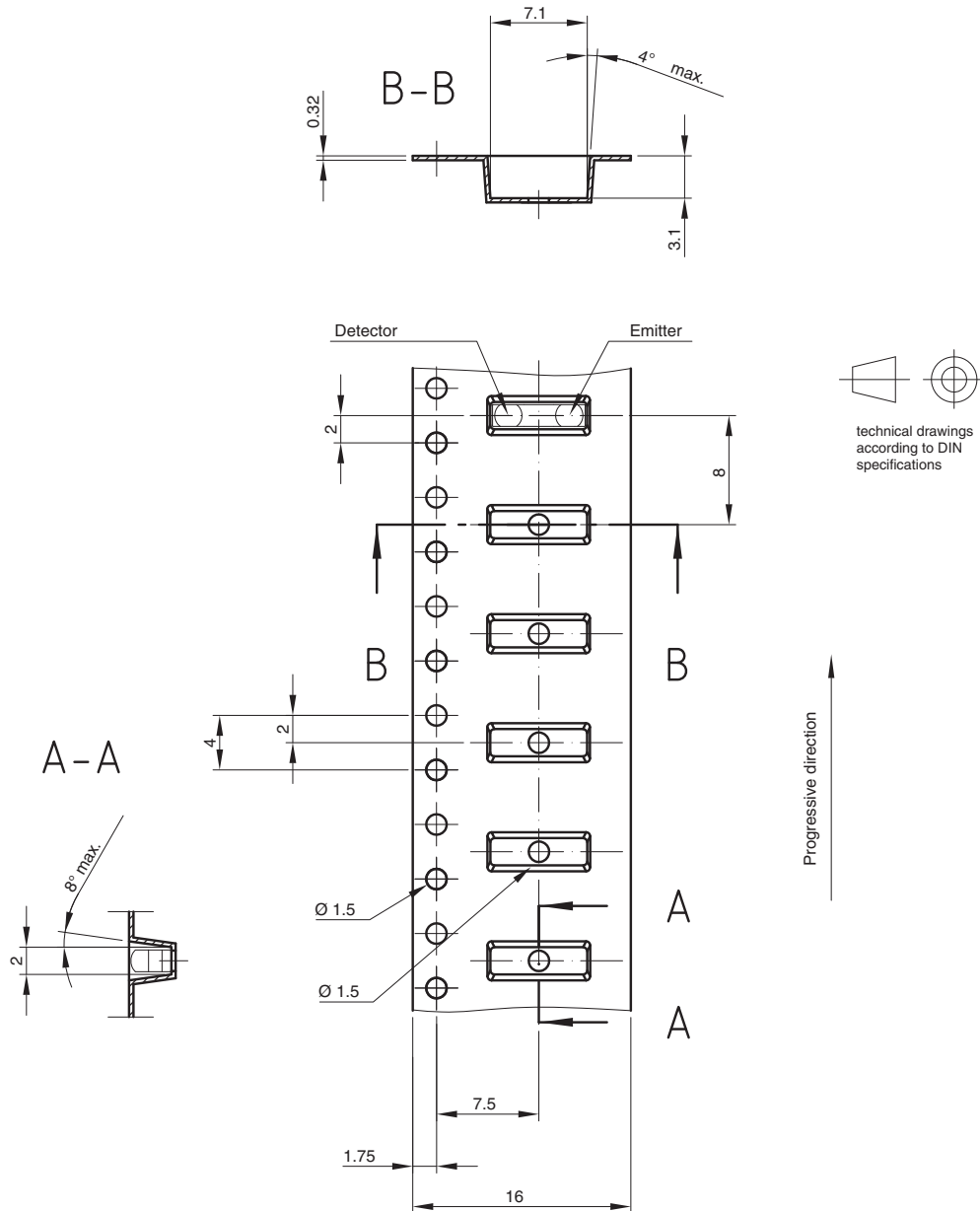
Issue: prel. copy; 24.11.04

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**TAPE DIMENSIONS FOR TT1** in millimeters

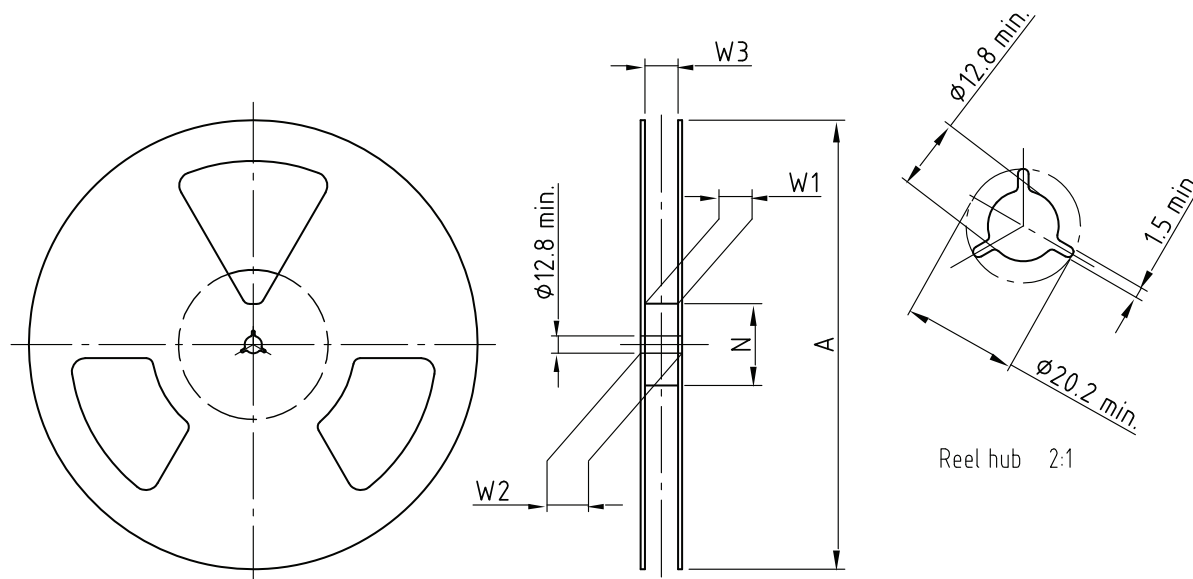
Tape for Top View Oriented Parts



Drawing-No.: 9.700-5340.01-4  
 Issue: 1; 15.01.09  
 21663



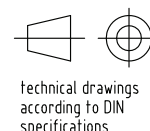
**REEL DIMENSIONS** in millimeters



Drawing-No.: 9.800-5090.01-4  
Issue: 1; 29.11.05  
14017

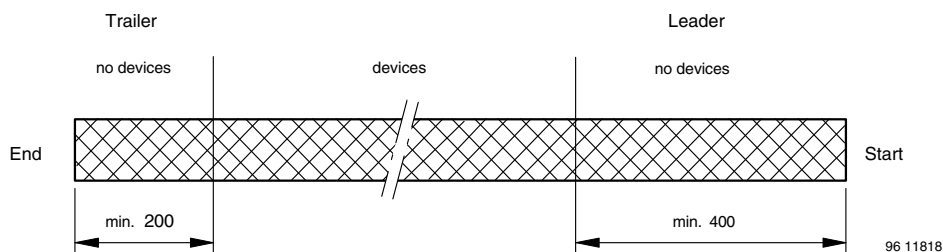
Form of the leave open of the wheel is supplier specific.

Dimension acc. to IEC EN 60 286-3



TAPING VARIANT	TAPE WIDTH (mm)	A MAX. (mm)	N (mm)	W <sub>1</sub> MIN. (mm)	W <sub>2</sub> MAX. (mm)	W <sub>3</sub> MIN. (mm)	W <sub>3</sub> MAX. (mm)
TT1 / TR1	16	180	60	16.4	22.4	15.9	19.4
TT3 / TR3	16	330	50	16.4	22.4	15.9	19.4

**LEADER AND TRAILER DIMENSIONS** in millimeters



**COVER TAPE PEEL STRENGTH**

According to DIN EN 60286-3  
0.1 N to 1.3 N  
300 ± 10 mm/min.  
165° to 180° peel angle

**LABEL**

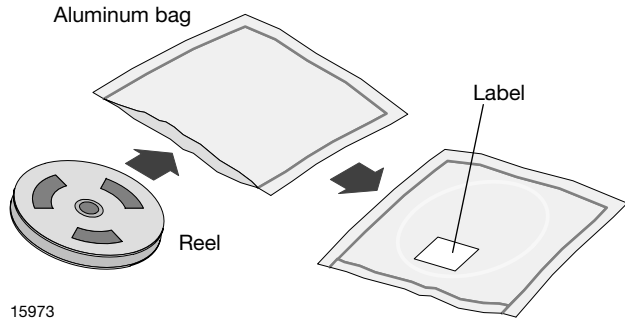
**Standard bar code labels for finished goods**

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.



**DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



**FINAL PACKING**

The sealed reel is packed into a cardboard box.

**RECOMMENDED METHOD OF STORAGE**

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

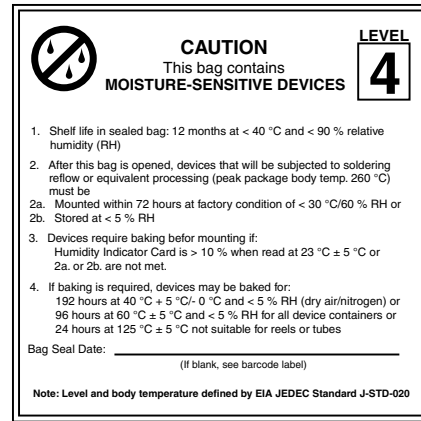
After more than 72 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC® standard J-STD-020 level 4 label is included on all dry bags.



EIA JEDEC standard J-STD-020 level 4 label is included on all dry bags

**OUTER PACKAGING**

The sealed reel is packed into a pizza box.

CARTON BOX DIMENSIONS in millimeters				
<p>Thickness</p> <p>Width</p> <p>Length</p> <p>22127</p>				
ORDER CODE	BOXING	THICKNESS	WIDTH	LENGTH
TT3 / TR3	Pizza box (taping in reels)	50	340	340
TT1 / TR1	Pizza box (taping in reels)	32	190	190



VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL (finished goods)		
PLAIN WRITING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxxx+	Company logo
Long bar code top	Type	Length
Item-number	N	8
Plant-code	N	2
Sequence-number	X	3
Quantity	N	8
Total length	-	21
Short bar code bottom	Type	Length
Selection-code	X	3
Data-code	N	3
Batch-number	X	10
Filter	-	1
Total length	-	17

**ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

**VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS**

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.





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