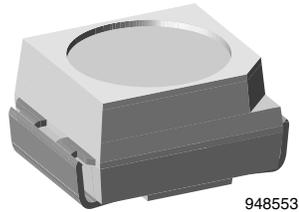




High Speed Infrared Emitting Diode, 890 nm, Surface Emitter Technology



FEATURES

- Package type: surface-mount
- Package form: PLCC-2
- Dimensions (L x W x H in mm): 3.5 x 2.8 x 1.75
- AEC-Q101 qualified
- Peak wavelength: $\lambda_p = 890$ nm
- High reliability
- High radiant intensity
- Angle of half intensity: $\phi = \pm 60^\circ$
- Suitable for high pulse current operation
- Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



DESCRIPTION

As part of the [SurfLight™](#) portfolio, the VSMY3890X01 is an infrared, 890 nm emitting diode based on surface emitter technology with high radiant intensity, high optical power and high speed, molded in a PLCC-2 package for surface mounting (SMD).

RELEASED FOR APPLICATIONS

- Infrared radiation source for operation with CMOS cameras (illumination)
- High speed IR data transmission
- Automotive sensors
- Light curtain

PRODUCT SUMMARY				
COMPONENT	I_e (mW/sr)	ϕ (°)	λ_p (nm)	t_r (ns)
VSMY3890X01	18	± 60	890	15

Note

- Test conditions see table “Basic Characteristics”

ORDERING INFORMATION			
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
VSMY3890X01-GS08	Tape and reel	MOQ: 7500 pcs, 1500 pcs/reel	PLCC-2
VSMY3890X01-GS18	Tape and reel	MOQ: 8000 pcs, 8000 pcs/reel	PLCC-2

Note

- MOQ: minimum order quantity



ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_R	5	V
Forward current		I_F	100	mA
Pulse peak forward current	$t_p/T = 0.5$, $t_p = 100\text{ }\mu\text{s}$	I_{FM}	200	mA
Surge forward current	$t_p = 100\text{ }\mu\text{s}$	I_{FSM}	1	A
Power dissipation		P_V	190	mW
Junction temperature		T_j	100	$^{\circ}\text{C}$
Operating temperature range		T_{amb}	-40 to +85	$^{\circ}\text{C}$
Storage temperature range		T_{stg}	-40 to +110	$^{\circ}\text{C}$
Soldering temperature	According to Fig. 7, J-STD-020	T_{sd}	260	$^{\circ}\text{C}$
Thermal resistance junction-to-ambient	J-STD-051, soldered on PCB	R_{thJA}	250	K/W

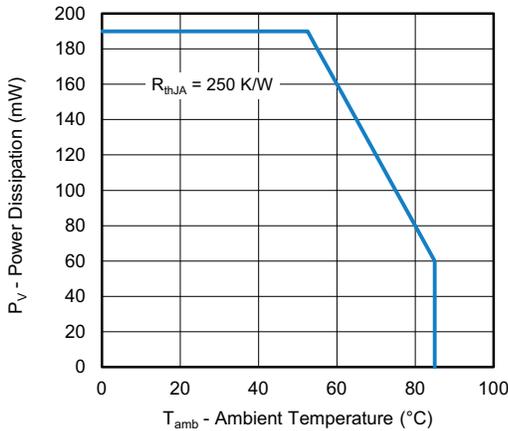


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

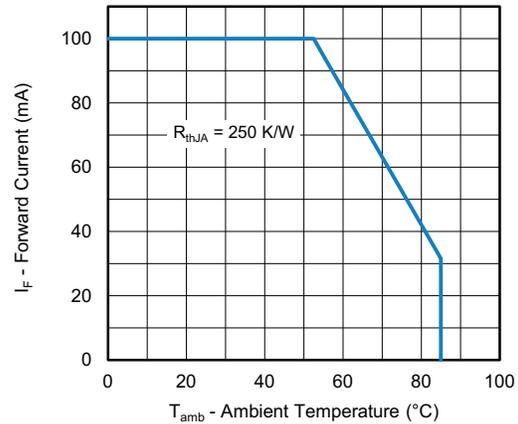


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 100\text{ mA}$, $t_p = 20\text{ ms}$	V_F	-	1.6	1.9	V
	$I_F = 1\text{ A}$, $t_p = 100\text{ }\mu\text{s}$	V_F	-	2.8	-	V
Temperature coefficient of V_F	$I_F = 1\text{ mA}$	TK_{V_F}	-	-2.0	-	mV/K
	$I_F = 100\text{ mA}$	TK_{V_F}	-	-2.0	-	mV/K
Reverse current		I_R	Not designed for reverse operation			μA
Junction capacitance	$V_R = 0\text{ V}$, $f = 1\text{ MHz}$, $E = 0$	C_j	-	60	-	pF
Radiant intensity	$I_F = 100\text{ mA}$, $t_p = 20\text{ ms}$	I_e	12	18	25	mW/sr
	$I_F = 1\text{ A}$, $t_p = 100\text{ }\mu\text{s}$	I_e	-	160	-	mW/sr
Radiant power	$I_F = 100\text{ mA}$, $t_p = 20\text{ ms}$	ϕ_e	-	55	-	mW
Temperature coefficient of ϕ_e	$I_F = 100\text{ mA}$	TK_{ϕ_e}	-	-0.2	-	%/K
Angle of half intensity		φ	-	± 60	-	$^{\circ}$
Peak wavelength	$I_F = 100\text{ mA}$	λ_p	875	890	905	nm
Spectral bandwidth	$I_F = 100\text{ mA}$	$\Delta\lambda$	-	35	-	nm
Temperature coefficient of λ_p	$I_F = 100\text{ mA}$	TK_{λ_p}	-	0.3	-	nm/K
Rise time	$I_F = 100\text{ mA}$	t_r	-	15	-	ns
Fall time	$I_F = 100\text{ mA}$	t_f	-	15	-	ns

BASIC CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

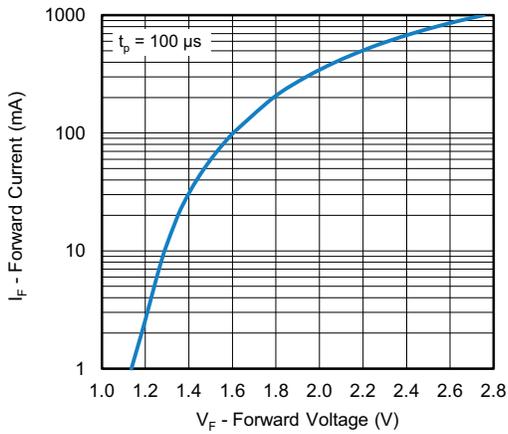


Fig. 3 - Forward Current vs. Forward Voltage

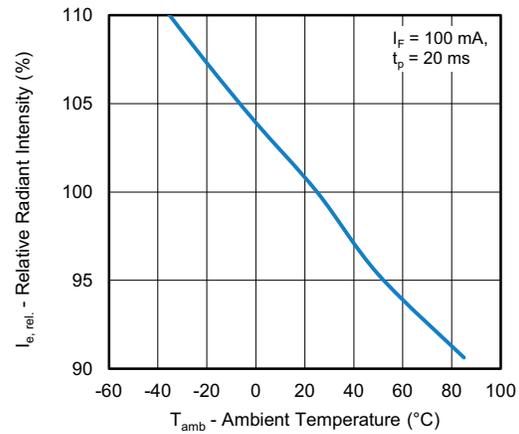


Fig. 6 - Relative Radiant Intensity vs. Ambient Temperature

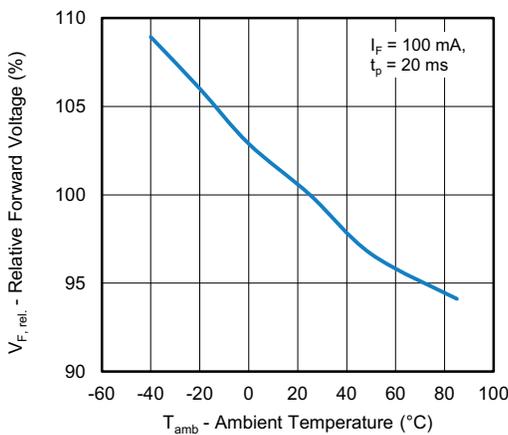


Fig. 4 - Relative Forward Voltage vs. Ambient Temperature

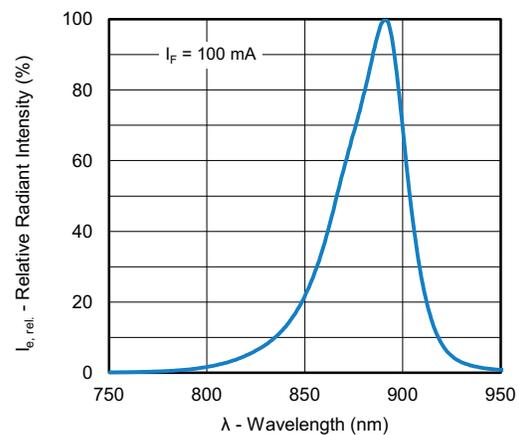


Fig. 7 - Relative Radiant Intensity vs. Wavelength

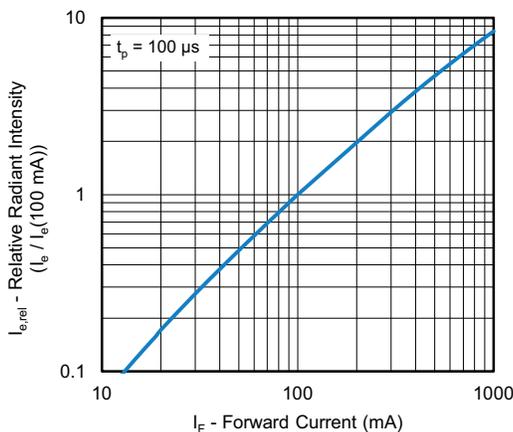


Fig. 5 - Relative Radiant Intensity vs. Forward Current

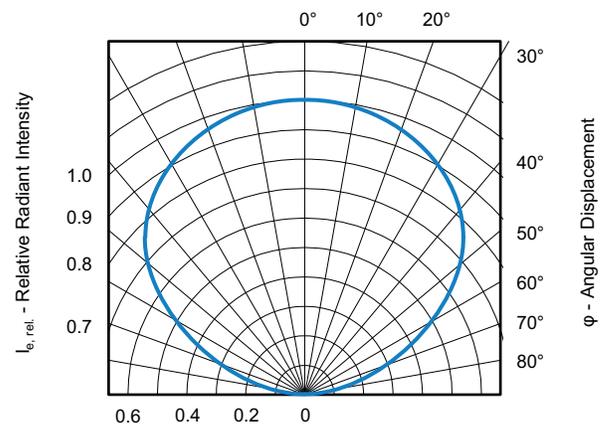
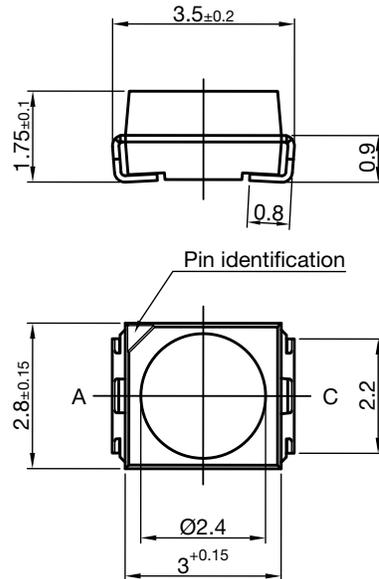


Fig. 8 - Relative Radiant Intensity vs. Angular Displacement

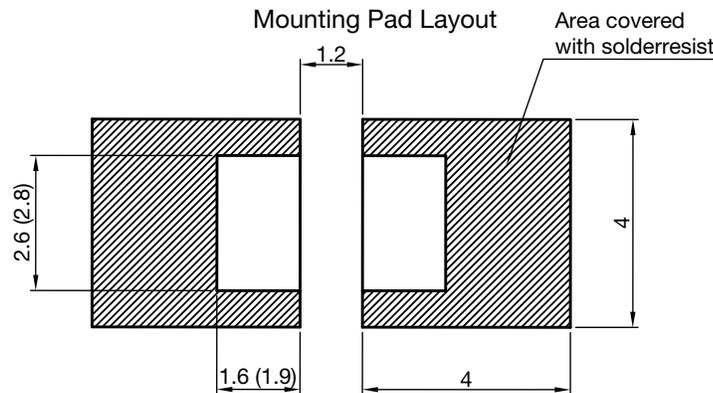
PACKAGE DIMENSIONS in millimeters



Technical drawings according to DIN specifications

Dimensions in mm

Drawing-No.: 6.541-5067.02-4
Issue: 5; 23.09.13



Dimensions: Reflow and vapor phase (wave soldering)

SOLDER PROFILE

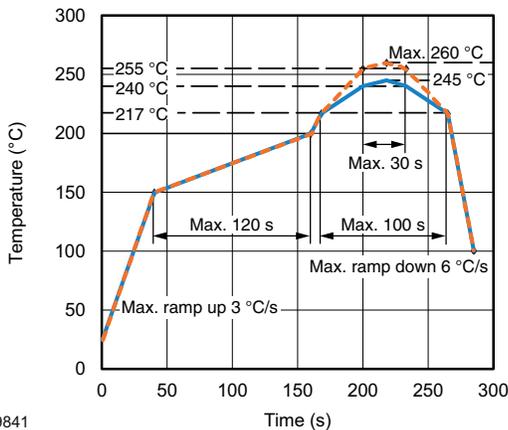


Fig. 9 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: $T_{amb} < 30\text{ °C}$, $RH < 60\%$

Moisture sensitivity level 3, according to J-STD-020

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at $40\text{ °C} (+ 5\text{ °C})$, $RH < 5\%$.

19841

TAPE AND REEL

PLCC-2 components are packed in antistatic blister tape (DIN IEC (CO) 564) for automatic component insertion. Cavities of blister tape are covered with adhesive tape.

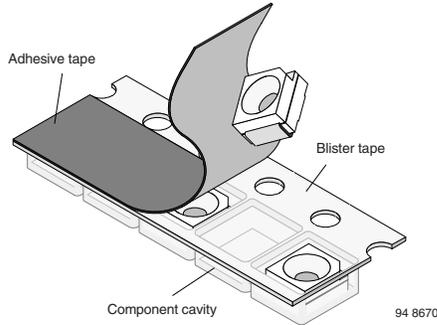


Fig. 10 - Blister Tape

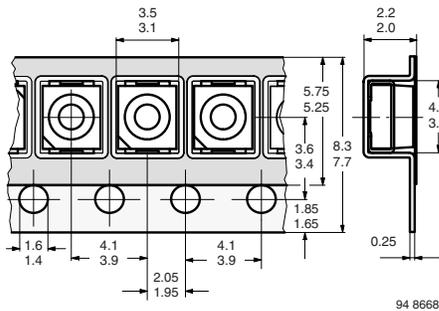


Fig. 11 - Tape Dimensions in mm for PLCC-2

MISSING DEVICES

A maximum of 0.5 % of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.

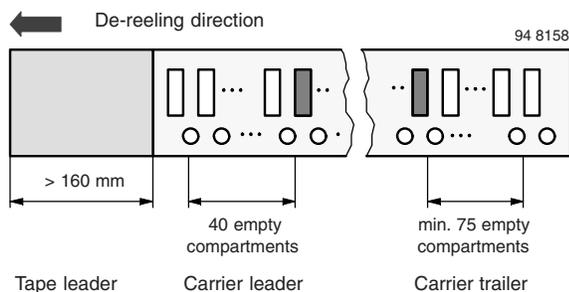


Fig. 12 - Beginning and End of Reel

The tape leader is at least 160 mm and is followed by a carrier tape leader with at least 40 empty compartments. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape. The least component is followed by a carrier tape trailer with at least 75 empty compartments and sealed with cover tape.

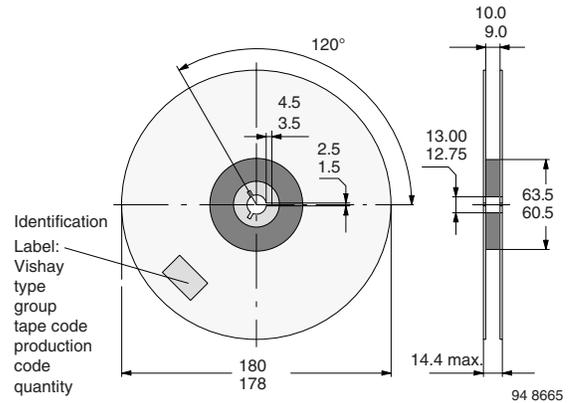


Fig. 13 - Dimensions of Reel-GS08

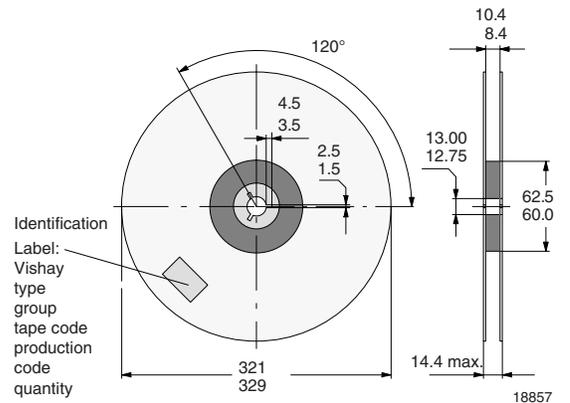


Fig. 14 - Dimensions of Reel-GS18

COVER TAPE REMOVAL FORCE

The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s. In order to prevent components from popping out of the blisters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.



Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.