Low Capacitance ESD Protection Diodes for High-Speed Data Interfaces

**MARKING**

(example only)

Bar = cathode marking

YYY = type code (see table below)

XX = date code

**DESIGN SUPPORT TOOLS**

For technical questions, contact: ESDprotection@vishay.com

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**FEATURES**

- IEC 61000-4-5 (lightning) see IPPM below
- ESD immunity acc. IEC 61000-4-2
  - ± 8 kV contact discharge
  - ± 15 kV air discharge
- ESD capability according to AEC-Q101: human body model: class H3B: > 8 kV
- SOT-23 package
- Low capacitance for high speed data lines, cellular handsets, USB port protection, LAN equipment, peripherals
- e3 - Sn
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

**ORDERING INFORMATION**

<table>
<thead>
<tr>
<th>PART NUMBER (EXAMPLE)</th>
<th>ENVIRONMENTAL AND QUALITY CODE</th>
<th>PACKAGING CODE</th>
<th>ORDERING CODE (EXAMPLE)</th>
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<tbody>
<tr>
<td>GL05T-</td>
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<td></td>
<td></td>
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<tr>
<td>GL05T-</td>
<td>AEC-Q101 QUALIFIED</td>
<td></td>
<td></td>
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<tr>
<td>GL05T-</td>
<td>RoHS-COMPLIANT + LEAD (Pb)-FREE</td>
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<td></td>
</tr>
<tr>
<td>GL05T-</td>
<td>STANDARD</td>
<td>TIN PLATED</td>
<td></td>
</tr>
<tr>
<td>GL05T-</td>
<td>GREEN</td>
<td>3K PER 7th REEL</td>
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<tr>
<td>GL05T-</td>
<td>8 mm TAPE, 15K/BOX = MOQ</td>
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<td>GL05T-</td>
<td>10K PER 13th REEL</td>
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<tr>
<td>GL05T-</td>
<td>8 mm TAPE, 10K/BOX = MOQ</td>
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<tr>
<td>GL05T-</td>
<td>GL05T-E3-08</td>
<td></td>
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<tr>
<td>GL05T-</td>
<td>GL05T-G3-08</td>
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<tr>
<td>GL05T-</td>
<td>GL05T-HE3-08</td>
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<tr>
<td>GL05T-</td>
<td>GL05T-HG3-08</td>
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<td>GL05T-E3-18</td>
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<td>GL05T-HE3-18</td>
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<tr>
<td>GL05T-</td>
<td>GL05T-HG3-18</td>
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**PACKAGE DATA**

<table>
<thead>
<tr>
<th>DEVICE NAME</th>
<th>PACKAGE NAME</th>
<th>TYPE CODE</th>
<th>ENVIRONMENTAL STATUS</th>
<th>WEIGHT</th>
<th>MOLDING COMPOUND</th>
<th>MOISTURE SENSITIVITY LEVEL</th>
<th>SOLDERING CONDITIONS</th>
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<tbody>
<tr>
<td>GL05T</td>
<td>SOT-23</td>
<td>L05</td>
<td>Standard</td>
<td>8.8 mg</td>
<td>UL 94 V-0</td>
<td>MSL level 1 (according J-STD-020)</td>
<td>Peak temperature max. 260 °C</td>
</tr>
<tr>
<td>GL05T</td>
<td>SOT-23</td>
<td>L06</td>
<td>Green</td>
<td>8.1 mg</td>
<td>UL 94 V-0</td>
<td>MSL level 1 (according J-STD-020)</td>
<td>Peak temperature max. 260 °C</td>
</tr>
<tr>
<td>GL12T</td>
<td>SOT-23</td>
<td>L12</td>
<td>Standard</td>
<td>8.8 mg</td>
<td>UL 94 V-0</td>
<td>MSL level 1 (according J-STD-020)</td>
<td>Peak temperature max. 260 °C</td>
</tr>
<tr>
<td>GL12T</td>
<td>SOT-23</td>
<td>L13</td>
<td>Green</td>
<td>8.1 mg</td>
<td>UL 94 V-0</td>
<td>MSL level 1 (according J-STD-020)</td>
<td>Peak temperature max. 260 °C</td>
</tr>
<tr>
<td>GL15T</td>
<td>SOT-23</td>
<td>L15</td>
<td>Standard</td>
<td>8.8 mg</td>
<td>UL 94 V-0</td>
<td>MSL level 1 (according J-STD-020)</td>
<td>Peak temperature max. 260 °C</td>
</tr>
<tr>
<td>GL15T</td>
<td>SOT-23</td>
<td>L16</td>
<td>Green</td>
<td>8.1 mg</td>
<td>UL 94 V-0</td>
<td>MSL level 1 (according J-STD-020)</td>
<td>Peak temperature max. 260 °C</td>
</tr>
<tr>
<td>GL24T</td>
<td>SOT-23</td>
<td>L24</td>
<td>Standard</td>
<td>8.8 mg</td>
<td>UL 94 V-0</td>
<td>MSL level 1 (according J-STD-020)</td>
<td>Peak temperature max. 260 °C</td>
</tr>
<tr>
<td>GL24T</td>
<td>SOT-23</td>
<td>L25</td>
<td>Green</td>
<td>8.1 mg</td>
<td>UL 94 V-0</td>
<td>MSL level 1 (according J-STD-020)</td>
<td>Peak temperature max. 260 °C</td>
</tr>
</tbody>
</table>
The GLxxT contains an avalanche diode (pin 3-1) and a switching diode (pin 3-2). With pin 1 connected to the signal or data line and pin 2 connected to ground both diodes are in series (pin 3 remains unconnected). The big and robust avalanche diode, driven in reverse direction, provides the working range VRWM of 5 V, 12 V, 15 V or 24 V. Due to its size the capacitance of the avalanche diode is in the range of typ. 260 pF (GL05T) and 65 pF (GL24T). The small switching diode in series has a low capacitance of just 2.5 pF (typ.). As both diodes are in series (with pin 3 not connected) the total capacitance of both diodes measured between pin 1 and 2 is as low as the capacitance of the switching diode.

Before the GLxxT can provide this low capacitance the big capacitance of the avalanche diode has to be charged up with the first signal or data pulses. This is usually no problem for digital signals like USB or other data ports.

With the GLxxT a signal or data line can be protected against positive transients only. For negative transients another GLxxT can be used to provide a back path for the negative transients as well.
ELECTRICAL CHARACTERISTICS GL05T  (T_{amb} = 25 °C unless otherwise specified)

**PARAMETER** | **TEST CONDITIONS/REMARKS** | **SYMBOL** | **MIN.** | **TYP.** | **MAX.** | **UNIT**
--- | --- | --- | --- | --- | --- | ---
Protection paths | Number of lines which can be protected | N_{channel} | - | - | 1 | lines
Reverse stand-off voltage | Max. reverse working voltage | V_{RWM} | - | - | 5 | V
Reverse voltage | at I_R = 20 μA | V_R | 5 | - | - | V
Reverse current | at V_R = 5 V | I_R | - | - | 20 | μA
Reverse breakdown voltage | at I_R = 1 mA | V_{BR} | 6.9 | 7.5 | 8.0 | V
Reverse clamping voltage | at I_{pp} = 1 A | V_C | - | - | 9.8 | V | at I_{pp} = 5 A | - | - | 11 | V
Capacitance | at V_R = 0 V; f = 1 MHz | C_D | - | 2.5 | 5 | pF

ELECTRICAL CHARACTERISTICS GL12T  (T_{amb} = 25 °C unless otherwise specified)

**PARAMETER** | **TEST CONDITIONS/REMARKS** | **SYMBOL** | **MIN.** | **TYP.** | **MAX.** | **UNIT**
--- | --- | --- | --- | --- | --- | ---
Protection paths | Number of lines which can be protected | N_{channel} | - | - | 1 | lines
Reverse stand-off voltage | Max. reverse working voltage | V_{RWM} | - | - | 12 | V
Reverse voltage | at I_R = 1 μA | V_R | 12 | - | - | V
Reverse current | at V_R = 12 V | I_R | - | - | 1 | μA
Reverse breakdown voltage | at I_R = 1 mA | V_{BR} | 13.3 | 14.3 | 17.2 | V
Reverse clamping voltage | at I_{pp} = 1 A | V_C | - | - | 19 | V | at I_{pp} = 5 A | - | - | 24 | V
Capacitance | at V_R = 0 V; f = 1 MHz | C_D | - | 2.5 | 5 | pF

ELECTRICAL CHARACTERISTICS GL15T  (T_{amb} = 25 °C unless otherwise specified)

**PARAMETER** | **TEST CONDITIONS/REMARKS** | **SYMBOL** | **MIN.** | **TYP.** | **MAX.** | **UNIT**
--- | --- | --- | --- | --- | --- | ---
Protection paths | Number of lines which can be protected | N_{channel} | - | - | 1 | lines
Reverse stand-off voltage | Max. reverse working voltage | V_{RWM} | - | - | 15 | V
Reverse voltage | at I_R = 1 μA | V_R | 15 | - | - | V
Reverse current | at V_R = 15 V | I_R | - | - | 1 | μA
Reverse breakdown voltage | at I_R = 1 mA | V_{BR} | 16.7 | 17.7 | 22 | V
Reverse clamping voltage | at I_{pp} = 1 A | V_C | - | - | 24 | V | at I_{pp} = 5 A | - | - | 33 | V
Capacitance | at V_R = 0 V; f = 1 MHz | C_D | - | 2.5 | 5 | pF
## ELECTRICAL CHARACTERISTICS GL24T (T\textsubscript{amb} = 25 °C unless otherwise specified)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS/REMARKS</th>
<th>SYMBOL</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Protection paths</td>
<td>Number of lines which can be protected</td>
<td>N\textsubscript{channel}</td>
<td>-</td>
<td>-</td>
<td>1</td>
<td>lines</td>
</tr>
<tr>
<td>Reverse stand-off voltage</td>
<td>Max. reverse working voltage</td>
<td>V\textsubscript{RWM}</td>
<td>-</td>
<td>-</td>
<td>24</td>
<td>V</td>
</tr>
<tr>
<td>Reverse voltage</td>
<td>at ( I_R ) = 1 μA</td>
<td>V\textsubscript{R}</td>
<td>24</td>
<td>-</td>
<td>-</td>
<td>V</td>
</tr>
<tr>
<td>Reverse current</td>
<td>at ( V_R = 24 ) V</td>
<td>I\textsubscript{R}</td>
<td>-</td>
<td>-</td>
<td>1</td>
<td>μA</td>
</tr>
<tr>
<td>Reverse breakdown voltage</td>
<td>at ( I_R = 1 ) mA</td>
<td>V\textsubscript{BR}</td>
<td>26.7</td>
<td>28.2</td>
<td>33</td>
<td>V</td>
</tr>
<tr>
<td>Reverse clamping voltage</td>
<td>at ( I_{PP} = 1 ) A</td>
<td>V\textsubscript{C}</td>
<td>-</td>
<td>-</td>
<td>43</td>
<td>V</td>
</tr>
<tr>
<td>Capacitance</td>
<td>at ( V_R = 0 ) V, ( f = 1 ) MHz</td>
<td>C\textsubscript{D}</td>
<td>2.5</td>
<td>5</td>
<td>pF</td>
<td></td>
</tr>
</tbody>
</table>

*Fig. 1 - Typical Forward Current \( I_F \) vs. Forward Voltage \( V_F \)*

*Fig. 2 - Typical Forward Current \( I_F \) vs. Forward Voltage \( V_F \)*

*Fig. 3 - Typical Reverse Voltage \( V_R \) vs. Reverse Current \( I_R \)*

For technical questions, contact: ESDprotection@vishay.com
**PACKAGE DIMENSIONS** in millimeters (inches): **SOT-23**

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**Top view**

- 3.1 (0.122)
- 2.8 (0.110)
- 0.45 (0.018)
- 0.35 (0.014)
- 0.45 (0.018)
- 0.35 (0.014)

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**Unreeling direction**

- 0.9 (0.035) max.

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**Foot print recommendation:**

- 1.43 (0.056)
- 1.20 (0.047)
- 0.45 (0.018)
- 0.35 (0.014)
- 0.7 (0.028)

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**Orientation in carrier tape**

- **SOT-23**
- S8-V-3929.01-006 (4)
- 04.02.2010
- 22607

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