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# BYW82, BYW83, BYW84, BYW85, BYW86

**Vishay Semiconductors** 

# **Standard Avalanche Sinterglass Diode**





## **MECHANICAL DATA**

Case: SOD-64 Terminals: plated axial leads, solderable per MIL-STD-750, method 2026 Polarity: color band denotes cathode end Mounting position: any Weight: approx. 858 mg

#### **FEATURES**

- Glass passivated junction
- · Hermetically sealed package
- · Controlled avalanche characteristics
- Low reverse current
- High surge current loading
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## **APPLCIATIONS**

• Rectification, general purpose

ORDERING INFORMATION (Example)				
DEVICE NAME	ORDERING CODE	TAPED UNITS	MINIMUM ORDER QUANTITY	
BYW82 or BYW83 or BYW84 and BYW86	BYW86-TR	2500 per 10" tape and reel	12 500	
BYW82 or BYW84 and BYW85	BYW85-TAP	2500 per ammopack	12 500	
BYW85	BYW85TR	2500 per 10" tape and reel	12 500	
BYW83 or <b>BYW86</b>	BYW86TAP	2500 per ammopack	12 500	

PARTS TABLE		
PART	TYPE DIFFERENTIATION	PACKAGE
BYW82	V <sub>R</sub> = 200 V, I <sub>F(AV)</sub> = 3 A	SOD-64
BYW83	$V_{R} = 400 \text{ V}, \text{ I}_{F(AV)} = 3 \text{ A}$	SOD-64
BYW84	V <sub>R</sub> = 600 V, I <sub>F(AV)</sub> = 3 A	SOD-64
BYW85	V <sub>R</sub> = 800 V, I <sub>F(AV)</sub> = 3 A	SOD-64
BYW86	V <sub>R</sub> = 1000 V, I <sub>F(AV)</sub> = 3 A	SOD-64

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
Reverse voltage = repetitive peak reverse voltage	See electrical characteristics	BYW82	$V_{R} = V_{RRM}$	200	V	
		BYW83	$V_{R} = V_{RRM}$	400	V	
		BYW84	$V_{R} = V_{RRM}$	600	V	
		BYW85	$V_{R} = V_{RRM}$	800	V	
		BYW86	$V_{R} = V_{RRM}$	1000	V	
Peak forward surge current	$t_p = 10$ ms, half sine wave		I <sub>FSM</sub>	100	А	
Repetitive peak forward current			I <sub>FRM</sub>	18	А	
Average forward current			I <sub>F(AV)</sub>	3	А	
Pulse avalanche peak power	$t_p$ = 20 µs, half sine wave, $T_j$ = 175 °C		P <sub>R</sub>	1000	W	
Pulse energy in avalanche mode, non repetitive (inductive load switch off)	$I_{(BR)R}$ = 1 A, $T_j$ = 175 °C		E <sub>R</sub>	20	mJ	
i <sup>2</sup> t-rating			i <sup>2</sup> t	40	A2s	
Junction and storage temperature range			$T_j = T_{stg}$	-55 to +175	°C	

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1

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<b>MAXIMUM THERMAL RESISTANCE</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Junction ambient	Lead length I = 10 mm, $T_L$ = constant	R <sub>thJA</sub>	25	K/W
	On PC board with spacing 25 mm	R <sub>thJA</sub>	70	K/W

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 3 A	V <sub>F</sub>	-	-	1	V
Reverse current	$V_{R} = V_{RRM}$	I <sub>R</sub>	-	0.1	1	μA
	$V_{R} = V_{RRM}, T_{j} = 100 \ ^{\circ}C$	I <sub>R</sub>	-	5	10	μA
Breakdown voltage	$I_R = 100 \ \mu A, \ tp/T = 0.01, \ tp = 0.3 \ ms$	V <sub>(BR)</sub>	-	-	1600	V
Diode capacitance	$V_R = 4 V, f = 1 MHz$	CD	-	40	60	pF
Reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, i <sub>R</sub> = 0.25 A	t <sub>rr</sub>	-	3.5	5	μs
	$I_F = 1 \text{ A}, \text{ dI/dt} = 5 \text{ A/}\mu\text{s}, V_R = 50 \text{ V}$	t <sub>rr</sub>	-	4.5	7.5	μs
Reverse recovery charge	I <sub>F</sub> = 1 A, dI/dt = 5 A/μs	Q <sub>rr</sub>	-	8	12	μC

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

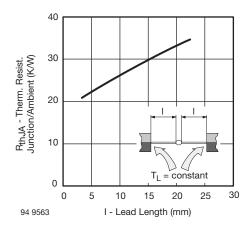


Fig. 1 - Max. Thermal Resistance vs. Lead Length

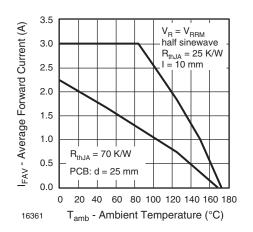


Fig. 2 - Max. Average Forward Current vs. Ambient Temperature

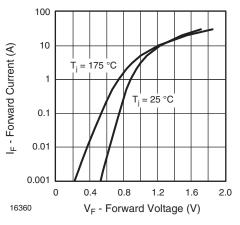


Fig. 3 - Forward Current vs. Forward Voltage

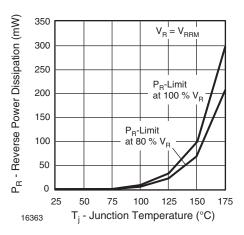


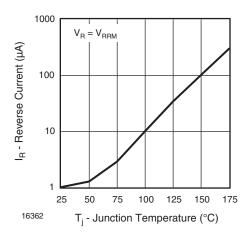
Fig. 4 - Max. Reverse Power Dissipation vs. Junction Temperature

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BYW82, BYW83, BYW84, BYW85, BYW86

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Fig. 5 - Reverse Current vs. Junction Temperature

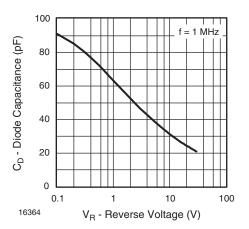


Fig. 6 - Diode Capacitance vs. Reverse Voltage

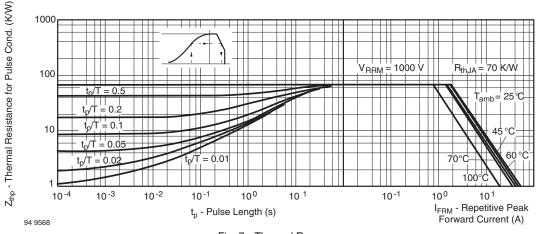
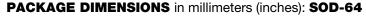
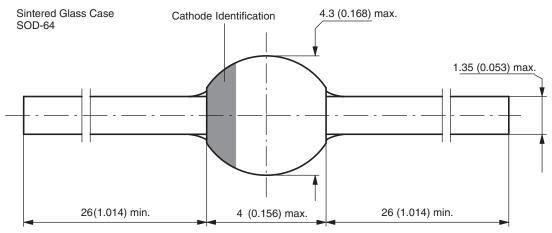


Fig. 7 - Thermal Response





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Rev. 2.0, 22-Feb-18

3

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1