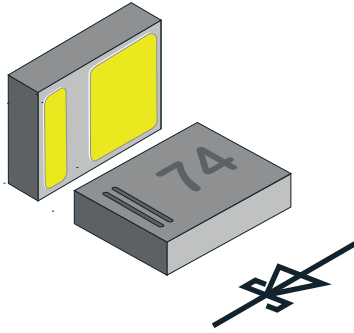




Schottky Rectifier Surface-Mount FlipKY® Gen 3



FEATURES

- Schottky diode for high-speed switching
- Very low dimensions:
1.2 mm x 0.9 mm x 0.29 mm
- 1.5 A forward current
- Low forward voltage drop (typ. 460 mV at 1500 m A)
- Low reverse current (< 20 µA at 10 V)
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

LINKS TO ADDITIONAL RESOURCES



PARTS TABLE								
PART	GRADE	ORDERING CODE	CIRCUIT CONFIGURATION	PACKAGE NAME	TYPE MARKING	WEIGHT	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY
VSKY1540C9	AEC-Q101	VSKY1540C9HG4-08	Single	CLP1209-2L	74	0.73 mg	10 000	10 000

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V _R	40	V
Forward continuous current		I _F	1500	mA
Surge forward current	Single pulse; 8.3 ms half sine-wave	I _{FSM}	23	A

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Thermal resistance junction to soldering point	Acc. JEDEC® JESD51-41	R _{thJS}	7	K/W
Maximum operating junction temperature		T _{j max.}	150	°C
Storage temperature range		T _{stg}	-65 to +150	°C

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	TYP.	MAX.	UNIT
Leakage current	V _R = 10 V	I _R	-	20	µA
Leakage current	V _R = 40 V	I _R	-	85	µA
Forward voltage	I _F = 100 mA	V _F	311	361	mV
Forward voltage	I _F = 1000 mA	V _F	422	483	mV
Forward voltage	I _F = 1500 mA	V _F	460	530	mV
Diode capacitance	V _R = 0 V, f = 1 MHz	C _D	275	-	pF



RATINGS AND CHARACTERISTICS CURVES ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)

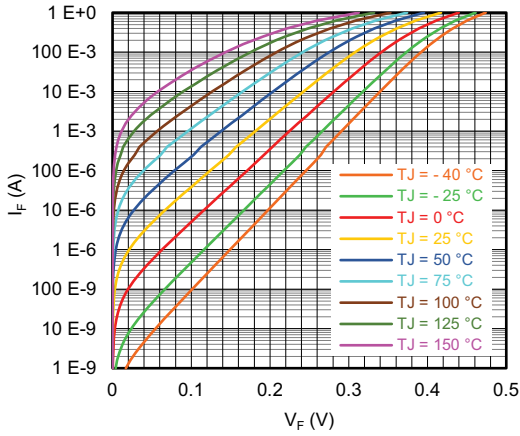


Fig. 1 - Typical Forward Current vs. Forward Voltage

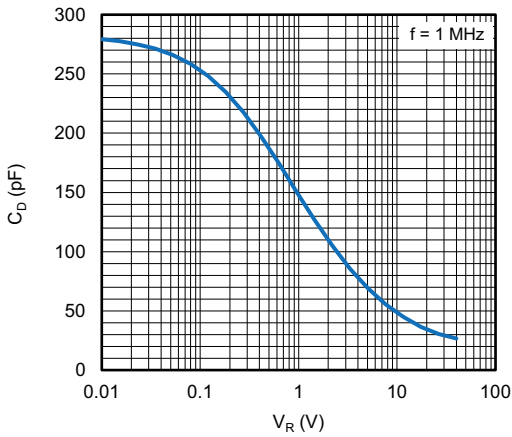


Fig. 2 - Typical Capacitance vs. Reverse Voltage

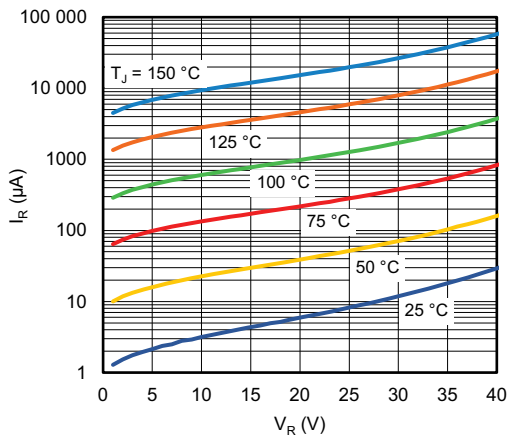
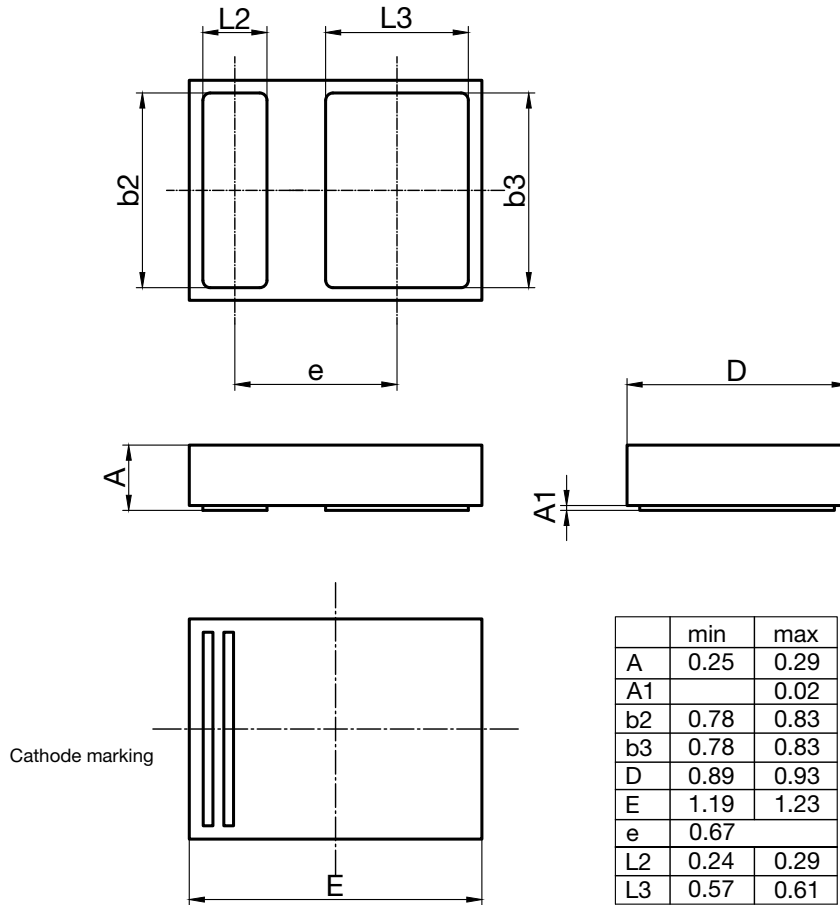


Fig. 3 - Typical Reverse Leakage Current vs. Reverse Voltage

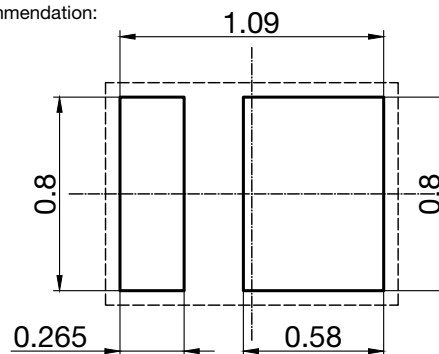


PACKAGE DIMENSIONS in millimeters: **CLP1209-2L**

Package=Chip Dimensions in mm



foot print recommendation:



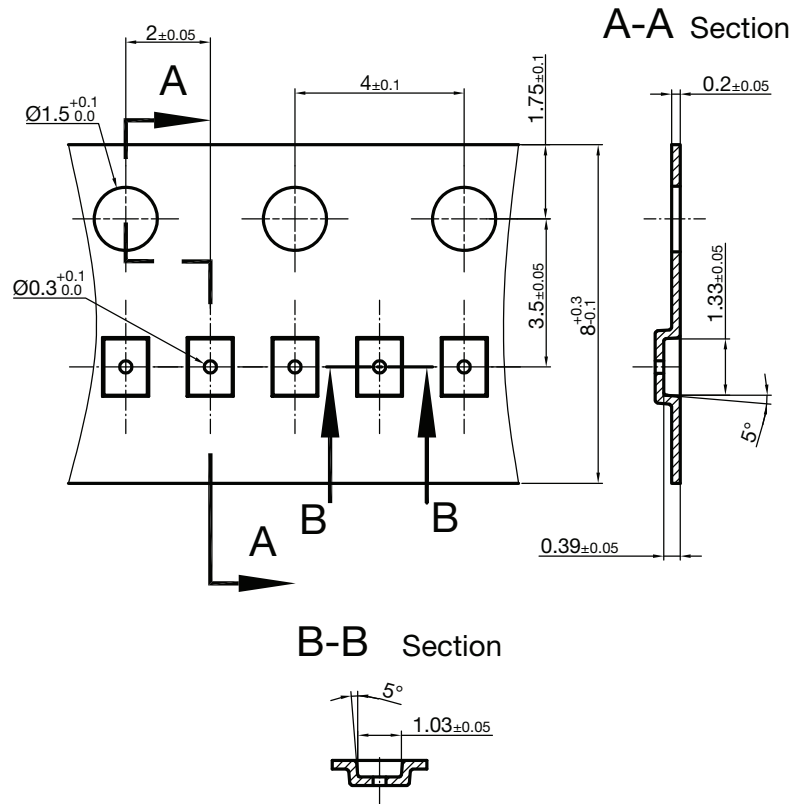
Document no.:S8-V-3906.04-053 (4)
 Created - Date: 03. July. 2018
 Rev.1 - Date: 19. Dec. 2023
 23230

Footprint and soldering recommendation:

please see Application Note: www.vishay.com/doc?85917



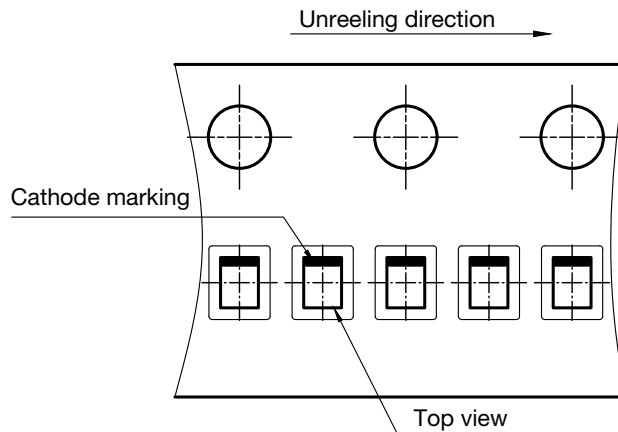
CARRIER TAPE in millimeters: **CLP1209-2L**



Cummulative tolerances of 10 sprocket holes is ± 0.2 mm

Document: S8-V-3906.04-54 (4)
created date: 03.07.2018
23231

ORIENTATION IN CARRIER CLP1209-2L



Document: S8-V-3906.04-55 (4)
created date: 03.07.2018
Rev.: 19.12.2023
23232



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