RoHS

COMPLIANT

HALOGEN

FREE

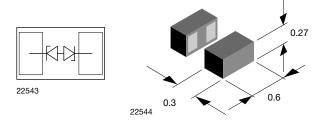
**GREEN** 

(5-2008)



Vishay Semiconductors

# **Bidirectional Symmetrical (BiSy) Single Line ESD Protection Diode** in Silicon Package



#### **MARKING**



#### **LINKS TO ADDITIONAL RESOURCES**







#### **FEATURES**

- Ultra compact CLP0603 package
- Low package height < 0.3 mm
- 1-line ESD protection
- AEC-Q101 qualified available
- Working range ± 3.3 V
- Low leakage current < 0.1 μA
- Low load capacitance C<sub>D</sub> < 14 pF</li>
- ESD immunity acc. IEC 61000-4-2 ± 30 kV contact discharge ± 30 kV air discharge
- Lead plating: Au (e4)
- · Lead material: Ni
- · Topside coating
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



please see Application Note: www.vishay.com/doc?85917

ORDERING INFORMATION							
		ENVIRONMENTAL AND QUA	LITY CODE	PACKAGING CODE	ORDERING CODE (EXAMPLE)		
PART NUMBER (EXAMPLE)	AEC-Q101 QUALIFIED	Rohs-Compliant + Lead (Pb)-free terminations	GOLD PLATED	15K PER 7" REEL (8 mm TAPE)			
		GREEN		15K/BOX = MOQ			
VCUT03G1-SD0	-	G	4	-08	VCUT03G1-SD0-G4-08		
		G		-08	VCUT03G1-SD0HG4-08		

PACKAGE DATA							
DEVICE NAME PACKAGE NAME		TYPE CODE	WEIGHT	SOLDERING CONDITIONS			
VCUT03G1-SD0	CLP0603-2L	3G	0.12 mg	Peak temperature max. 260 °C Reflow soldering according JEDEC® STD-020			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	PARAMETER TEST CONDITIONS SYMI						
Peak pulse current	acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	6	Α			
Peak pulse power	Pin 1 to pin 2 acc. IEC 61000-4-5; $t_p$ = 8/20 $\mu$ s; single shot	P <sub>PP</sub>	78	W			
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	147			
	Air discharge acc. IEC 61000-4-2; 10 pulses	- V <sub>ESD</sub>	± 30	kV			
Operating temperature	Junction temperature	TJ	-55 to +150	°C			
Storage temperature		T <sub>stg</sub>	-55 to +150	°C			

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#### **CUT THE SPIKES WITH VCUT03G1-SD0**

The VCUT03G1-SD0 is a Bidirectional and Symmetrical (BiSy) ESD protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VCUT03G1-SD0 offers a high isolation (low leakage current, low capacitance) within the specified working range. Due to the short leads and small package size of the tiny CLP0603 package the line inductance is very low, so that fast transients like and ESD strike can be clamped with minimal over- or undershoots.

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines	
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	3.3	V	
Reverse voltage	at I <sub>R</sub> = 0.1 μA	V <sub>R</sub>	3.3	-	-	V	
Reverse current	at V <sub>RWM</sub> = 3.3 V	I <sub>R</sub>	-	-	0.1	μΑ	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6.5	8	9	V	
Reverse clamping voltage	at I <sub>PP</sub> = 1 A; t <sub>p</sub> = 8/20 µs single shot	V <sub>C</sub>	-	8.8	10	V	
neverse clamping voltage	at $I_{PP} = I_{PPM} = 6 \text{ A}$ ; $t_p = 8/20 \mu\text{s}$ single shot	V <sub>C</sub>	-	11	13	V	
0	at V <sub>R</sub> = 0 V; f = 1 MHz	C <sub>D</sub>	-	13	14	pF	
Capacitance	at V <sub>R</sub> = 2.5 V; f = 1 MHz	C <sub>D</sub>	-	11 -		pF	
Clamping voltage	Transmission Line Pulse (TLP); $t_p = 100 \text{ ns}$ $I_{TLP} = 8 \text{ A}$	V <sub>C-TLP</sub>	-	9.8	-	V	
Clamping voltage	Transmission Line Pulse (TLP); $t_p = 100 \text{ ns}$ $I_{TLP} = 16 \text{ A}$	V <sub>C-TLP</sub>	-	11	-	V	
Dynamic resistance	Transmission Line Pulse (TLP); $t_p = 100 \text{ ns}$	$R_{DYN}$	-	0.15	-	Ω	

#### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

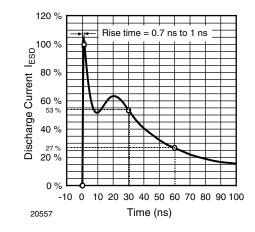


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega$ /150 pF)

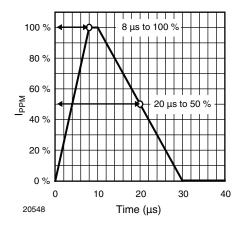


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

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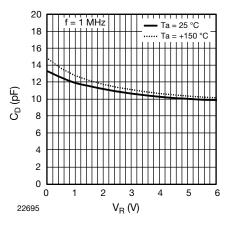


Fig. 3 - Typical Capacitance  $C_D$  vs. Reverse Voltage  $V_R$ 

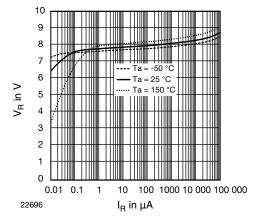


Fig. 4 - Typical Reverse Voltage V<sub>R</sub> vs. Reverse Current I<sub>R</sub>

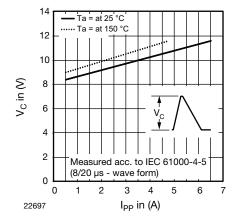


Fig. 5 - Typical Peak Clamping Voltage  $V_C$  vs. Peak Pulse Current  $I_{PP}$ 

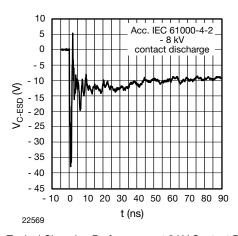


Fig. 6 - Typical Clamping Performance at 8 kV Contact Discharge acc. IEC 61000-4-2

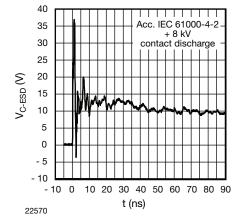


Fig. 7 - Typical Clamping Performance at 8 kV Contact Discharge acc. IEC 61000-4-2

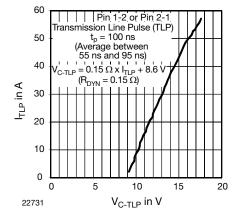
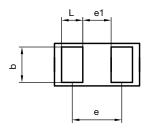


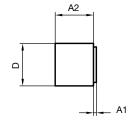
Fig. 8 - Typical Clamping Voltage at 100 ns Transmission Line Pulse (TLP)



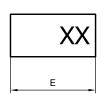
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#### PACKAGE DIMENSIONS in millimeters (mils): CLP0603-2L Gen2





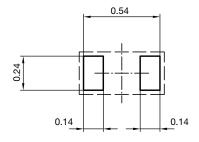




	Millimeters					
	min.	nom.	max.	min.	nom.	max.
А	0.25	0.28	0.30	9.84	11.02	11.81
A1	0.01	0.01	0.02	0.39	0.39	0.79
A2	0.24	0.27	0.28	9.45	10.63	11.02
b	0.22	0.25	0.28	8.66	9.84	11.02
D	0.27	0.30	0.33	10.62	11.81	12.99
Е	0.57	0.60	0.63	22.44	23.62	24.80
е		0.40			15.75	
e1		0.25			9.84	
L	0.12	0.15	0.18	4.72	5.91	7.09

#### XX ... TYPE CODE AND ALSO PIN1 LOCATION

#### foot print recommendation:



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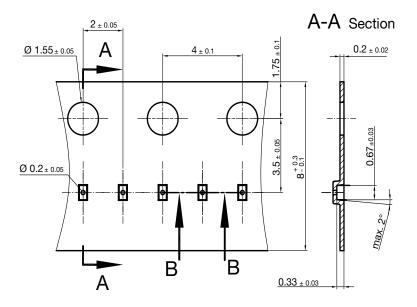
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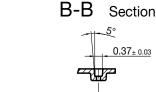
#### Footprint and soldering recommendation:

please see Application Note: www.vishay.com/doc?85917

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#### **CARRIER TAPE** in millimeters: **CLP0603-2L Gen2**

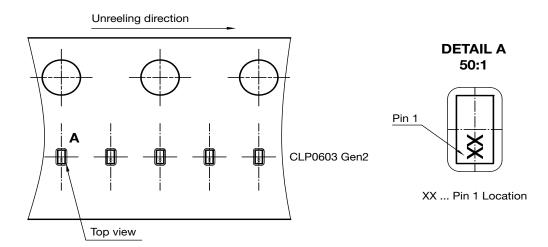




Cummulative tolerances of 10 sprocket holes is +/-0.2 mm

22591 Document no. S8-V-3906.04-0025 (4) Created - Date: 22, Nov. 2010

#### **ORIENTATION IN CARRIER CLP0603-2L Gen2**



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