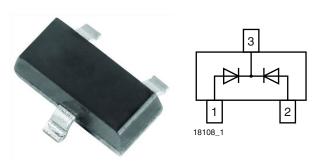


Small Signal Switching Diode, Dual



LINKS TO ADDITIONAL RESOURCES



Case: SOT-23









FEATURES

- Silicon epitaxial planar diode
- · Fast switching dual diode with common cathode
- AEC-Q101 qualified available (part number on request)
- Molding compound meets UL 94 V-0 flammability rating
- Moisture sensitivity level (MSL) 1
- Base P/N-G3 green, commercial grade
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





RoHS HALOGEN FREE

GREEN

Weight: approx. 9.2 mg

MECHANICAL DATA

Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

| PARTS TABLE | | | | | | | |
|-------------|---------------|-----------------------|-------------------|-----------------------|-----------------------------------|------------------------|--|
| PART | ORDERING CODE | AEC-Q101 QUALIFIED | TYPE MARKING | CIRCUIT CONFIGURATION | TAPED UNITS PER REEL | MINIMUM ORDER QUANTITY | |
| BAV23C-G | BAV23C-G3-08 | no | KT7 Common cathod | Common aethodo | 3 000 (8 mm tape on 7" reel) | 15 000 | |
| | BAV23C-G3-18 | no | | Common camode | 10 000 (8 mm tape on 13" reel) | 10 000 | |

| PACKAGE | | | | | | |
|---------------------|--------|--------------------------------------|--------------------------------|------------------------------|--|--|
| PACKAGE NAME WEIGHT | | MOLDING COMPOUND FLAMMABILITY RATING | MOISTURE SENSITIVITY LEVEL | SOLDERING CONDITIONS | | |
| SOT-23 | 9.2 mg | UL 94 V-0 | MSL 1 (according J-STD-020) | Peak temperature max. 260 °C | | |

| ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified) | | | | | | | |
|--|--|--------------------|-------|------|--|--|--|
| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT | | | |
| Continuous reverse voltage | | V_R | 200 | V | | | |
| Repetitive peak reverse voltage | | V_{RRM} | 250 | V | | | |
| Non-repetitive peak forward current (1) | t = 1 μs | I _{FSM} | 9 | Α | | | |
| Non-repetitive peak forward surge current (1) | t = 1 s | I _{FSM} | 0.5 | Α | | | |
| Maximum average forward rectified current (1) | f ≥ 50 Hz | I _{F(AV)} | 200 | mA | | | |
| Forward continuous current (1) | | I _F | 400 | mA | | | |
| Repetitive peak forward current | | I _{FRM} | 625 | mA | | | |
| Devey dissination | on FR-4 board with recommended soldering footprint | В | 300 | mW | | | |
| Power dissipation | Infinite heatsink | P _{tot} | 500 | mW | | | |

Note

(1) Infinite heatsink



| THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified) | | | | | | |
|---|---|------------------|-------------|------|--|--|
| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT | | |
| Thermal resistance junction to ambient air | according to JEDEC [®] 51-3 on FR-4 board with recommended soldering footprint | R_{thJA} | 420 | K/W | | |
| Thermal resistance junction to lead | Infinite heatsink | R_{thJL} | 250 | K/W | | |
| Junction temperature | | Tj | 150 | °C | | |
| Storage temperature range | | T _{stg} | -65 to +150 | °C | | |
| Operating temperature range | | T _{op} | -55 to +150 | °C | | |

| ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified) | | | | | | |
|--|---|-------------------|------|------|------|------|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| Reverse breakdown voltage | $I_R = 100 \mu A, t_p = 300 ms$ | V _(BR) | 250 | | | V |
| Forward voltage | I _F = 100 mA | V_{F} | | | 1 | V |
| Forward voltage | I _F = 200 mA | V _F | | | 1.25 | V |
| Reverse current | V _R = 200 V | I _R | | | 100 | nA |
| Reverse current | V _R = 200 V, T _j = 150 °C | I _R | | | 100 | μΑ |
| Dynamic forward resistance | I _F = 10 mA | r _f | | 5 | | Ω |
| Diode capacitance | V _R = 0 V, f = 1 MHz | C _D | | | 5 | pF |
| Reverse recovery time | $I_F = I_R = 30 \text{ mA}, R_L = 100 \Omega$ $I_R = 3 \text{ mA}$ | t _{rr} | | | 50 | ns |



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

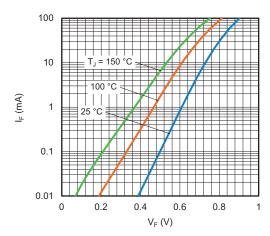


Fig. 1 - Typical Forward Current vs. Forward Voltage

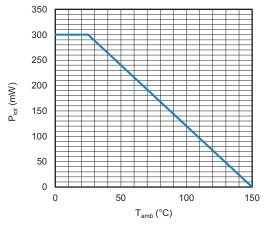


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature

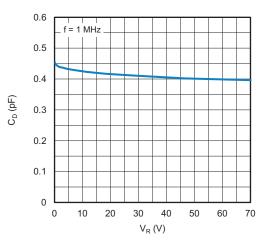


Fig. 3 - Typical Capacitance vs. Reverse Voltage

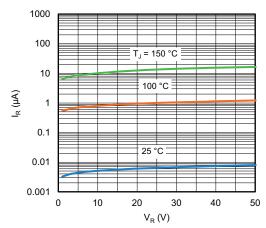
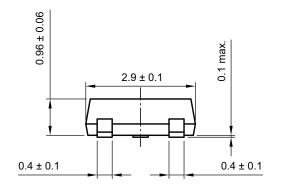
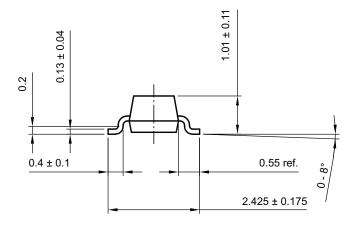


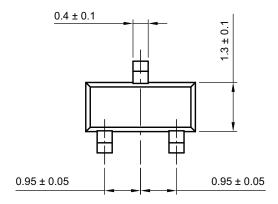
Fig. 4 - Typical Reverse Leakage Current vs. Reverse Voltage



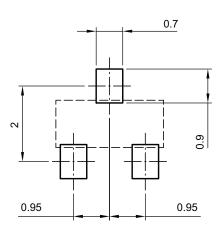
PACKAGE DIMENSIONS in millimeters: **SOT-23**







footprint recommendation:

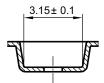


Created - Date: 18-Oct-2021 Rev. 01 - Date: 18-Jan-2022 S8-V-3929.01-009 (4)

CARRIER TAPE SOT-23

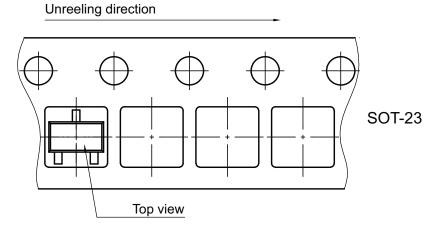
A-A Section 0.229 ± 0.013 0.229 ± 0.013 0.229 ± 0.013 0.22 ± 0.013

B-B Section



Created Date: 04-Feb-2010 Rev. Date: 07-Feb-2022 S8-V-3929.01-005 (4)

ORIENTATION IN CARRIER TAPE SOT-23



Created Date: 04-Feb-2010 Rev. Date: 07-Nov-2022 S8-V-3929.01-005 (4)



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