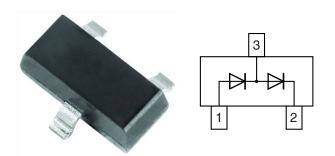
MMBD7000

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Vishay Semiconductors

Small Signal Switching Diode, Dual



LINKS TO ADDITIONAL RESOURCES



MECHANICAL DATA

Case: SOT-23

Weight: approx. 9.2 mg

Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box

08/3K per 7" reel (8 mm tape), 15K/box

FEATURES

- Silicon epitaxial planar diode
- Fast switching dual diode, especially suited for automatic insertion
- AEC-Q101 qualified available
- Molding compound meets UL 94 V-0 flammability rating
- Moisture sensitivity level (MSL) 1
- Base P/N-E3 RoHS-compliant, commercial grade
- Base P/N-HE3_A RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

PARTS TABLE							
PART	ORDERING CODE	AEC-Q101 QUALIFIED	TYPE MARKING	CIRCUIT CONFIGURATION	TAPED UNITS PER REEL	MINIMUM ORDER QUANTITY	
MMBD7000	MMBD7000-E3-08	no	M5G	Dual serial	3 000	15 000	
	MMBD7000-HE3_A-08	yes			(8 mm tape on 7" reel)	15 000	
	MMBD7000-E3-18	no			10 000	10 000	
	MMBD7000-HE3_A-18	yes			(8 mm tape on 13" reel)	10 000	

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER TEST CONDITION		SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	100	V	
Forward current (continuous) ⁽¹⁾		١ _F	350	mA	
Non-repetitive peak forward current ⁽¹⁾ t = 1 s		I _{FSM}	500	mA	
Power dissipation	on FR-4 board with recommended soldering footprint	р	270	mW	
Power dissipation	Infinite heatsink	P _{tot}	390	mW	

Note

(1) Infinite heatsink

THERMAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air	according to JEDEC [®] 51-3 on FR-4 board with recommended soldering footprint		460	K/W		
Thermal resistance junction to lead	Infinite heatsink	R _{thJL}	320	K/W		
Maximum junction temperature		Tj	150	°C		
Storage temperature range		T _{stg}	-65 to +150	°C		
Operating temperature range		T _{op}	-55 to +150	°C		

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MMBD7000

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	MAX.	UNIT	
Reverse breakdown voltage	I _R = 100 μA	V _(BR)	100		V	
	V _R = 50 V	I _R		400	nA	
Leakage current	V _R = 100 V	I _R		3	μA	
	V _R = 50 V, T _j = 125 °C	I _R		100	μA	
	$I_F = 1 \text{ mA}$	V _F	0.55	0.70	V	
Forward voltage	I _F = 10 mA	V _F	0.67	0.82	V	
	I _F = 100 mA	V _F	0.75	1.10	V	
Diode capacitance	$V_{R} = 0, f = 1 MHz$	CD		1.5	pF	
Reverse recovery time	$I_F = I_R = 10 \text{ mA}, i_R = 1 \text{ mA}, R_L = 100 \Omega$	t _{rr}		4	ns	

TYPICAL CHARACTERISICS (T_{amb} = 25 °C, unless otherwise specified)

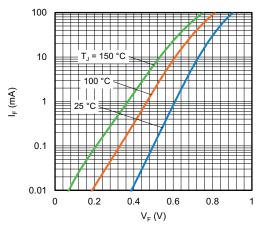


Fig. 1 - Forward Current vs. Forward Voltage

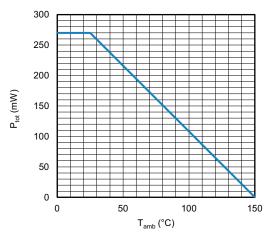


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature

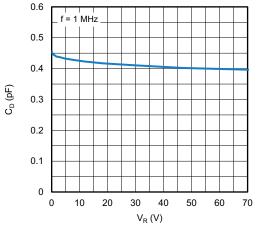


Fig. 3 - Typical Capacitance vs. Reverse Voltage

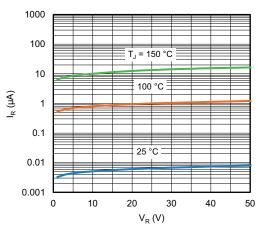


Fig. 4 - Typical Reverse Leakage Current vs. Reverse Voltage

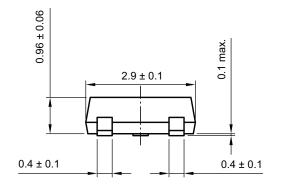
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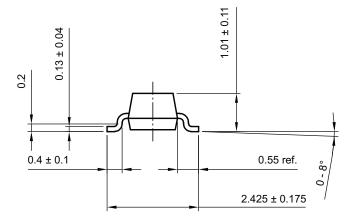
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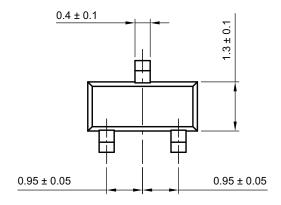


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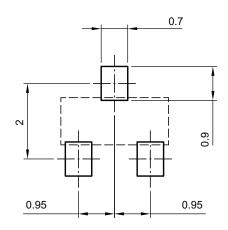
PACKAGE DIMENSIONS in millimeters: SOT-23







footprint recommendation:

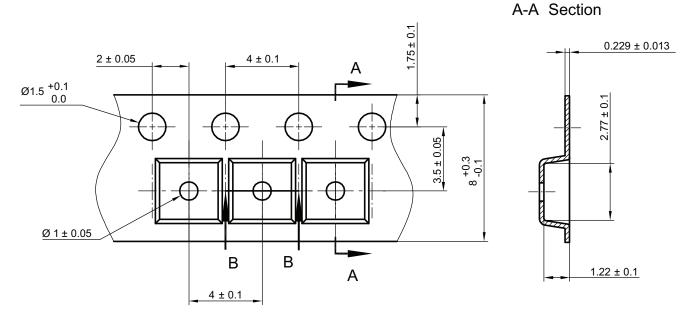


Created - Date: 18-Oct-2021 Rev. 01 - Date: 18-Jan-2022 S8-V-3929.01-009 (4)

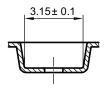




CARRIER TAPE SOT-23

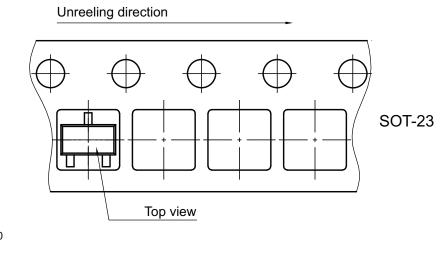


B-B Section



Created Date: 04-Feb-2010 Rev. Date: 07-Feb-2022 S8-V-3929.01-005 (4)

ORIENTATION IN CARRIER TAPE SOT-23



Created Date: 04-Feb-2010 Rev. Date: 07-Nov-2022 S8-V-3929.01-005 (4)

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