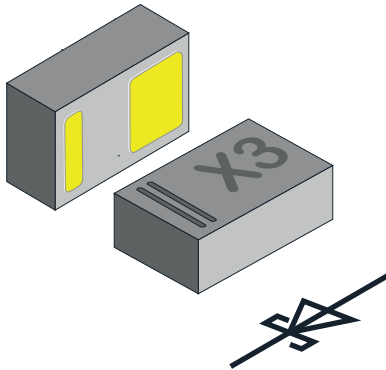


Small Signal Schottky Diode FlipKY[®] Gen 3



FEATURES

- Schottky diode for high-speed switching
- Very low dimensions:
0.8 mm x 0.5 mm x 0.29 mm
- 0.3 A forward current
- Low forward voltage drop (typ. 388 mV at 300 mA)
- Low reverse current (< 4 μ A at 10 V)
- AEC-Q101 qualified
- Material categorization:
for definitions of compliance please see www.vishay.com/doc?99912

 AUTOMOTIVE
GRADE

RoHS
COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

LINKS TO ADDITIONAL RESOURCES



PARTS TABLE

PART	GRADE	ORDERING CODE	CIRCUIT CONFIGURATION	PACKAGE NAME	TYPE MARKING	WEIGHT	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY
VSKY033085	AEC-Q101	VSKY033085HG4-08	Single	CLP0805-2L	X3	0.271 mg	10 000	10 000

ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_R	30	V
Forward continuous current		I_F	300	mA
Surge forward current	Single pulse; 8.3 ms half sine-wave	I_{FSM}	9	A

THERMAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Thermal resistance junction to soldering point	Acc. JEDEC [®] JESD51-14	R_{thJS}	12	K/W
Maximum operating junction temperature		$T_j \text{ max.}$	150	$^{\circ}\text{C}$
Storage temperature range		T_{stg}	-65 to +150	$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	TYP.	MAX.	UNIT
Leakage current	$V_R = 10\text{ V}$	I_R	-	4	μA
Leakage current	$V_R = 30\text{ V}$	I_R	-	15	μA
Forward voltage	$I_R = 10\text{ mA}$	V_F	270	330	mV
Forward voltage	$I_R = 100\text{ mA}$	V_F	341	402	mV
Forward voltage	$I_R = 300\text{ mA}$	V_F	388	465	mV
Diode capacitance	$V_R = 0\text{ V}, f = 1\text{ MHz}$	C_D	150	-	pF

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)

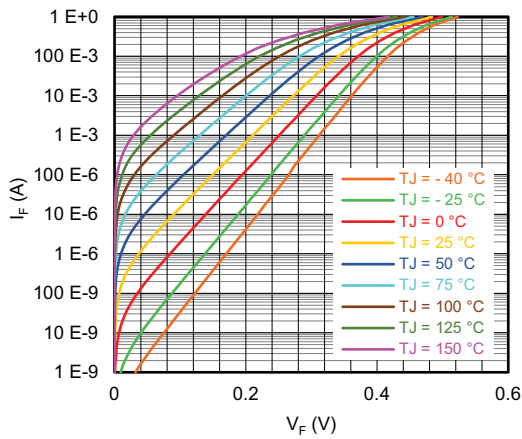


Fig. 1 - Typical Forward Current vs. Forward Voltage at Various Temperatures

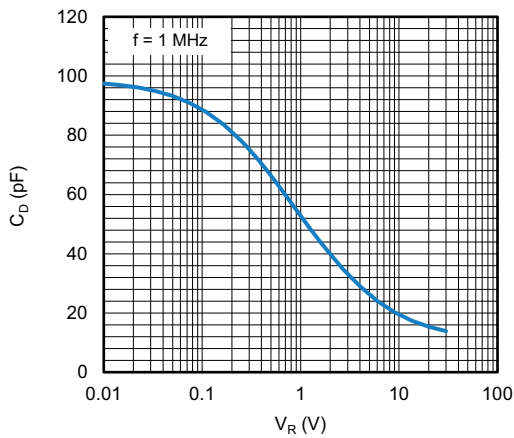


Fig. 2 - Typical Capacitance vs. Reverse Voltage

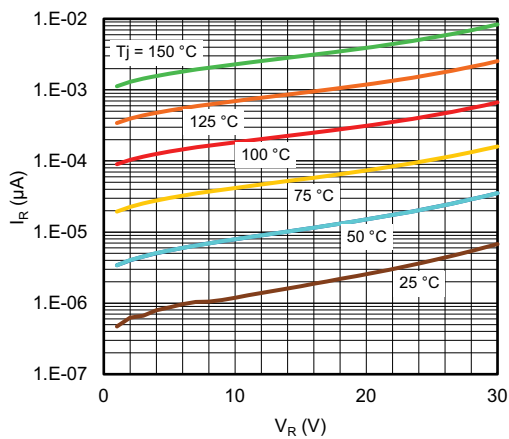
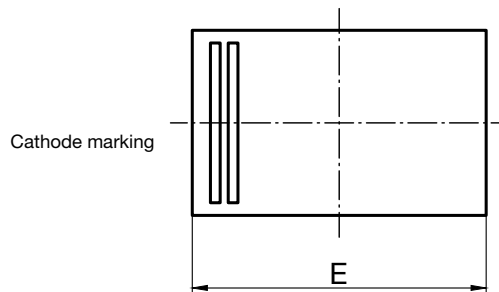
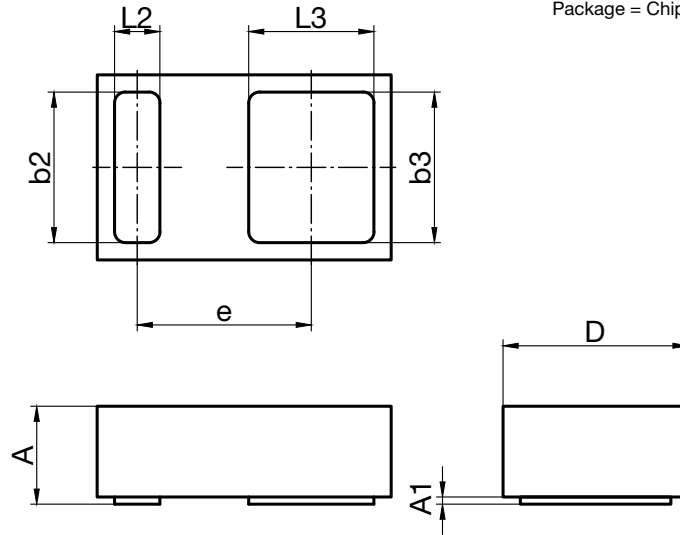


Fig. 3 - Typical Reverse Leakage Current vs. Reverse Voltage

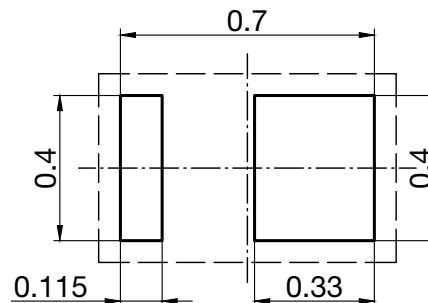
PACKAGE DIMENSIONS in millimeters: **CLP0805-2L**

Package = Chip Dimensions in mm



	min	max
A	0.25	0.29
A1		0.02
b2	0.39	0.44
b3	0.39	0.44
D	0.49	0.53
E	0.79	0.83
e	0.48	
L2	0.10	0.15
L3	0.32	0.37

foot print recommendation:



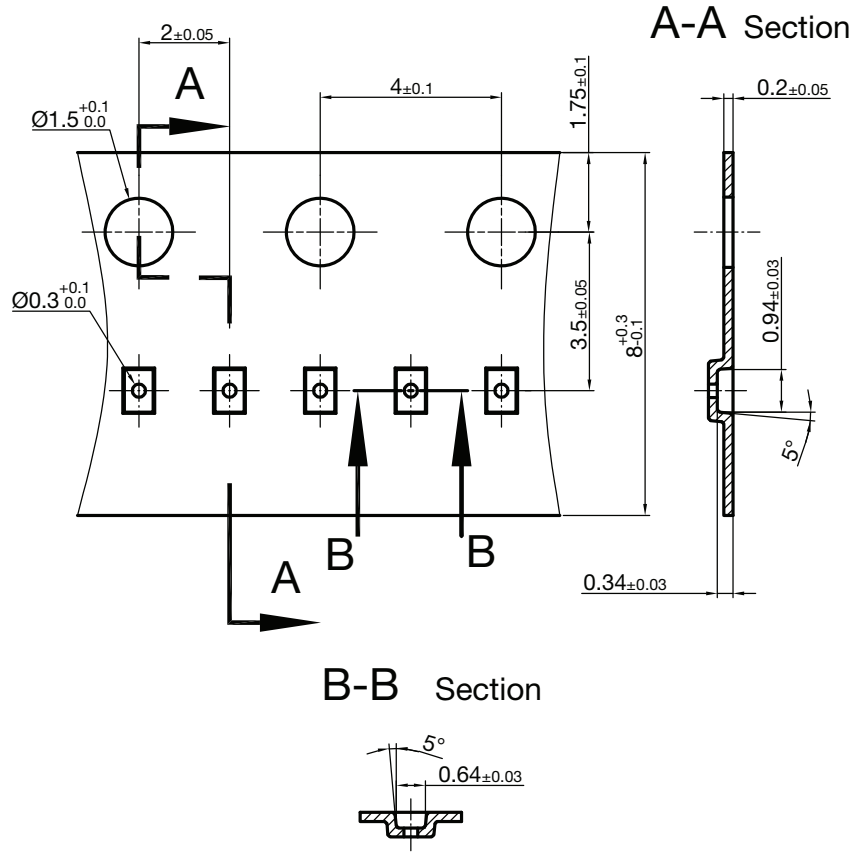
Document no.:S8-V-3906.04-056 (4)
 Created - Date: 03. July 2018
 Rev.1 - Date: 19. Dec. 2023
 23235

Footprint and soldering recommendation:

please see Application Note: www.vishay.com/doc?85917



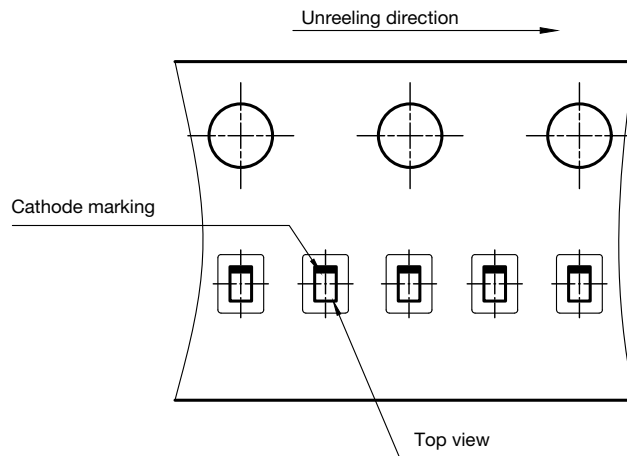
CARRIER TAPE in millimeters: **CLP0805-2L**



S8-V-3906.04-057 (4)
created Date: 03.07.2018
23237

Cummulative tolerances of 10 sprocket holes is ± 0.2 mm

ORIENTATION IN CARRIER CLP0805-2L



S8-V-3906.04-058 (4)
created Date: 03.07.2018
Rev.1: 19.12.2023
23236



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