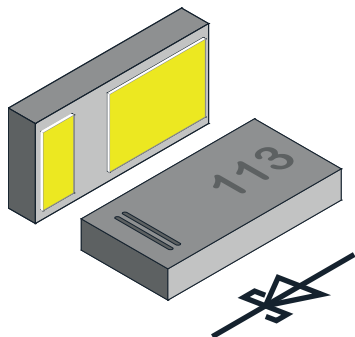


Schottky Rectifier Surface-Mount FlipKY[®] Gen 3



FEATURES

- Schottky diode for high-speed switching
- Very low dimensions: 1.6 mm x 0.8 mm x 0.29 mm
- 2.0 A forward current
- Low forward voltage drop (typ. 500 mV at 2000 mA)
- Low reverse current (< 25 μ A at 10 V)
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

LINKS TO ADDITIONAL RESOURCES



PARTS TABLE								
PART	GRADE	ORDERING CODE	CIRCUIT CONFIGURATION	PACKAGE NAME	TYPE MARKING	WEIGHT	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY
VSKY2030G8	AEC-Q101	VSKY2030G8HG4-08	Single	CLP1608-2L	113	0.84 mg	5000	5000

ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_R	30	V
Forward continuous current		I_F	2000	mA
Surge forward current	Single pulse; 8.3 ms half sine-wave	I_{FSM}	28	A

THERMAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Thermal resistance junction to soldering point	Acc. JEDEC [®] JESD51-41	R_{thJS}	6	K/W
Maximum operating junction temperature		$T_j \text{ max.}$	150	$^{\circ}\text{C}$
Storage temperature range		T_{stg}	-65 to + 150	$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	TYP.	MAX.	UNIT
Leakage current	$V_R = 10\text{ V}$	I_R	-	25	μA
Leakage current	$V_R = 30\text{ V}$	I_R	-	96	μA
Forward voltage	$I_R = 100\text{ mA}$	V_F	290	342	mV
Forward voltage	$I_R = 1000\text{ mA}$	V_F	400	464	mV
Forward voltage	$I_R = 2000\text{ mA}$	V_F	500	558	mV
Diode capacitance	$V_R = 0\text{ V}$, $f = 1\text{ MHz}$	C_D	375	-	pF



RATINGS AND CHARACTERISTICS CURVES ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)

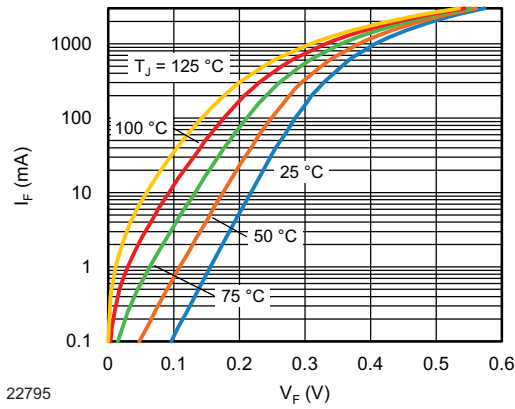


Fig. 1 - Typical Forward Current vs. Forward Voltage at Various Temperatures

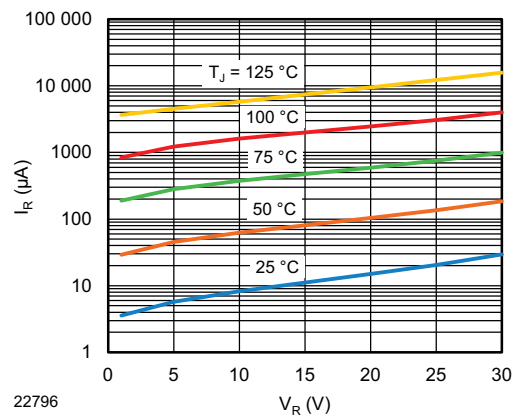


Fig. 2 - Typical Reverse Current vs. Reverse Voltage at Various Temperatures

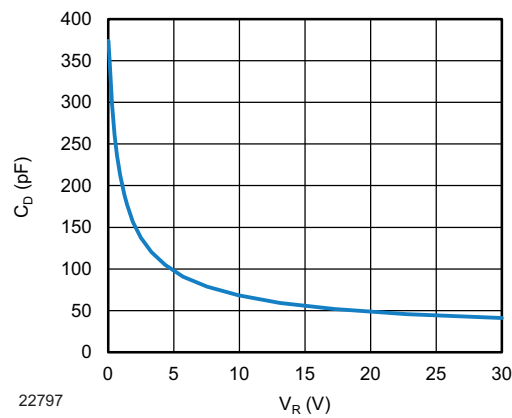
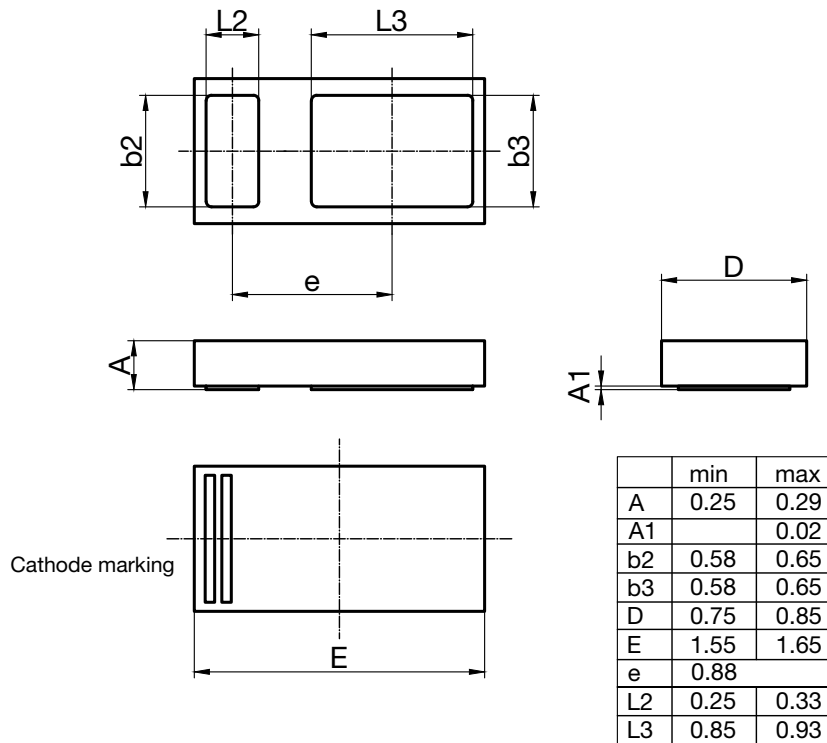


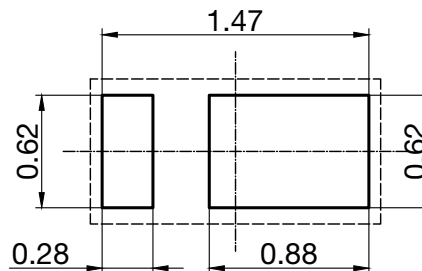
Fig. 3 - Typical Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters: **CLP1608-2L**

Package=Chip Dimensions in mm



foot print recommendation:

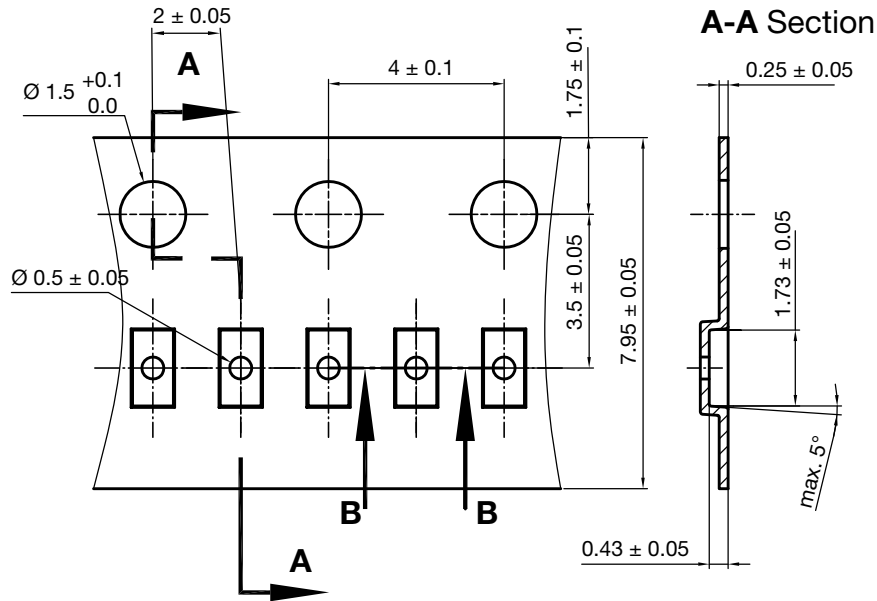


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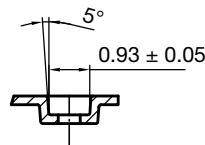
Footprint and soldering recommendation:

 please see Application Note: www.vishay.com/doc?85917

CARRIER TAPE in millimeters: **CLP1608-2L**



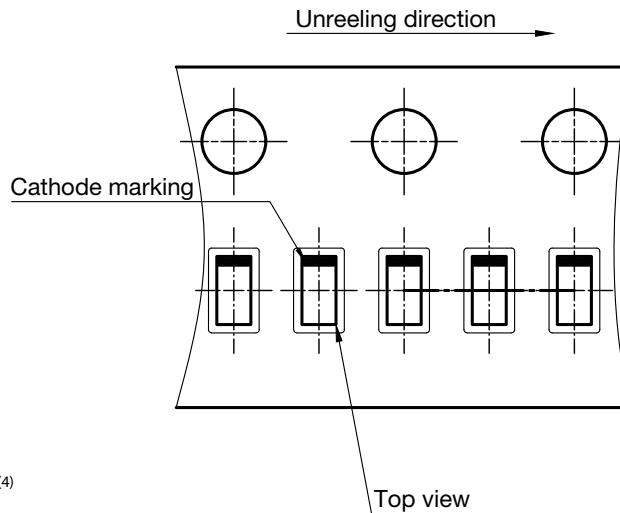
B-B Section



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 Rev.2 - Date: 28. Jan. 2016
 23064

Cummlative tolerances of 10 sprocket holes is ± 0.2 mm

ORIENTATION IN CARRIER CLP1608-2L



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