FEATURES

Trench MOS Schottky technology

- Low forward voltage drop, low power losses
- · High efficiency operation
- Solder bath temperature 275 °C maximum, 10 s per JESD 22-B106
- AEC-Q101 gualified available: Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Case: TO-247AD 3L

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

Mounting torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER Maximum repetitive peak reverse voltage		SYMBOL	VX80M100PW	UNIT V	
		V _{RRM}	100		
Maximum average forward rectified current	per device	I _{F(AV)}	80		
(fig. 1)	per diode		40	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	550	A	
Operating junction temperature range		T _J ⁽¹⁾	-40 to +175	°C	
Storage temperature range		T _{STG}	-40 to +175		

Note

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{BJA}$

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VX80M100PW

Vishay General Semiconductor

Dual High Voltage TMBS[®] (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.44$ V at $I_F = 10$ A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 40 A			
V _{RRM}	100 V			
I _{FSM}	550 A			
V_F at I_F = 40 A (T_J = 125 °C)	0.66 V			
T _J max.	175 °C			
Package	TO-247AD 3L			
Circuit configuration	Common cathode			



GRAD



HALOGEN

FREE



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ELECTRICAL CHARACTERISTIC	CS (T _J = 25 °C	C unless othe	rwise noted)	1		
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
	I _F = 10 A	T _J = 25 °C	V _F (1)	0.52	-	V
	I _F = 20 A			0.61	-	
Instantaneous forward voltage per diode	I _F = 40 A			0.77	0.84	
	I _F = 10 A	T _J = 125 °C		0.44	-	
	I _F = 20 A			0.55	-	
	I _F = 40 A			0.66	0.74	
Reverse current at rated V _R per diode	V _R = 70 V	$T_{\rm J} = 25 ^{\circ}{\rm C}$	I _R (2)	0.0055	-	mA
	v _R = 70 v	T _J = 125 °C		5	-	
	V _R = 100 V	T _J = 25 °C		-	0.6	
	$v_{\rm R} = 100 \rm v$	T _J = 125 °C		9	35	
Typical junction capacitance	4.0 V,	1 MHz	CJ	3400	-	pF

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL VX80M100PW UNIT			
Typical thermal resistance per device	R _{θJC} ⁽¹⁾	0.6	°C/W	

Note

⁽¹⁾ Thermal resistance junction-to-case to follow JEDEC[®] 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
VX80M100PW-M3/P	5.64	Р	25/tube	Tube		
VX80M100PWHM3/P ⁽¹⁾	5.64	Р	25/tube	Tube		

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C, unless otherwise noted)

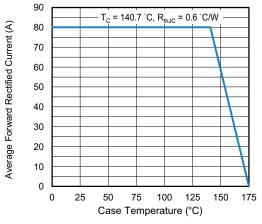


Fig. 1 - Maximum Forward Current Derating Curve

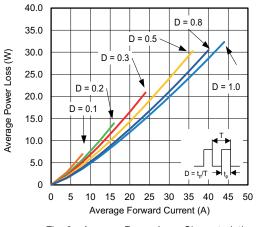


Fig. 2 - Average Power Loss Characteristics

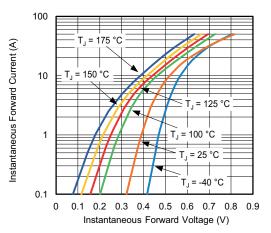


Fig. 3 - Typical Instantaneous Forward Characteristics

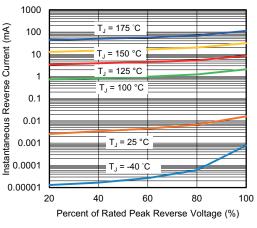


Fig. 4 - Typical Reverse Leakage Characteristics

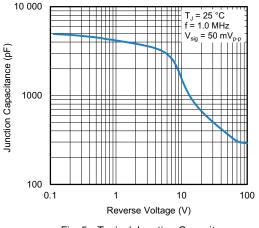


Fig. 5 - Typical Junction Capacitance

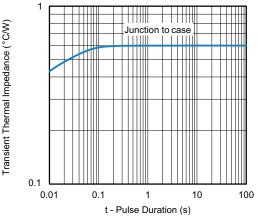


Fig. 6 - Typical Transient Thermal Impedance

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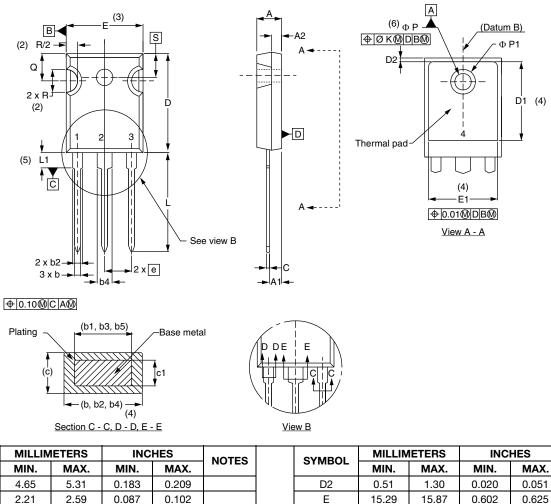
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PACKAGE OUTLINE DIMENSIONS in millimeters (inches) TO-247AD 3L



b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
с	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

2.49

0.059

0.098

0.51 15.29 13.46 5.46	1.30 15.87 - BSC	0.020 0.602 0.53	0.051 0.625 -	3
13.46 5.46	-	0.53	0.625	3
5.46	- BSC		-	
	BSC	0.015		
		0.215	BSC	
0.2	54	0.0	10	
19.81	20.32	0.780	0.800	
3.71	4.29	0.146	0.169	
3.56	3.66	0.14	0.144	
-	6.98	-	0.275	
5.31	5.69	0.209	0.224	
4.52	5.49	0.178	0.216	
5.51	BSC	0.217	BSC	
	3.71 3.56 - 5.31 4.52	3.71 4.29 3.56 3.66 - 6.98 5.31 5.69	3.71 4.29 0.146 3.56 3.66 0.14 - 6.98 - 5.31 5.69 0.209 4.52 5.49 0.178	3.71 4.29 0.146 0.169 3.56 3.66 0.14 0.144 - 6.98 - 0.275 5.31 5.69 0.209 0.224 4.52 5.49 0.178 0.216

Notes

SYMBOL

A

A1

A2

1.50

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

⁽³⁾ Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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NOTES

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