V8PM63

Vishay General Semiconductor

# High Current Density Surface-Mount TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.43$  V at  $I_F = 4$  A

# eSMP<sup>®</sup> Series

K O Anode 1 Cathode O Anode 2

## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	8.0 A			
V <sub>RRM</sub>	60 V			
I <sub>FSM</sub>	140 A			
$V_F$ at $I_F$ = 8.0 A ( $T_J$ = 125 °C)	0.52 V			
T <sub>J</sub> max.	175 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

## FEATURES

- Very low profile typical height of 1.1 mm
- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
   Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

## MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V8PM63	UNIT	
Device marking code		8M63		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	60	V	
Maximum average forward rectified current (fig. 1)	I <sub>F</sub> <sup>(1)</sup>	8.0	Α	
	I <sub>F</sub> <sup>(2)</sup>	4.3		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	140	A	
Operating junction temperature range T <sub>J</sub> <sup>(3)</sup>		-40 to +175	°C	
Storage temperature range	T <sub>STG</sub>	-55 to +175	°C	

Notes

(1) Mounted on 30 mm x 30 mm pad areas aluminum PCB

<sup>(2)</sup> Free air, mounted on recommended copper pad area

 $^{(3)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP\_D/dT\_J <1/  $R_{\theta JA}$ 

 Revision: 01-Jun-2023
 1
 Document Number: 87025

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_J = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	$I_{F} = 4.0 \text{ A}$	T <sub>J</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.52	-	V
	I <sub>F</sub> = 8.0 A			0.58	0.64	
	I <sub>F</sub> = 4.0 A	– T <sub>J</sub> = 125 °C		0.43	-	
	I <sub>F</sub> = 8.0 A			0.52	0.57	
Reverse current	V <sub>B</sub> = 60 V	T <sub>J</sub> = 25 °C T <sub>J</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	-	0.02	- mA
	$v_{\rm R} = 60 v$			1.2	3.5	
Typical junction capacitance	4.0 V, 1 MHz		CJ	1460	-	pF

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}$  Pulse test: pulse width  $\leq 5~ms$ 

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V8PM63	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	75	°C/W	
Typical thermal resistance	R <sub>θJM</sub> <sup>(3)</sup>	4		

#### Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(2)</sup> Free air mounted on recommended copper pad area; thermal resistance R<sub>0JA</sub> - junction to ambient

 $^{(3)}$  Mounted on 30 mm x 30 mm aluminum PCB; thermal resistance  $R_{\theta JM}$  - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V8PM63-M3/H	0.10	Н	1500	7" diameter plastic tape and reel	
V8PM63-M3/I	0.10	I	6500	13" diameter plastic tape and reel	
V8PM63HM3/H <sup>(1)</sup>	0.10	Н	1500	7" diameter plastic tape and reel	
V8PM63HM3/I <sup>(1)</sup>	0.10	I	6500	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

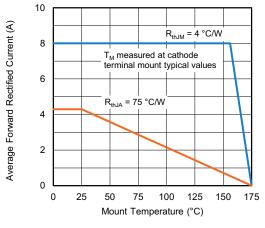


Fig. 1 - Forward Current Derating Curve

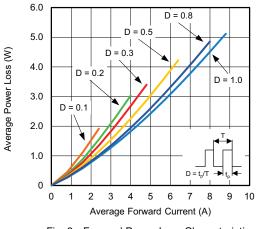


Fig. 2 - Forward Power Loss Characteristics

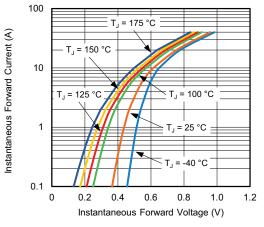


Fig. 3 - Typical Instantaneous Forward Characteristics

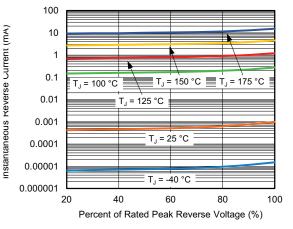
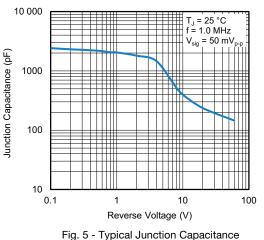
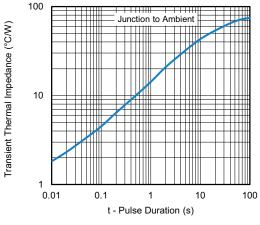


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode







Revision: 01-Jun-2023

3

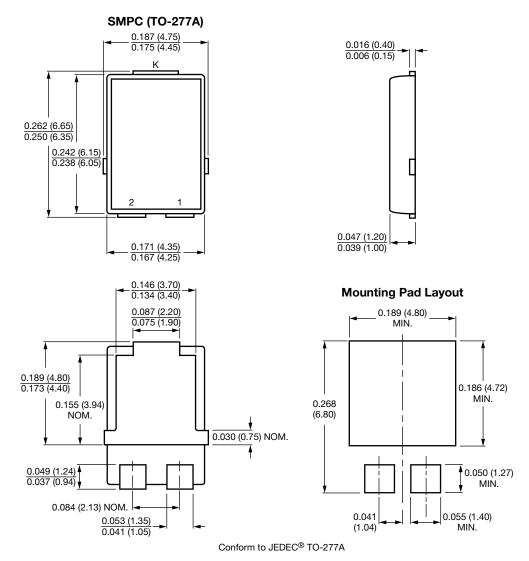
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## PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



 Revision: 01-Jun-2023
 4
 Document Number: 87025

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1