Ultra Low $V_F = 0.31$ V at $I_F = 5.0$ A

FEATURES

Dual Low-Voltage TMBS[®] (Trench MOS Barrier Schottky) Rectifier

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
 Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test **Polarity:** as marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V40D60C	UNIT	
Device marking code			V40D60C		
Maximum repetitive peak reverse voltage		V _{RRM}	60	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)} ⁽¹⁾	40	А	
	per diode		20		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	250	А	
Operating junction temperature range		T _J ⁽²⁾	-40 to +150	°C	
Storage temperature range		T _{STG}	-55 to +150		

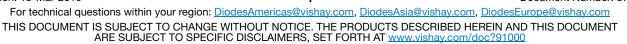
Notes

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{0JA}

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DESIGN SUPPORT TOOLS AVAILABLE

Anode 1 O-

Anode 2 C

Top View

eSMP[®] Series

SMPD (TO-263AC)

Bottom View

Cathode



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 20 A				
V _{RRM}	60 V				
I _{FSM}	250 A				
V_F at I_F = 20 A (T_A = 125 °C)	0.47 V				
T _J max.	150 °C				
Package	SMPD (TO-263AC)				
Circuit configuration	Common cathode				

V40D60C

Vishay General Semiconductor

Pro

AUTOMOTIV GRAD



HALOGEN



V40D60C



Vishay General Semiconductor

ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	$I_F = 5 A$	T _A = 25 °C	V _F (1)	0.43	-	V	
	I _F = 10 A			0.47	-		
	I _F = 20 A			0.54	0.62		
	$I_F = 5 A$	T _A = 125 °C		0.31	-		
	I _F = 10 A			0.38	-		
	I _F = 20 A			0.47	0.55		
Reverse current per diode		T _A = 25 °C	I _R ⁽²⁾	-	4	mA	
		T _A = 125 °C		17	50		
Typical junction capacitance	4.0 V, 1 MHz		CJ	3020	-	pF	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V40D60C	UNIT		
Typical thermal resistance per device	R _{0JC} ⁽¹⁾	0.8	°C/W		
	R _{0JA} (2)(3)	45	0/10		

Notes

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{θ JA}

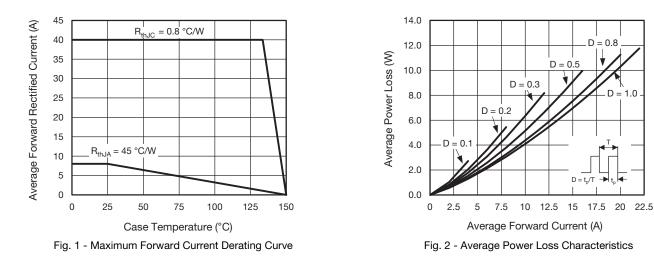
⁽³⁾ Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V40D60C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V40D60CHM3/I (1)	0.55	l	2000/reel	13" diameter plastic tape and reel		

Note

⁽¹⁾ AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



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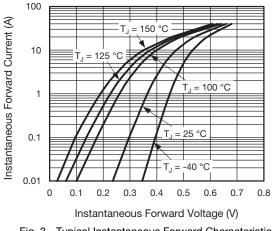
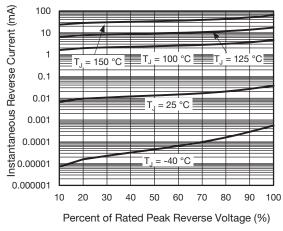


Fig. 3 - Typical Instantaneous Forward Characteristics





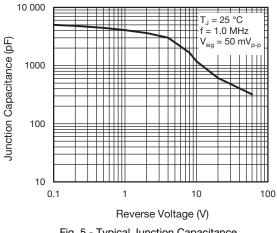


Fig. 5 - Typical Junction Capacitance

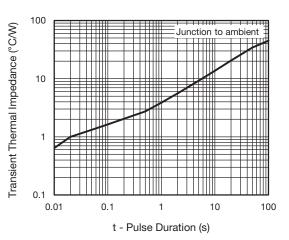
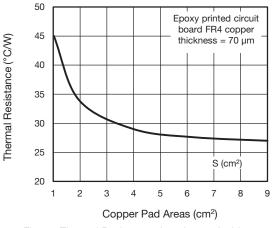
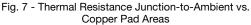


Fig. 6 - Typical Transient Thermal Impedance





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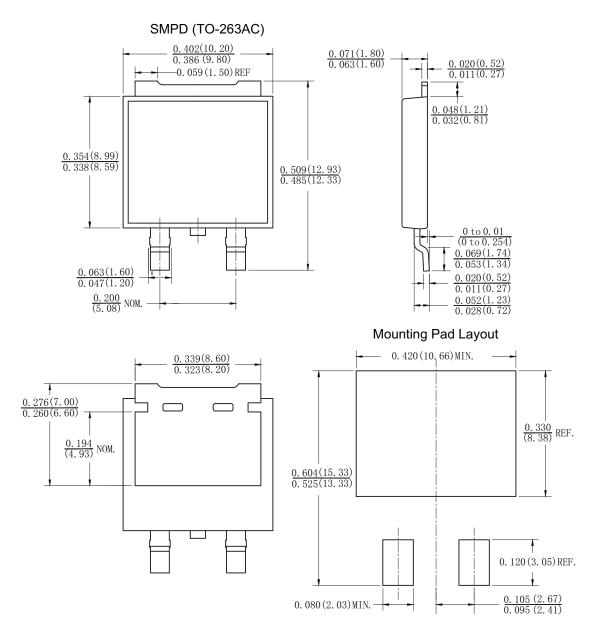
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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