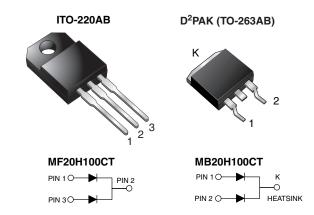
## MB20H100CT, MF20H100CT

Vishay General Semiconductor

HALOGEN FREE

## **Dual Common Cathode High Voltage Schottky Rectifier**

High Barrier Technology for Improved High Temperature Performance



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 10 A				
V <sub>RRM</sub>	100 V				
I <sub>FSM</sub>	250 A				
I <sub>R</sub>	4.5 μA				
V <sub>F</sub>	0.64				
T <sub>J</sub> max.	175 °C				
Package	ITO-220AB, D <sup>2</sup> PAK (TO-263AB)				
Circuit configuration	Common cathode				

#### **FEATURES**

- Power pack
- Guardring for overvoltage protection
- Low power loss, high efficiency
- Low forward voltage drop
- · Low leakage current
- High forward surge capability
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for D<sup>2</sup>PAK (TO-263AB) package)
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for ITO-220AB package)
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

For use in high frequency rectifier of switching mode power supplies, freewheeling diodes, DC/DC converters and polarity protection application.

#### **MECHANICAL DATA**

Case: ITO-220AB, D2PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating

Base P/NHE3\_X - RoHS-compliant, AEC-Q101 qualified ("\_X" denotes revision code e.g. A, B,.....)

Base P/NHM3 - RoHS-compliant, halogen-free, AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

HE3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

PARAMETER		SYMBOL	MB20H100CT	MF20H100CT	UNIT	
Device marking code			MBRB20H100CT	MBRF20H100CT		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	100			
Working peak reverse voltage		V <sub>RWM</sub> 100		00	V	
Maximum DC blocking voltage		V <sub>DC</sub>	100			
Maximum average femored rectified average	total device	1	2	0		
Maximum average forward rectified current	per diode	I <sub>F(AV)</sub>	10			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	250		Α	
Peak repetitive reverse current per diode at t <sub>p</sub> = 2.0 µs, 1 kHz		I <sub>RRM</sub>	1.0			
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000		V/µs	
Operating junction and storage temperature range		T <sub>J</sub> . T <sub>STG</sub>	-65 to	+175	°C	
Isolation voltage (ITO-220AB only) from terminal to heatsink t = 1 min		V <sub>AC</sub>	1500		V	



# MB20H100CT, MF20H100CT

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUE	UNIT	
Maximum instantaneous forward voltage per diode	V <sub>F</sub> <sup>(1)</sup>	I <sub>F</sub> = 10 A	T <sub>C</sub> = 25 °C	0.77	V	
		I <sub>F</sub> = 10 A	T <sub>C</sub> = 125 °C	0.64		
		I <sub>F</sub> = 20 A	T <sub>C</sub> = 25 °C	0.88		
		I <sub>F</sub> = 20 A	T <sub>C</sub> = 125 °C	0.73		
Maximum reverse current at working peak reverse voltage per diode	I <sub>R</sub> <sup>(2)</sup>	Rated V <sub>R</sub>	T <sub>J</sub> = 25 °C	4.5	μΑ	
			T <sub>J</sub> = 125 °C	6.0	mA	

#### Notes

 $^{(1)}$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	MB20H100CT	MF20H100CT	UNIT	
Typical thermal resistance per diode	$R_{ heta JC}$	2.0	5.8	°C/W	

ORDERING INFORMATION						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ITO-220AB	MF20H100CTHE3_B/P	1.99	Р	50/tube	Tube	
D <sup>2</sup> PAK (TO-263AB)	MB20H100CTHM3/I	1.35	ı	800/reel	Tape and reel	

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### **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

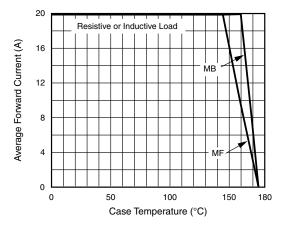


Fig. 1 - Forward Current Derating Curve

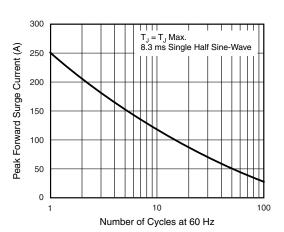


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

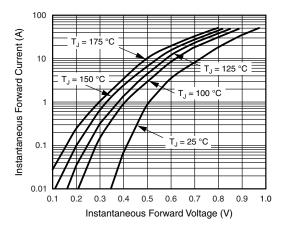


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

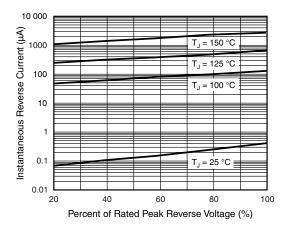


Fig. 4 - Typical Reverse Characteristics Per Diode

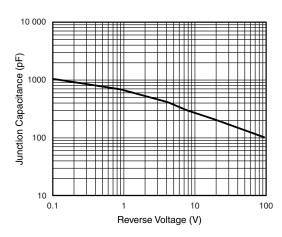


Fig. 5 - Typical Junction Capacitance Per Diode

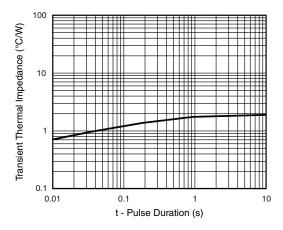
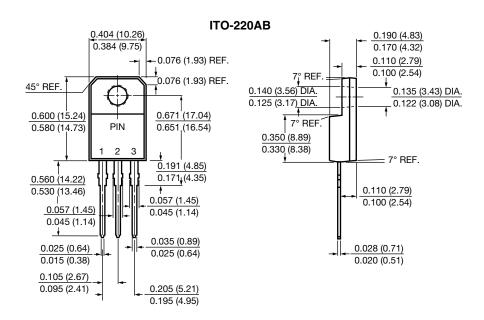


Fig. 6 - Typical Transient Thermal Impedance Per Diode

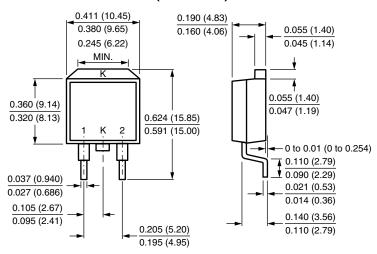


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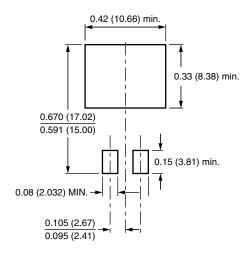
### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



### D<sup>2</sup>PAK (TO-263AB)



### **Mounting Pad Layout**





## **Legal Disclaimer Notice**

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