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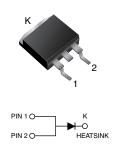


Vishay General Semiconductor

# TMBS® (Trench MOS Barrier Schottky) Rectifier for PV Solar Cell Bypass Protection

Ultra Low  $V_F = 0.28 \text{ V}$  at  $I_F = 5 \text{ A}$ 

#### D<sup>2</sup>PAK (TO-263AB)



#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS					
I <sub>F(DC)</sub>	40 A				
V <sub>RRM</sub>	45 V				
I <sub>FSM</sub>	240 A				
V <sub>F</sub> at I <sub>F</sub> = 40 A	0.51 V				
T <sub>OP</sub> max. (AC mode)	150 °C				
T <sub>J</sub> max. (DC forward current)	200 °C				
Package	D <sup>2</sup> PAK (TO-263AB)				
Circuit configuration	Single				

#### **FEATURES**

- · Trench MOS Schottky technology
- · Low forward voltage drop, low power losses

High efficiency operation

Compliant
HALOGEN

HALOGEN

 Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

#### **MECHANICAL DATA**

Case: D<sup>2</sup>PAK (TO-263AB)

Epoxy meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant and

commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	VBT4045BP	UNIT		
Maximum repetitive peak reverse voltage	$V_{RRM}$	45	V		
Maximum DC forward bypassing current (fig. 1)	I <sub>F(DC)</sub> (1)	40	Α		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	240	Α		
Operating junction temperature range (AC mode)	T <sub>OP</sub>	-40 to +150	°C		
Junction temperature in DC forward current without reverse bias, $t \le 1\ h$	T <sub>J</sub> <sup>(1)</sup>	≤ 200	°C		

#### Notes

<sup>(1)</sup> With heatsink

<sup>(2)</sup> Meets the requirements of IEC 61215 Ed. 2 bypass diode thermal test



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.41	-	V
	I <sub>F</sub> = 20 A			0.50	=	
	I <sub>F</sub> = 40 A			0.57	0.67	
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.28	=	
	I <sub>F</sub> = 20 A			0.41	=	
	I <sub>F</sub> = 40 A			0.51	0.63	
Reverse current	V <sub>R</sub> = 45 A	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	3000	μΑ
	v <sub>R</sub> = 45 A	T <sub>A</sub> = 125 °C		29	85	mA

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)			
PARAMETER	SYMBOL	VBT4045BP	UNIT
Typical thermal resistance	$R_{\theta JC}$	0.8	°C/W

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
D <sup>2</sup> PAK (TO-263AB)	VBT4045BP-M3/4W	1.37	4W	50/tube	Tube		
D <sup>2</sup> PAK (TO-263AB)	VBT4045BP-M3/8W	1.37	8W	800/reel	Tape and reel		

### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

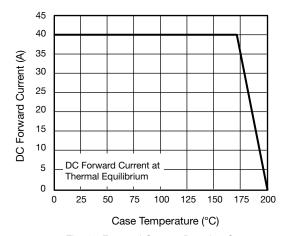


Fig. 1 - Forward Current Derating Curve

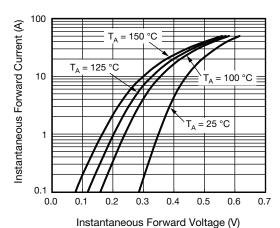
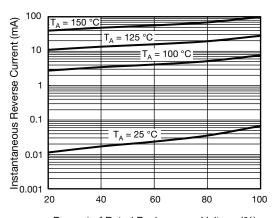


Fig. 2 - Typical Instantaneous Forward Characteristics



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Percent of Rated Peak reverse Voltage (%)

Fig. 3 - Typical Reverse Characteristics

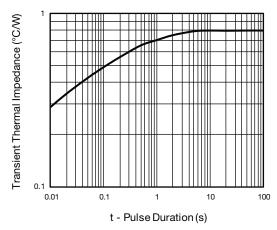


Fig. 5 - Typical Transient Thermal Impedance

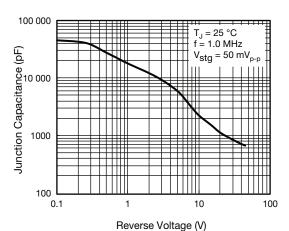
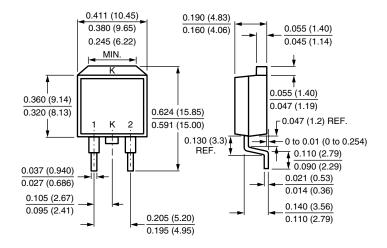


Fig. 4 - Typical Junction Capacitance

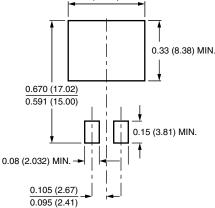
#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

#### D<sup>2</sup>PAK (TO-263AB)



## 0.42 (10.66) MIN

**Mounting Pad Layout** 





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