Vishay General Semiconductor

# **Dual Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.52$  V at  $I_F = 5$  A



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## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 10 A				
V <sub>RRM</sub>	80 V				
I <sub>FSM</sub>	100 A				
$V_F$ at $I_F = 10 A$	0.60 V				
T <sub>J</sub> max.	150 °C				
Package	D <sup>2</sup> PAK (TO-263AB)				
Circuit configurations	Common cathode				

### FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum FREE peak of 245 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

## **MECHANICAL DATA**

**Case:** D<sup>2</sup>PAK (TO-263AB) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

#### Polarity: as marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	VBT2080C	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	80	V	
Maximum average forward rectified current (fig. 1)	per device	- I <sub>F(AV)</sub>	20	A	
	per diode		10		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	100	А	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode <sup>(1)</sup>	I <sub>F</sub> = 5 A	– T <sub>A</sub> = 25 °C	V <sub>F</sub>	0.57	-	V	
	$I_F = 10 \text{ A}$			0.67	0.81		
	$I_F = 5 A$	- T <sub>A</sub> = 125 °C		0.52	-		
	I <sub>F</sub> = 10 A			0.60	0.70		
Reverse current per diode <sup>(2)</sup>	V <sub>R</sub> = 80 V	T <sub>A</sub> = 25 °C	1	20	600	μA	
		T <sub>A</sub> = 125 °C	I <sub>R</sub>	10	20	mA	

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

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<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	VBT2080C	UNIT	
Turning thermal registering	per diode	R <sub>θJC</sub>	3.0	°C/W	
Typical thermal resistance	per device		2.0		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
D <sup>2</sup> PAK (TO-263AB)	VBT2080C-M3/4W	1.36	4W	50/tube	Tube	
D <sup>2</sup> PAK (TO-263AB)	VBT2080C-M3/8W	1.36	8W	800/reel	Tape and reel	

## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25 \text{ °C}$ unless otherwise noted)

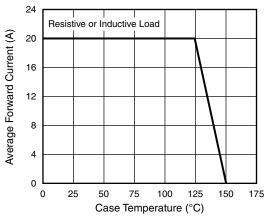


Fig. 1 - Maximum Forward Current Derating Curve

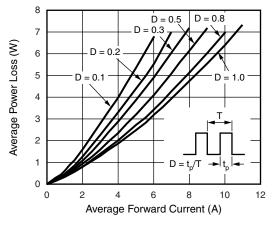


Fig. 2 - Forward Power Loss Characteristics Per Diode

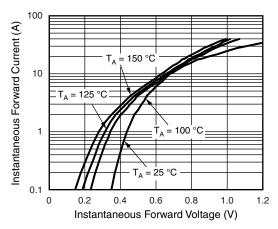


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

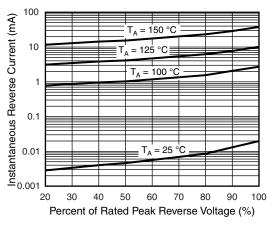
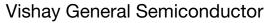
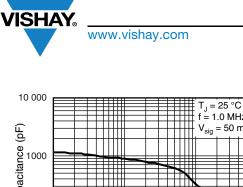


Fig. 4 - Typical Reverse Characteristics Per Diode

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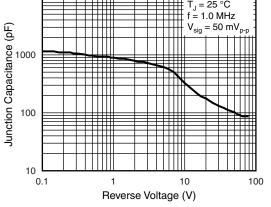


Fig. 5 - Typical Junction Capacitance Per Diode

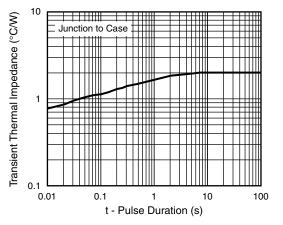
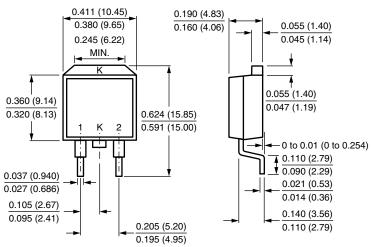


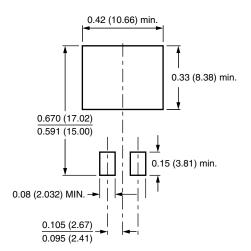
Fig. 6 - Typical Transient Thermal Impedance Per Device

**PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



D<sup>2</sup>PAK (TO-263AB)

## **Mounting Pad Layout**





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