HALOGEN

FREE



Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.50 \text{ V}$ at $I_F = 5 \text{ A}$



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 10 A			
V _{RRM}	100 V			
I _{FSM}	150 A			
V _F at I _F = 10 A	0.58 V			
T _J max.	150 °C			
Package	D ² PAK (TO-263AB)			
Circuit configuration	Common cathode			

FEATURES

Trench MOS Schottky technology

· Low forward voltage drop, low power losses

• High efficiency operation

 Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C

 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: D²PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	VB20100C	UNIT
Maximum repetitive peak reverse voltage		V _{RRM}	100	V
Maximum average forward rectified current (fig. 1)	per device	ı	20	^
	per diode	I _{F(AV)}	10	A A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	150	А
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode (1)	I _F = 5 A	T _A = 25 °C	V _F	0.55	-	V	
	I _F = 10 A			0.65	0.79		
	I _F = 5 A	T _A = 125 °C		0.50	-		
	I _F = 10 A			0.58	0.68		
Reverse current per diode (2)	V _R = 70 V	T _A = 25 °C	I _R	17	-	μΑ	
	V _R = 70 V	T _A = 125 °C		5.3	-	mA	
	V _R = 100 V	T _A = 25 °C		-	800	μΑ	
	v _R = 100 v	T _A = 125 °C		12	25	mA	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)			
PARAMETER	SYMBOL	VB20100C	UNIT
Typical thermal resistance per diode	$R_{\theta JC}$	2.8	°C/W

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
D ² PAK (TO-263AB)	VB20100C-M3/4W	1.39	4W	50/tube	Tube	
D ² PAK (TO-263AB)	VB20100C-M3/8W	1.39	8W	800/reel	Tape and reel	

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

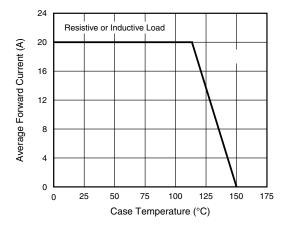


Fig. 1 - Maximum Forward Current Derating Curve

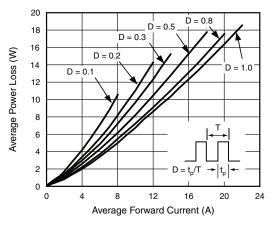


Fig. 2 - Forward Power Loss Characteristics Per Diode

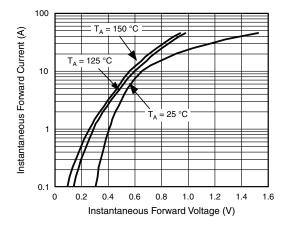


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

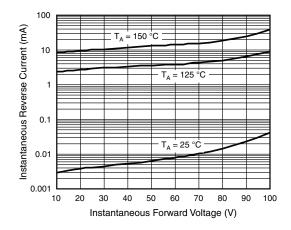


Fig. 4 - Typical Reverse Characteristics Per Diode

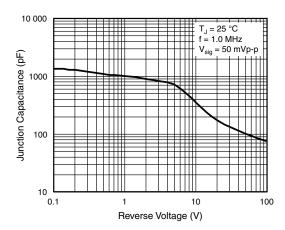


Fig. 5 - Typical Junction Capacitance Per Diode

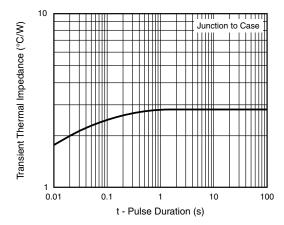


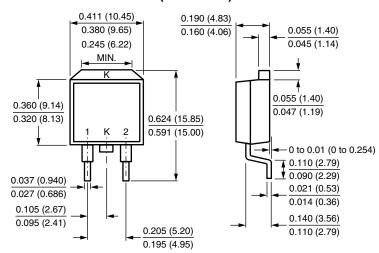
Fig. 6 - Typical Transient Thermal Impedance Per Diode



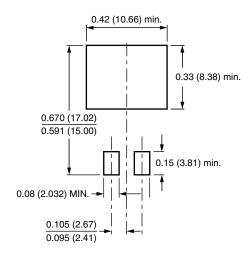
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

D²PAK (TO-263AB)



Mounting Pad Layout





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