SMB (DO-214AA)

Cathode O Anode

LINKS TO ADDITIONAL RESOURCES

3D Models

PRIMARY CHARACTERISTICS						
V _{BR}	6.8 V to 43 V					
V _{WM}	5.8 V to 36.8 V					
P _{PPM}	600 W					
I _{FSM}	75 A					
T _J max.	185 °C					
Polarity	Unidirectional					
Package	SMB (DO-214AA)					

TYPICAL APPLICATIONS

Use in sensitive electronics protection against voltage transients induced by inductive load switching and lighting on ICs, MOSFET, signal lines of sensor units for consumer, computer, industrial, automotive, and telecommunication.

FEATURES

- Junction passivation optimized design passivated anisotropic rectifier technology
- T_J = 185 °C capability suitable for high reliability and automotive requirement
- · Available in unidirectional polarity only
- 600 W peak pulse power capability with a 10/1000 µs waveform, repetitive rate (duty cycle): 0.01 %
- Excellent clamping capability
- Very fast response time
- Low incremental surge resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
 Automotive ordering code: base P/NHE3 or P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

MECHANICAL DATA

Case: SMB (DO-214AA)

Molding compound meets UL 94 V-0 flammability rating Base P/NHE3_X - RoHS-compliant and AEC-Q101 qualified Base P/NHM3_X - halogen-free, RoHS-compliant, and AEC-Q101 qualified

("_X" denotes revision code e.g. A, B, ...)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 HE3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes cathode end

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)								
PARAMETER	SYMBOL	VALUE	UNIT					
Peak pulse power dissipation with a 10/1000 μs waveform (fig. 1) $^{(1)(2)}$	P _{PPM}	600	W					
Peak pulse current with a 10/1000 μs waveform (fig. 3) $^{(1)}$	I _{PPM}	See table next page	А					
Peak forward surge current 8.3 ms single half sine-wave ⁽²⁾⁽³⁾	I _{FSM}	75	А					
Maximum instantaneous forward voltage at 50 A ⁽²⁾⁽³⁾	V _F	3.5	V					
Operating junction and storage temperature range	T _J , T _{STG}	-65 to +185	°C					

Notes

 $^{(1)}$ Non-repetitive current pulse, per fig. 3 and derated above T_A = 25 °C per fig. 2

(2) Mounted on 0.2" x 0.2" (5.0 mm x 5.0 mm) copper pads at each terminal

⁽³⁾ Measured on 8.3 ms single half sine-wave, or equivalent square wave, duty cycle = 4 pulses per minute maximum

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TPSMB6.8A thru TPSMB43A

Vishay General Semiconductor

RoHS

COMPLIANT HALOGEN

FREE

Surface-Mount PAR® Transient Voltage Suppressors

High Temperature Stability and High Reliability Conditions



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)											
DEVICE TYPE	DEVICE MARKING CODE	BREAKDOWN VOLTAGE V _{BR} ⁽¹⁾ AT I _T (V)		TEST CURRENT I _T (mA)	STAND-OFF VOLTAGE V _{WM} (M	MAXIMUM REVERSE LEAKAGE AT V _{WM} I _R (µA)	$\begin{array}{l} \text{MAXIMUM} \\ \text{REVERSE} \\ \text{LEAKAGE} \\ \text{AT V}_{\text{WM}} \\ \text{T}_{\text{J}} = 150\ ^{\circ}\text{C} \\ \text{I}_{\text{D}} \end{array}$	MAXIMUM PEAK PULSE SURGE CURRENT I _{PPM} ⁽²⁾ (A)	MAXIMUM CLAMPING VOLTAGE AT I _{PPM} V _C (M)	TYPICAL TEMP. COEFFICIENT OF V _{BR} ⁽³⁾ αT	
		MIN.	NOM.	MAX.			(Party	(μA)	~	(•/	(%/°C)
TPSMB6.8A	KEP	6.45	6.80	7.14	10	5.8	500	1000	57.1	10.5	0.047
TPSMB7.5A	KGP	7.13	7.50	7.88	10	6.4	250	500	53.1	11.3	0.052
TPSMB8.2A	KKP	7.79	8.20	8.61	10	7.02	100	200	49.6	12.1	0.056
TPSMB9.1A	KMP	8.65	9.10	9.55	1	7.78	25	50	44.8	13.4	0.060
TPSMB10A	KPP	9.5	10.0	10.5	1	8.55	5	20	41.4	14.5	0.064
TPSMB11A	KRP	10.5	11.0	11.6	1	9.4	2	5	38.5	15.6	0.067
TPSMB12A	KTP	11.4	12.0	12.6	1	10.2	2	5	35.9	16.7	0.070
TPSMB13A	KVP	12.4	13.0	13.7	1	11.1	2	5	33	18.2	0.072
TPSMB15A	KXP	14.3	15.0	15.8	1	12.8	1	5	28.3	21.2	0.076
TPSMB16A	KZP	15.2	16.0	16.8	1	13.6	1	5	26.7	22.5	0.078
TPSMB18A	LEP	17.1	18.0	18.9	1	15.3	1	5	23.8	25.2	0.080
TPSMB20A	LGP	19	20.0	21	1	17.1	1	5	21.7	27.7	0.082
TPSMB22A	LKP	20.9	22.0	23.1	1	18.8	1	5	19.6	30.6	0.084
TPSMB24A	LMP	22.8	24.0	25.2	1	20.5	1	5	18.1	33.2	0.085
TPSMB27A	LPP	25.7	27.0	28.4	1	23.1	1	5	16	37.5	0.087
TPSMB30A	LRP	28.5	30.0	31.5	1	25.6	1	5	14.5	41.4	0.088
TPSMB33A	LTP	31.4	33.0	34.7	1	28.2	1	5	13.1	45.7	0.089
TPSMB36A	LVP	34.2	36.0	37.8	1	30.8	1	5	12	49.9	0.090
TPSMB39A	LXP	37.1	39.0	41.0	1	33.3	1	5	11.1	53.9	0.091
TPSMB43A	LZP	40.9	43.0	45.2	1	36.8	1	5	10.1	59.3	0.092

Notes

⁽¹⁾ V_{BR} measured after I_T applied for 300 µs, I_T = square wave pulse or equivalent

⁽²⁾ Surge current waveform per fig. 3 and derated per fig. 2

⁽³⁾ To calculate V_{BR} vs. junction temperature, use the following formula: V_{BR} at T_J = V_{BR} at 25 °C x (1 + α T x (T_J - 25))

⁽⁴⁾ All terms and symbols are consistent with ANSI/IEEE C62.35

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
TPSMB6.8AHE3_A/H ⁽¹⁾	0.096	Н	750	7" diameter plastic tape and reel			
TPSMB6.8AHE3_A/I (1)	0.096	l	3200	13" diameter plastic tape and reel			
TPSMB6.8AHM3_A/H ⁽¹⁾	0.096	Н	750	7" diameter plastic tape and reel			
TPSMB6.8AHM3_A/I (1)	0.096	I	3200	13" diameter plastic tape and reel			

Note

(1) AEC-Q101 qualified



TPSMB6.8A thru TPSMB43A

Vishay General Semiconductor

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C, unless otherwise noted)

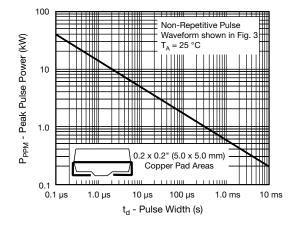


Fig. 1 - Peak Pulse Power Rating Curve

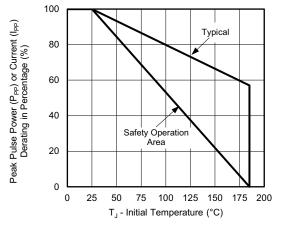


Fig. 2 - Pulse Power or Current vs. Initial Junction Temperature

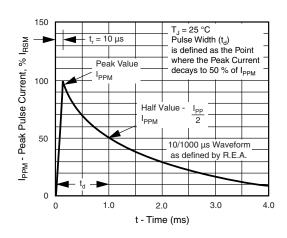


Fig. 3 - Pulse Waveform

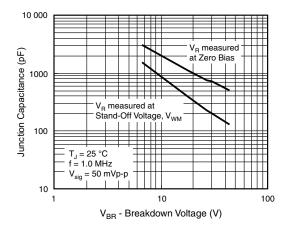


Fig. 4 - Typical Junction Capacitance

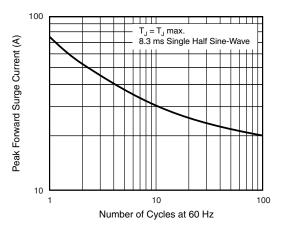


Fig. 5 - Maximum Non-Repetitive/Peak Forward Surge Current

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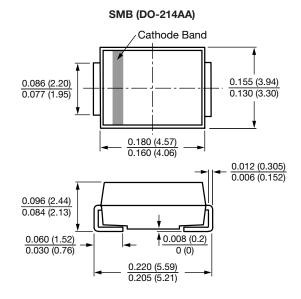
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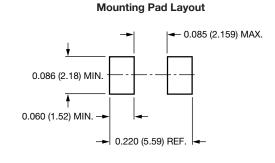


TPSMB6.8A thru TPSMB43A

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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)







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