AUTOMOTIVE

RoHS

COMPLIANT

HALOGEN FREE



## Vishay General Semiconductor

# **High Current Density Surface Mount Ultrafast Rectifiers**



### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 2.0 A			
V <sub>RRM</sub>	100 V, 150 V			
I <sub>FSM</sub>	40 A			
t <sub>rr</sub>	25 ns			
V <sub>F</sub> at I <sub>F</sub> = 2.0 A	0.77 V			
T <sub>J</sub> max.	175 °C			
Package	SMPC (TO-277A)			
Circuit configurations	Common cathode			

#### **FEATURES**

- Very low profile typical height of 1.1 mm
- · Ideal for automated placement
- · Oxide planar chip junction
- Ultrafast recovery times for high frequency
- · Low forward voltage drop, low power loss
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### TYPICAL APPLICATIONS

For use in high frequency rectification and freewheeling application in switching mode converters and inverters for consumer computer, automotive, and telecommunication applications.

#### **MECHANICAL DATA**

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/NHM3\_X - halogen-free, RoHS-compliant and

AEC-Q101 qualified

("\_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	UH4PBC	UH4PCC	UNIT
Device marking code			H4BC	H4CC	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	100	150	V
Maximum average forward rectified current (fig. 1)	total device		4.0		Α
	per diode	I <sub>F(AV)</sub>	2.0		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	40		А
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +175		°C



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 1.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.84	-	V
	I <sub>F</sub> = 2.0 A			0.93	1.05	
	I <sub>F</sub> = 1.0 A	T <sub>A</sub> = 125 °C		0.68	-	
	I <sub>F</sub> = 2.0 A			0.77	0.85	
Reverse current per diode	Poted V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	5	μΑ
	Rated V <sub>R</sub>	T <sub>A</sub> = 125 °C		6.4	25	
Maximum reverse recovery time per diode	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>rr</sub> = 0.25 A			20	25	
Typical reverse recovery time per diode	$I_F = 1.0 \text{ A}, \text{ dI/dt} = 50 \text{ A/}\mu\text{s}, \\ V_R = 30 \text{ V}, I_{rr} = 0.1 I_{RM}$		t <sub>rr</sub>	24	-	ns
Typical softness factor (t <sub>b</sub> /t <sub>a</sub> )per diode	$I_F = 2 \text{ A, dI/dt} = 200 \text{ A/}\mu\text{s,}$ $V_R = 200 \text{ V, } I_{rr} = 0.1 I_{RM}$ $T_A = 125 \text{ °C}$		S	0.3	-	-
Typical reverse recovery current per diode			I <sub>RM</sub>	5.4	-	Α
Typical stored charge per diode			Q <sub>rr</sub>	88	-	nC
Typical junction capacitance per diode	4.0 V, 1 MHz		CJ	21	-	pF

#### Notes

 $^{(1)}$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	UH4PBC	UH4PCC	UNIT		
Typical thermal resistance per device	R <sub>0</sub> JA (1)	60		°C/W		
Typical thermal resistance per device	$R_{ heta JL}$	4				

#### Note

(1) Units mounted on recommended PCB 1 oz. pad layout

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
UH4PCCHM3_A/H (1)	0.10	Н	1500	7" diameter plastic tape and reel			
UH4PCCHM3_A/I (1)	0.10	I	6500	13" diameter plastic tape and reel			

#### Note

(1) AEC-Q101 qualified

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## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

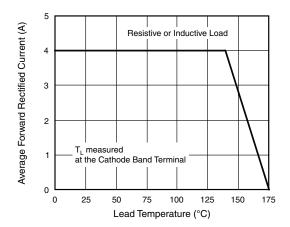


Fig. 1 - Maximum Forward Current Derating Curve

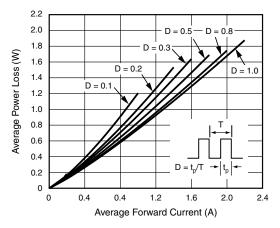


Fig. 2 - Forward Power Loss Characteristics Per Diode

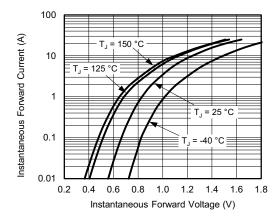


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

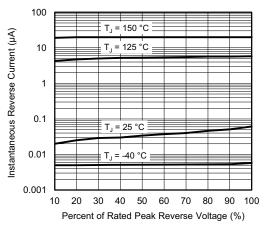


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

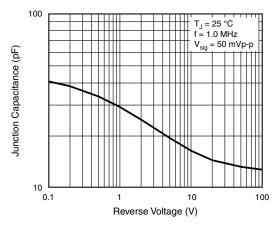


Fig. 5 - Typical Junction Capacitance Per Diode

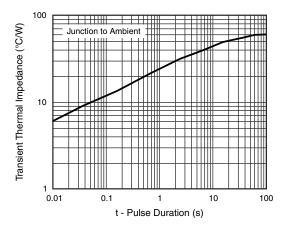
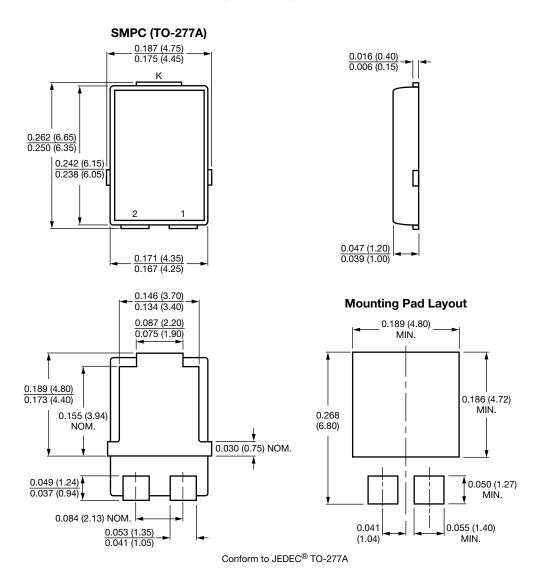


Fig. 6 - Typical Transient Thermal Impedance Per Device



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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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