Trench MOS Schottky technology generation 2 Very low profile - typical height of 1.7 mm

- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation

FEATURES

- Meets MSL level 1, per J-STD-020. LF maximum peak of 260 °C
- AEC-Q101 qualified available: Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 M3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	V10D100C	UNIT		
Maximum repetitive peak reverse voltage		V _{RRM}	100	V		
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	10	A		
	per diode		5			
Peak forward surge current 10 ms single half sine-wave superimposed on rated load		I _{FSM}	100	А		
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs		
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C		

V10D100C Vishay General Semiconductor

Dual High Voltage TMBS[®] (Trench MOS Barrier Schottky) Rectifier

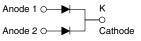
Ultra Low V_F = 0.48 V at I_F = 2.5 A

eSMP[®] Series SMPD (TO-263AC) 2 Top View **Bottom View** V10D100C

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 5 A			
V _{RRM}	100 V			
I _{FSM}	100 A			
V _F at I _F = 5 A (T _A = 125 °C)	0.60 V			
T _J max.	150 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			



Case: SMPD (TO-263AC)

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COMPLIANT

HALOGEN FREE





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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CO	TEST CONDITIONS S		TYP.	MAX.	UNIT	
Instantaneous forward voltage	I _F = 2.5 A	- T _A = 25 °C	V _F ⁽¹⁾	0.55	-	V	
	I _F = 5.0 A			0.67	0.75		
	I _F = 2.5 A	T _A = 125 °C		0.48	-		
	$I_{F} = 5.0 \text{ A}$			0.60	0.68		
Reverse current at rated V _R per diode	V _R = 70 V	T _A = 25 °C	I _R ⁽²⁾	2.3	-	μA	
	v _R = 70 v	T _A = 125 °C		2.3	-	mA	
	V _B = 100 V	T _A = 25 °C		-	500	μA	
	$v_{\rm R} = 100 V$	T _A = 125 °C		7	20	mA	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V10D100C	UNIT	
Typical thermal resistance	per diode	$R_{ extsf{ heta}JC}$	3.5		
	per device		2.5	°C/W	
	per device	R _{0JA} (1)(2)	48		

Notes

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$ - junction-to-mount

⁽²⁾ Free air, without heatsink

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
SMPD (TO-263AC)	V10D100C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel
SMPD (TO-263AC)	V10D100CHM3/I ⁽¹⁾	0.55	I	2000/reel	13" diameter plastic tape and reel

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

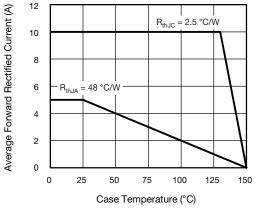


Fig. 1 - Maximum Forward Current Derating Curve

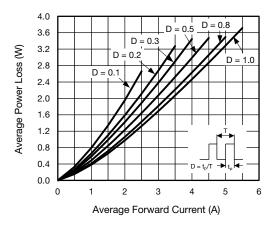


Fig. 2 - Average Power Loss Characteristics

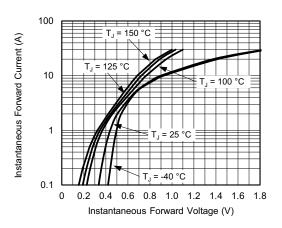


Fig. 3 - Typical Instantaneous Forward Characteristics

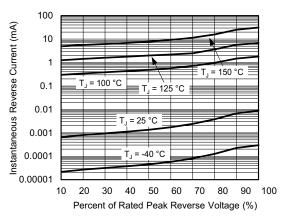


Fig. 4 - Typical Reverse Leakage Characteristics

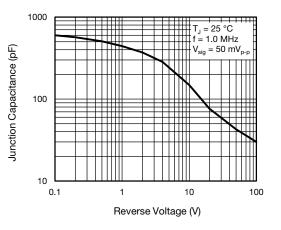


Fig. 5 - Typical Junction Capacitance

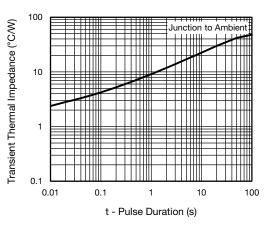


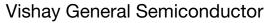
Fig. 6 - Typical Transient Thermal Impedance

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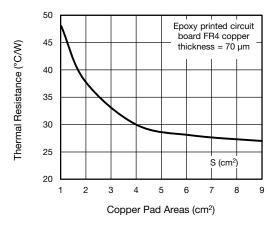
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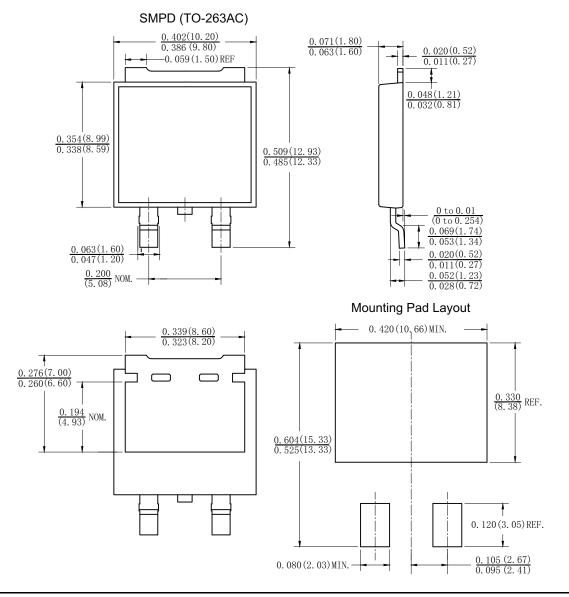








PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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