IRF610

Vishay Siliconix



**TO-220AB** 

**PRODUCT SUMMARY** 

V<sub>DS</sub> (V)

R<sub>DS(on)</sub> (Ω)

Q<sub>gs</sub> (nC)

Q<sub>gd</sub> (nC)

Q<sub>a</sub> max. (nC)

Configuration

# **Power MOSFET**

### FEATURES

- Dynamic dV/dt rating
- Repetitive avalanche rated
- Fast switching
- Ease of paralleling
- Simple drive requirements
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### Note

S

N-Channel MOSFET

1.5

200

8.2

1.8

4.5

Single

 $V_{GS} = 10 V$ 

\* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

### DESCRIPTION

Third generation power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	IRF610PbF
Lead (Pb)-free and halogen-free	IRF610PbF-BE3

<b>ABSOLUTE MAXIMUM RATINGS (T</b> <sub>C</sub>	= 25 °C, unl	ess otherwis	se noted)			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	200	v		
Gate-source voltage		V <sub>GS</sub>	± 20	v		
Continuous drain current	V + 10 V	T <sub>C</sub> = 25 °C T <sub>C</sub> = 100 °C		3.3		
	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 100 °C	I <sub>D</sub>	2.1	А	
Pulsed drain current <sup>a</sup>			I <sub>DM</sub>	10	1	
Linear derating factor			0.29	W/°C		
Single pulse avalanche energy <sup>b</sup>		E <sub>AS</sub>	64	mJ		
Repetitive avalanche current <sup>a</sup>		I <sub>AR</sub>	3.3	А		
Repetitive avalanche energy <sup>a</sup>		E <sub>AR</sub>	3.6	mJ		
Maximum power dissipation	T <sub>C</sub> = 25 °C		PD	36	W	
Peak diode recovery dV/dt <sup>c</sup>		dV/dt	5.0	V/ns		
Operating junction and storage temperature range	ange		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	
Soldering recommendations (peak temperature) <sup>d</sup>	For 10 s			300		
Mounting torque	6 22 or 1	6-32 or M3 screw		10	lbf ∙ in	
Mounting torque 6-32 or		VIS SCIEW		1.1	N · m	

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)

b.  $V_{DD}$  = 50 V, starting T<sub>J</sub> = 25 °C, L = 8.8 mH, R<sub>g</sub> = 25  $\Omega$ , I<sub>AS</sub> = 3.3 A (see fig. 12)

c.  $I_{SD} \leq 3.3$  A, dl/dt  $\leq 70$  A/µs,  $V_{DD} \leq V_{DS}, \, T_J \leq 150 \ ^\circ C$ 

d. 1.6 mm from case

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THERMAL RESISTANCE RAT	INGS								
PARAMETER	SYMBOL	TYP.		MAX.		UNIT			
Maximum junction-to-ambient	R <sub>thJA</sub>	- 62 0.50 -			°C/W				
Case-to-sink, flat, greased surface	R <sub>thCS</sub>								
Maximum junction-to-case (drain)	R <sub>thJC</sub>	- 3.5							
		•							
SPECIFICATIONS (T <sub>J</sub> = 25 °C, u	unless otherw	ise noted)							
PARAMETER	SYMBOL	-	CONDITIC	ONS	MIN.	TYP.	MAX.	UNIT	
Static		1				Į	Į	Į	
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0	V, I <sub>D</sub> = 25	50 µA	200	-	-	V	
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	Reference t	to 25 °C, I	<sub>D</sub> = 1 mA	-	0.30	-	V/°C	
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		2.0	-	4.0	V	
Gate-source leakage	I <sub>GSS</sub>		<sub>S</sub> = ± 20 V		-	-	± 100	nA	
Zaus anto voltano dusia suurant		$V_{DS} = 2$	$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		-	-	25		
Zero gate voltage drain current	IDSS	V <sub>DS</sub> = 160 V, V	$V_{\rm GS} = 0  \rm V,$	T <sub>J</sub> = 125 °C	-	-	250	μΑ	
Drain-source on-state resistance	R <sub>DS(on)</sub>	$V_{GS} = 10 V$	I <sub>D</sub>	= 2.0 A <sup>b</sup>	-	-	1.5	Ω	
Forward transconductance	9 <sub>fs</sub>	V <sub>DS</sub> = 50	0 V, I <sub>D</sub> = 2	.0 A <sup>b</sup>	0.8	-	-	S	
Dynamic	•					•	•	•	
Input capacitance	C <sub>iss</sub>	$V_{GS} = 0 V,$ $V_{DS} = 25 V,$ f = 1.0 MHz, see fig. 5		-	140	-	pF		
Output capacitance	C <sub>oss</sub>			-	53	-			
Reverse transfer capacitance	C <sub>rss</sub>			-	15	-			
Total gate charge	Qg				-	-	8.2		
Gate-source charge	Q <sub>gs</sub>	$V_{GS} = 10 V$	$V_{GS} = 10 \text{ V}$ $I_D = 3.3 \text{ A}, V_{DS} = 160 \text{ V}$ see fig. 6 and 13 b		-	-	1.8	nC	
Gate-drain charge	Q <sub>gd</sub>	see lig. 6 and 13 °		-	-	4.5			
Turn-on delay time	t <sub>d(on)</sub>				-	8.2	-		
Rise time	t <sub>r</sub>	$V_{DD}$ = 100 V, I_D = 3.3 A, $R_g$ = 24 $\Omega,R_D$ = 30 $\Omega,$ see fig. 10 $^{\rm b}$		-	17	-	ns		
Turn-off delay time	t <sub>d(off)</sub>			-	14	-			
Fall time	t <sub>f</sub>			-	8.9	-			
Gate input resistance	R <sub>g</sub>	f = 1 MHz, open drain		2.3	-	10.2	Ω		
Internal drain inductance	L <sub>D</sub>	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	nH		
Internal source inductance	L <sub>S</sub>			-	7.5	-			
Drain-Source Body Diode Characterist	cs								
Continuous source-drain diode current	I <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		-	-	3.3	A		
Pulsed diode forward current <sup>a</sup>	I <sub>SM</sub>			-	-	10			
Body diode voltage	V <sub>SD</sub>	T <sub>J</sub> = 25 °C, I <sub>S</sub>	$T_{J}$ = 25 °C, I <sub>S</sub> = 3.3 A, V <sub>GS</sub> = 0 V <sup>b</sup>		-	-	2.0	V	
Body diode reverse recovery time	t <sub>rr</sub>	- T <sub>J</sub> = 25 °C, I <sub>F</sub> = 3.3 A, dl/dt = 100 A/μs <sup>b</sup>		-	150	310	ns		
Body diode reverse recovery charge	Q <sub>rr</sub>			-	0.60	1.4	μC		
Forward turn-on time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by L			y L <sub>S</sub> and	L <sub>D</sub> )			

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)

b. Pulse width  $\leq 300~\mu s;~duty~cycle \leq 2~\%$ 

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### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

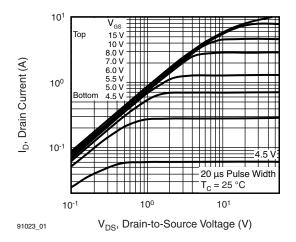


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C

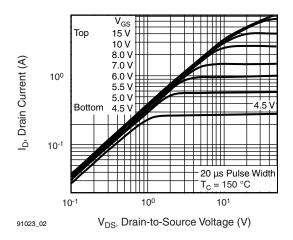
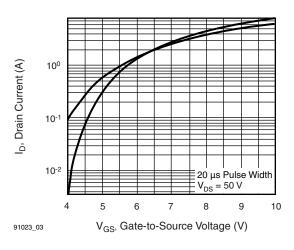


Fig. 2 - Typical Output Characteristics,  $T_C = 150$  °C





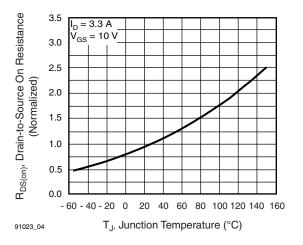


Fig. 4 - Normalized On-Resistance vs. Temperature

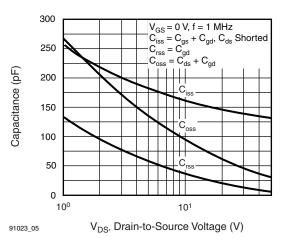


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

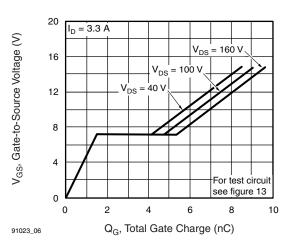


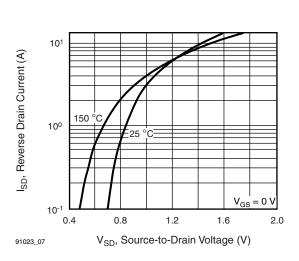
Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

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Fig. 7 - Typical Source-Drain Diode Forward Voltage

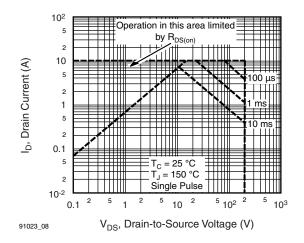


Fig. 8 - Maximum Safe Operating Area

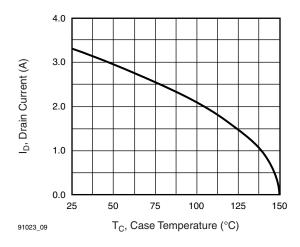


Fig. 9 - Maximum Drain Current vs. Case Temperature

 $V_{DS}$   $R_{D}$   $V_{GS}$  D.U.T.  $R_{G}$  D.U.T.  $Pulse width \le 1 \ \mu S$ Duty factor  $\le 0.1 \ \%$ 

Fig. 10a - Switching Time Test Circuit

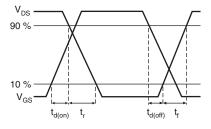
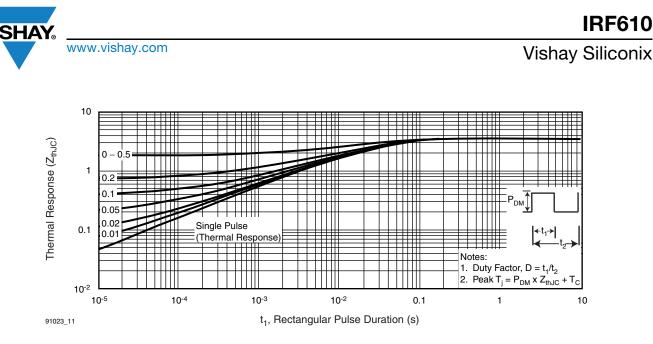


Fig. 10b - Switching Time Waveforms

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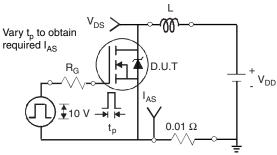
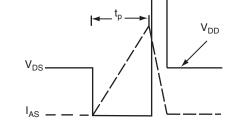


Fig. 12a - Unclamped Inductive Test Circuit



/<sub>DS</sub>

Fig. 12b - Unclamped Inductive Waveforms

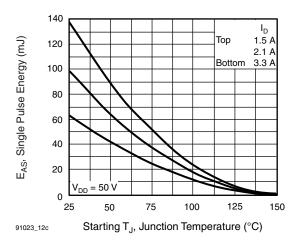
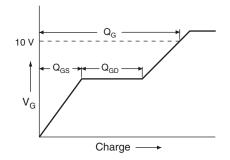


Fig. 12c - Maximum Avalanche Energy vs. Drain Current



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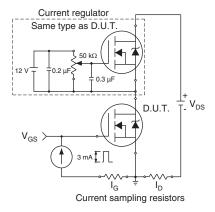
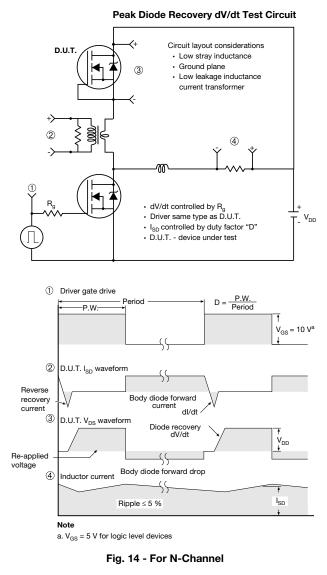


Fig. 13a - Basic Gate Charge Waveform

Fig. 13b - Gate Charge Test Circuit



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