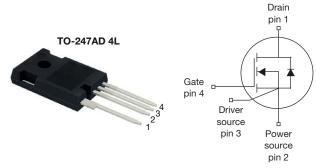


### MaxSiC® 1200 V N-Channel SiC MOSFET

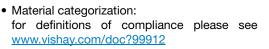


Marking Code: 120A080SL

| PRODUCT SUMMARY                            |                        |    |  |  |  |
|--|------------------------|----|--|--|--|
| V <sub>DS</sub> (V) at T <sub>J</sub> max. | 1200                   |    |  |  |  |
| R <sub>DS(on)</sub> typ. (mΩ) at 25 °C     | V <sub>GS</sub> = 18 V | 80 |  |  |  |
| Q <sub>g</sub> typ. (nC)                   | 45                     |    |  |  |  |
| I <sub>D</sub> (A)                         | 31                     |    |  |  |  |
| C <sub>oss</sub> typ. (pF)                 | 56                     |    |  |  |  |
| P <sub>D</sub> (W)                         | 174                    |    |  |  |  |
| Configuration                              | Single                 |    |  |  |  |

#### **FEATURES**

- · Fast switching speed
- Short circuit withstand time 3 µs





#### **APPLICATIONS**

- Solar inverters
- DC/DC converters
- Energy storage systems
- UPS (uninterruptible power supplies)

| ORDERING INFORMATION            |                  |
|---------------------------------|------------------|
| Package                         | TO-247AD 4L      |
| Lead (Pb)-free and halogen-free | MXP120A080SL-GE3 |

| <b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_C = 25$ °C  | C, unless otherwise    | noted)                            |                |      |
|--|------------------------|-----------------------------------|----------------|------|
| PARAMETER  |                        | SYMBOL                            | LIMIT          | UNIT |
| Drain-source voltage                             |                        | $V_{DS}$                          | 1200           |      |
| Gate-source voltage                              |                        | $V_{GS}$                          | -10 / +22      | V    |
| Recommended operation voltage of gate-source     |                        | $V_{GSOP}$                        | -5 to -3 / +18 |      |
| Continuous drain current                         | T <sub>C</sub> = 25 °C | I <sub>D</sub>                    | 31             | ۸    |
| Pulsed drain current <sup>a</sup>                |                        | I <sub>DM</sub>                   | 62             | Α    |
| Short-circuit withstand time <sup>b</sup>        |                        | T <sub>SC</sub>                   | 3              | μs   |
| Maximum power dissipation                        | T <sub>C</sub> = 25 °C | P <sub>D</sub>                    | 174            | W    |
| Operating junction and storage temperature range |                        | T <sub>J</sub> , T <sub>stg</sub> | -55 to +175    | °C   |
| Soldering recommendations (peak temperature)     | For 10 s               |                                   | 260            | °C   |
| Single pulse avalanche energy <sup>c</sup>       |                        | E <sub>AS</sub>                   | 113            | mJ   |

#### Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature
- b.  $V_{GS}$  = 18 V,  $V_{DS}$  = 800 V,  $R_{g(ext)}$  = 20  $\Omega$ , verified by the design / characterization
- c.  $T_J = 25$  °C,  $V_{DD} = 120$  V, L = 1 mH,  $V_{GS} = 18$  V,  $I_{AS} = 15$  A, verified by the design / characterization



| THERMAL RESISTANCE RATINGS       |            |      |      |       |  |
|----------------------------------|------------|------|------|-------|--|
| PARAMETER                        | SYMBOL     | TYP. | MAX. | UNIT  |  |
| Maximum junction-to-ambient      | $R_{thJA}$ | -    | 40   | °C/W  |  |
| Maximum junction-to-case (drain) | $R_{thJC}$ | -    | 0.86 | G/ VV |  |

| PARAMETER  | SYMBOL              | TEST CONDIT  | MIN.   | TYP. | MAX. | UNIT |          |
|--|---------------------|--|--|------|------|------|----------|
| Static   | •                   |  |  |      | L    | L    |          |
| Drain-source breakdown voltage   | V <sub>DS</sub>     | $V_{GS} = 0 \text{ V}, I_D =$  | V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA       |      | -    | -    | V        |
| Onto a companie de la contra de | V                   | $V_{DS} = V_{GS}$ , $I_D = 1$  | $V_{DS} = V_{GS}, I_D = 2.8 \text{ mA}$            |      | 2.9  | -    | V        |
| Gate-source threshold voltage (N)  | V <sub>GS(th)</sub> | $V_{DS} = V_{GS}, I_{D} = 2.8 \text{ m/s}$   | $V_{DS} = V_{GS}$ , $I_D = 2.8$ mA, $T_J = 175$ °C |      | 2.0  | -    | V        |
| Coto course legisere   | ,                   | $V_{GS} = 22 \text{ V}, V_{DS}$  | s = 0 V  | -    | -    | 100  | nA       |
| Gate-source leakage  | I <sub>GSS</sub>    | $V_{GS} = -10 \text{ V}, V_{D}$  | <sub>S</sub> = 0 V                                 | -    | -    | -100 |          |
| Zero gate voltage drain current  | I <sub>DSS</sub>    | V <sub>DS</sub> = 1200 V, V <sub>O</sub>   | <sub>GS</sub> = 0 V                                | -    | -    | 10   | μΑ       |
|  |                     | V <sub>GS</sub> = 15 V, I <sub>D</sub> :   | = 14 A   | -    | 97   | 121  | mΩ       |
| Drain-source on-state resistance   | R <sub>DS(on)</sub> | V <sub>GS</sub> = 18 V, I <sub>D</sub> :   | = 14 A   | -    | 80   | 100  |          |
|  |                     | V <sub>GS</sub> = 18 V, I <sub>D</sub> = 14 A, T <sub>J</sub> = 175 °C                       |  | -    | 144  | -    |          |
| Transconductance   | gfs                 | V <sub>DS</sub> = 10 V, I <sub>D</sub> =   | = 14 A   | -    | 5.5  | -    | S        |
| Dynamic  |                     |  |  |      |      |      |          |
| Input capacitance  | C <sub>iss</sub>    | V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 800 V, f = 100 KHz                                  |  | -    | 1356 | -    | pF       |
| Output capacitance   | C <sub>oss</sub>    |  |  | -    | 56   | -    |          |
| Reverse transfer capacitance   | C <sub>rss</sub>    |  |  | -    | 2    | -    |          |
| Total gate charge  | Qg                  | $V_{GS} = -5 \text{ V} \sim 18 \text{ V}, I_D = 14 \text{ A}, V_{DS} = 800 \text{ V}$        |  | -    | 45   | -    | nC       |
| Gate-source charge   | Q <sub>gs</sub>     |  |  | -    | 14   | -    |          |
| Gate-drain charge  | Q <sub>gd</sub>     |  |  | -    | 12   | -    |          |
| Gate Resistance  | $R_g$               | V <sub>DS</sub> = 0 V, f = 1 MHz   |  | -    | 4    | -    | Ω        |
| Switching Characteristics  |                     |  |  |      |      |      |          |
| Town on delections   | 1                   |  | T <sub>J</sub> = 25 °C                             | -    | 14   | -    |          |
| Turn-on delay time   | t <sub>d(on)</sub>  |  | T <sub>J</sub> = 175 °C                            | -    | 13   | -    | ns<br>μJ |
| Diag time  |                     |  | T <sub>J</sub> = 25 °C                             | -    | 12   | -    |          |
| Rise time  | t <sub>r</sub>      |  | T <sub>J</sub> = 175 °C                            | -    | 12   | -    |          |
| Turn off delevitions   | 1                   |  | T <sub>J</sub> = 25 °C                             | -    | 17   | -    |          |
| Turn-off delay time  | t <sub>d(off)</sub> | $V_{GS} = -5 \text{ V} \sim 18 \text{ V},$   | T <sub>J</sub> = 175 °C                            | -    | 18   | -    |          |
| Fall time  | +                   | $I_D = 14 \text{ A}, V_{DS} = 800 \text{ V},$<br>$R_{q(ext)} = 4.4 \Omega$                   | T <sub>J</sub> = 25 °C                             | -    | 10   | -    |          |
| raii tiirie  | t <sub>f</sub>      |  | T <sub>J</sub> = 175 °C                            | -    | 10   | -    |          |
| Turn on quitables anarry   | _                   |  | T <sub>J</sub> = 25 °C                             | -    | 132  | -    |          |
| Turn-on switching energy   | E <sub>on</sub>     |  | T <sub>J</sub> = 175 °C                            | -    | 119  | -    |          |
| Town (for Nobles and the   | -                   |  | T <sub>J</sub> = 25 °C                             | -    | 42   | -    |          |
| Turn-off switching energy  | E <sub>off</sub>    |  | T <sub>J</sub> = 175 °C                            | -    | 42   | -    |          |
| <b>Body Diode Ratings and Characteristic</b>   | C                   |  |  |      |      |      |          |
| Forward diode voltage  | V <sub>SD</sub>     | V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 7 A, T <sub>J</sub> = 25 °C                        |  | -    | 4.7  | -    | ٧        |
| Continuous diode forward current   | I <sub>SD</sub>     | V <sub>GS</sub> = -5 V, T <sub>J</sub> = 25 °C   |  | -    | -    | 24   |          |
| Pulsed diode forward current   | I <sub>SDM</sub>    |  |  | -    | -    | 62   | Α        |
| Reverse recovery time  | t <sub>rr</sub>     | V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 14 A,<br>V <sub>R</sub> = 800 V, di/dt = 1000 A/μs |  | -    | 16   | -    | ns       |
| Reverse recovery charge  | Q <sub>rr</sub>     |  |  | -    | 47   | -    | nC       |
| Reverse recovery current   | I <sub>RRM</sub>    |  |  | -    | 5    | _    | Α        |

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

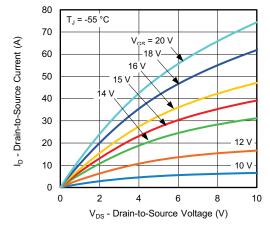


Fig. 1 Typical Output Characteristics

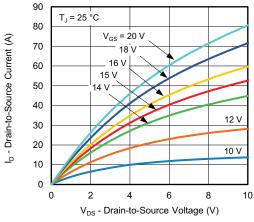


Fig. 2 Typical Output Characteristics

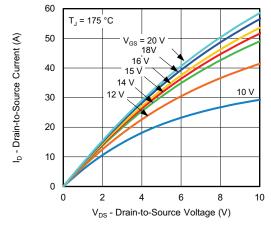


Fig. 3 Typical Output Characteristics

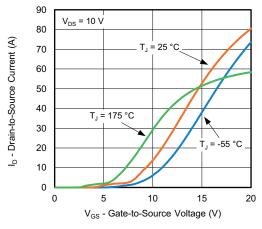


Fig. 4 Typical Transfer Characteristics

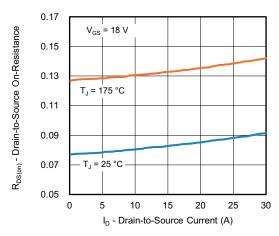


Fig. 5 Normalized On-Resistance vs. Drain Current

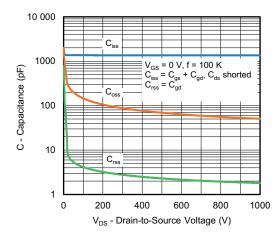


Fig. 6 Typical Capacitance vs. Drain-to-Source Voltage



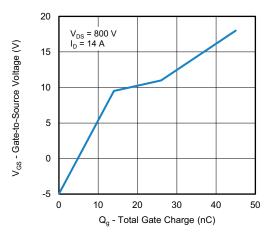


Fig. 7 Typical Gate Charge vs. Gate-to-Source Voltage

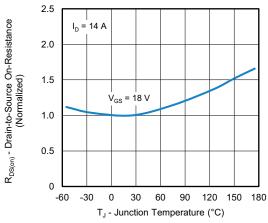


Fig. 8 Normalized On-Resistance vs. Temperature

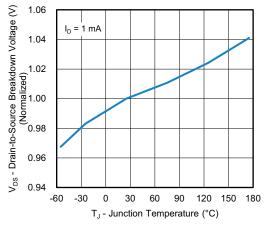


Fig. 9 Drain-to-Source Voltage vs. Temperature

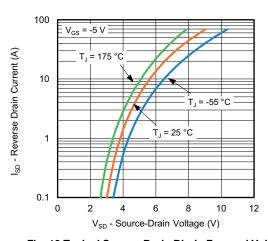


Fig. 10 Typical Source-Drain Diode Forward Voltage

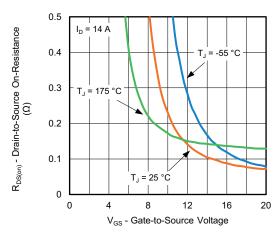


Fig. 11On-Resistance vs. Gate-to-Source Voltage

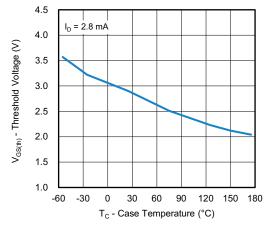


Fig. 12 Threshold Voltage vs. Case Temperature

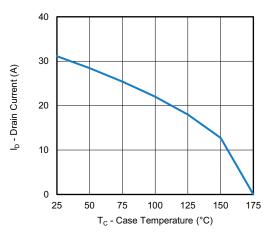


Fig. 13 Drain Current vs. Case Temperature

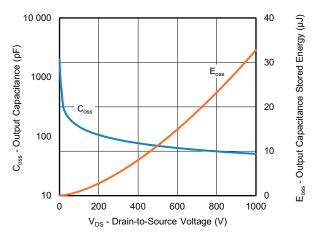


Fig. 14 Output Capacitance and its Stored Energy vs. Drain-to-Source Voltage

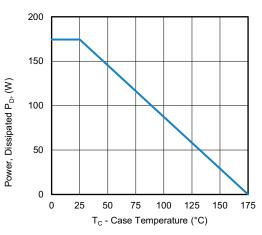


Fig. 15 Power, Dissipated P<sub>D</sub> vs. Case Temperature

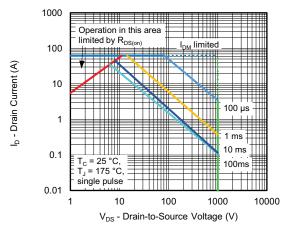


Fig. 16 Safe Operating Area

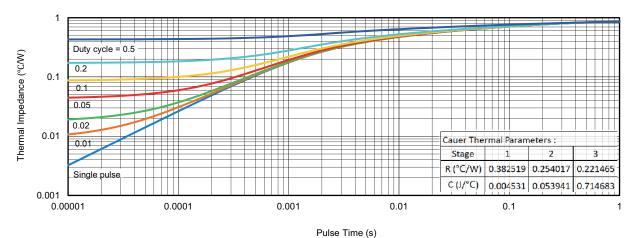


Fig. 17 Transient Thermal Impedance



#### www.vishay.com

### Vishay MaxPower Semiconductor

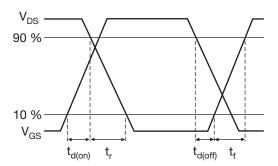


Fig. 18 Waveforms of Switching Time

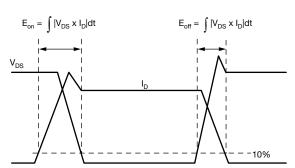


Fig. 19 Waveforms for Switching Energy

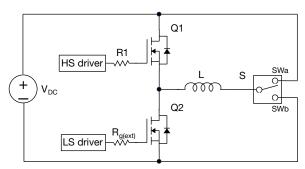


Fig. 20 Switching and Reverse Diode Characteristics Measurement Circuit

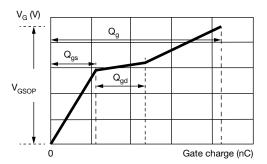


Fig. 21 Waveforms for Gate Charge

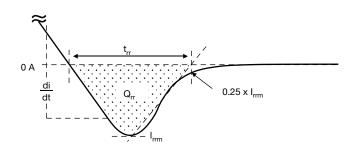


Fig. 22 Waveforms for Reverse Recovery

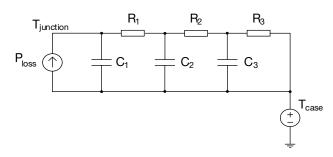


Fig. 23 Thermal Equivalent Circuit

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg292856">www.vishay.com/ppg292856</a>.



### **Legal Disclaimer Notice**

Vishay

### **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.