Vishay Semiconductors

Three Phase Bridge (Power Modules), 40 A



www.vishay.com

PRIMARY CHARACTERISTICS			
Ι _Ο	40 A		
V _{RRM}	1600 V		
Package	МТК		
Circuit configuration	Three phase bridge		

FEATURES

· Package fully compatible with the industry standard INT-A-PAK power modules series



COMPLIANT

- High thermal conductivity package, electrically insulated case
- Excellent power volume ratio, outline for easy connections to power transistor and IGBT modules
- 4000 V_{RMS} isolating voltage
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

A range of extremely compact, encapsulated three phase bridge rectifiers offering efficient and reliable operation. They are intended for use in general purpose and heavy duty applications.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
		40 (50)	А	
I _O	T _C	85 (60)	C°	
I _{FSM}	50 Hz	270	А	
	60 Hz	280	A	
l ² t	50 Hz	365	kA ² s	
	60 Hz	325	KA-S	
l²√t		3650	kA²√s	
V _{RRM}		1600	V	
T _{Stg}	Range	-40 to +150	°C	
TJ				

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS					
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J MAXIMUM mA	
40MTK	160	1600	1700	10	

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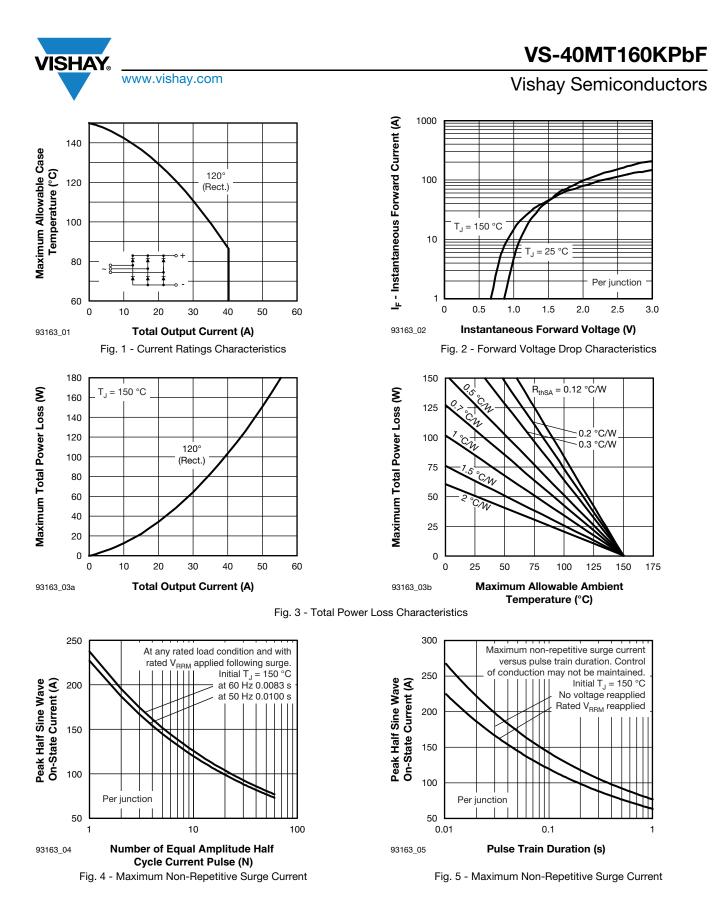
VS-40MT160KPbF



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FORWARD CONDUCTION						
PARAMETER	SYMBOL	. TEST CONDITIONS		VALUES	UNITS	
Maximum DC output		120° rect. conduction angle		40 (50)	А	
current at case temperature	Ι _Ο			85 (60)	°C	
	IFSM	t = 10 ms	No voltage		270	A
Maximum peak, one-cycle		t = 8.3 ms	reapplied		280	
forward, non-repetitive surge current		t = 10 ms	100 % V _{BBM}		225	
		t = 8.3 ms	reapplied	Initial	240	
	l ² t	t = 10 ms	No voltage	$T_J = T_J maximum$	365	kA ² s
Marian and 12t fair frain a		t = 8.3 ms	reapplied		325	
Maximum I ² t for fusing		t = 10 ms	100 % V _{RRM}		253	
		t = 8.3 ms	reapplied		240	
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied		3650	A²√s	
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J maximum		0.78	V	
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi x I_{F(AV)}), T_J$ maximum		0.9	v	
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J maximum		15		
High level value of forward slope resistance	r _{f2}	$(I > \pi x I_{F(AV)}), T_J$ maximum		14.1	mΩ	
Maximum forward voltage drop	V _{FM}	I_{pk} = 100 A, T_J = 25 °C, t_p = 400 μs single junction		2.02		
RMS isolation voltage	V _{ISOL}	$T_J = 25$ °C, all terminal shorted f = 50 Hz, t = 1 s		4000	V	

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction operating a storage temperature range	and	T _J , T _{Stg}		-40 to +150	°C	
			DC operation per module	0.41		
Maximum thermal resistance, junction to case		R _{thJC}	DC operation per junction	2.46	K/W	
			120° rect. conduction angle per module	0.45		
			120° rect. conduction angle per junction	2.7		
Maximum thermal resistance, case to heatsink per module		R _{thCS}	Mounting surface smooth, flat and greased	0.03		
Mounting torque ± 10 % –	to heatsink		A mounting compound is recommended and the	4 to 6	Nm	
	to terminal]	torque should be rechecked after a period of 3 hours to allow for the spread of the compound.	3 to 4		
Approximate weight			Lubricated threads. 176		g	



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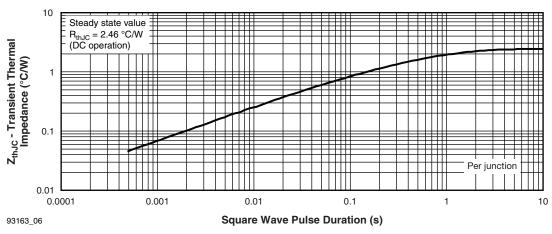
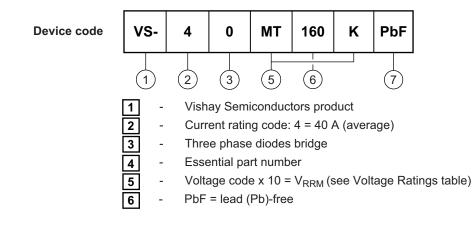


Fig. 6 - Thermal Impedance ZthJC Characteristics

ORDERING INFORMATION TABLE

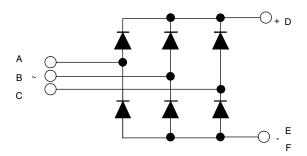
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Note

• To order the optional hardware go to www.vishay.com/doc?95172

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95004		

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