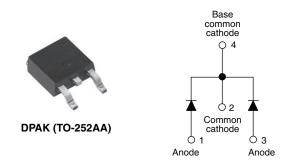
**Vishay Semiconductors** 

www.vishay.com

Ultrafast Rectifier, 2 x 3 A FRED Pt<sup>®</sup>



PRIMARY CHARACTERISTICS									
I <sub>F(AV)</sub> 2 x 3 A									
V <sub>R</sub>	200 V								
V <sub>F</sub> at I <sub>F</sub>	0.9 V								
t <sub>rr</sub> typ.	See Recovery table								
T <sub>J</sub> max.	175 °C								
Package	DPAK (TO-252AA)								
Circuit configuration	Common cathode								

### **FEATURES**

- Ultrafast recovery time
- · Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- COMPLIANT HALOGEN

FREE

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## **DESCRIPTION / APPLICATIONS**

VS-MURD620CT-M3 is the state of the art ultrafast recovery rectifier specifically designed with optimized performance of forward voltage drop and ultrafast recovery time. The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics. These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS						
Peak repetitive reverse voltage	V <sub>RRM</sub>		200	V						
Average rectified forward current per device	I <sub>F(AV)</sub>	Total device, rated V <sub>R</sub> , T <sub>C</sub> = 146 °C	6							
Non-repetitive peak surge current	I <sub>FSM</sub>		50	А						
Peak repetitive forward current per diode	I <sub>FM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, $T_C = 146 \ ^{\circ}C$	6							
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C						

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	200	-	-					
Forward voltage		I <sub>F</sub> = 3 A	-	0.9	1.0	v				
	V <sub>F</sub>	I <sub>F</sub> = 3 A, T <sub>J</sub> = 125 °C	-	0.78	0.96	v				
		I <sub>F</sub> = 6 A	-	1	1.2					
		I <sub>F</sub> = 6 A, T <sub>J</sub> = 125 °C	-	0.89	1.13					
	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	-	5					
Reverse leakage current		$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μA				
Junction capacitance	CT	V <sub>R</sub> = 200 V	-	12	-	pF				
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH				

Revision: 05-Jun-2023 Document Number: 93497 1 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000







www.vishay.com

# Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}$	õs, V <sub>R</sub> = 30 V	-	-	35				
	t <sub>rr</sub>	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_F$	-	-	25	-				
		T <sub>J</sub> = 25 °C		-	19	-	- ns - A			
		T <sub>J</sub> = 125 °C		-	26	-				
Poak rocovony ourront	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 3 A dI <sub>F</sub> /dt = 200 A/µs	-	3.1	-				
Peak recovery current		T <sub>J</sub> = 125 °C	$V_{\rm B} = 160 \text{ V}$	-	4.6	-				
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	30	-	nC			
		T <sub>J</sub> = 125 °C		-	60	-	nC			

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C				
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	-	9.0					
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>		-	-	80	°C/W				
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	-	-					
Weight			-	0.3	-	g				
weight			-	0.01	-	oz.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Marking device		Case style DPAK (TO-252AA)	MURD620CT							



# VS-MURD620CT-M3

## **Vishay Semiconductors**

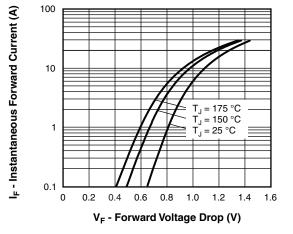


Fig. 1 - Typical Forward Voltage Drop Characteristics

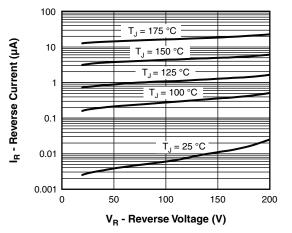


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

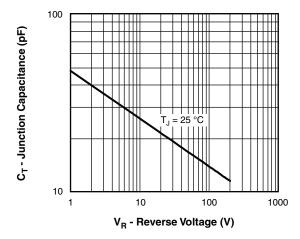


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

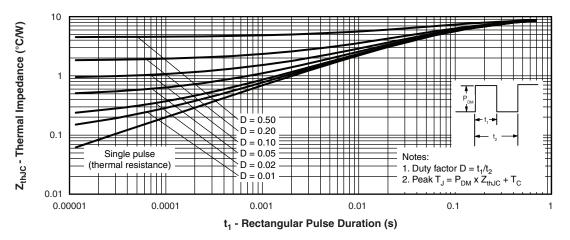


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

 Revision: 05-Jun-2023
 3
 Document Number: 93497

 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
 THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



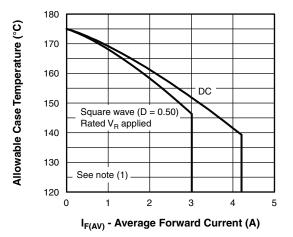


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

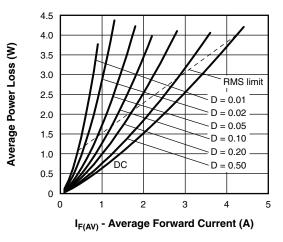
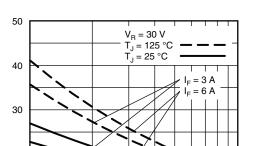


Fig. 6 - Forward Power Loss Characteristics

#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$





t<sub>rr</sub> (ns)



Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

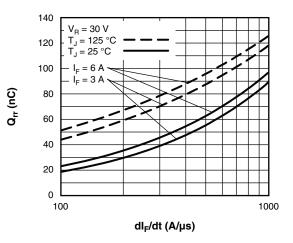


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

# VS-MURD620CT-M3

## **Vishay Semiconductors**

Revision: 05-Jun-2023 Document Number: 93497 4 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



# VISHAY, www.vishay.com

## **Vishay Semiconductors**

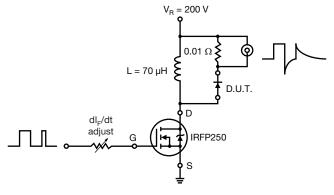


Fig. 9 - Reverse Recovery Parameter Test Circuit

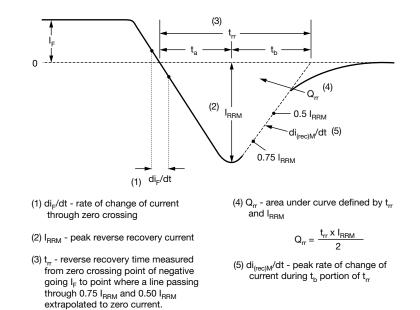


Fig. 10 - Reverse Recovery Waveform and Definitions

## **Vishay Semiconductors**



### **ORDERING INFORMATION TABLE**

Device code	VS-	MUR	D	6	20	СТ	TRL	-M3		
	1	2	3	4	5	6	7	8	1	
	<ol> <li>Vishay Semiconductors product</li> <li>Ultrafast MUR series</li> <li>D = D-PAK</li> <li>Current rating (6 = 6 A)</li> <li>Voltage rating (20 = 200 V)</li> </ol>									
	6 - 7 - 8 -	- CT - Tap - Env	CT = center tap (dual) Tape and real suffix TR = tape ar TRL = tape a					ape and ape and	d reel (left oriente d reel (right orier	nted

**ORDERING INFORMATION** (Example) **PREFERRED P/N** BASE QUANTITY PACKAGING DESCRIPTION VS-MURD620CT-M3 75 Antistatic plastic tube VS-MURD620CTTR-M3 2000 13" diameter reel VS-MURD620CTTRL-M3 3000 13" diameter reel VS-MURD620CTTRR-M3 3000 13" diameter reel

LINKS TO RELATED DOCUMENTS								
Dimensions www.vishay.com/doc?95627								
Part marking information	www.vishay.com/doc?95176							
Packaging information	www.vishay.com/doc?95033							





D-PAK (TO-252AA) "M"

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	INCHES		NOTES SYM		MILLIN	IETERS	INC	HES	NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51 BSC		0.020 BSC		
с	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension uncontrolled in L5

<sup>(3)</sup> Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(6)</sup> Dimension b1 and c1 applied to base metal only

<sup>(7)</sup> Datum A and B to be determined at datum plane H

<sup>(8)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-252AA



Vishay

# Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2025 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2025

1