# VS-SD400N/R Series

**Vishay Semiconductors** 

# Standard Recovery Diodes, (Stud Version), 400 A



- Wide current range
- High voltage ratings up to 2400 V
- High surge current capabilities
- Stud cathode and stud anode version
- Standard JEDEC<sup>®</sup> types
- Compression bonded encapsulations
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### TYPICAL APPLICATIONS

- Converters
- Power supplies
- Machine tool controls
- High power drives
- Medium traction applications

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
I <sub>F(AV)</sub>		480	A	
	T <sub>C</sub>	120	٥°	
I <sub>F(RMS)</sub>		630		
I <sub>FSM</sub>	50 Hz	8250	A	
	60 Hz	8640		
l <sup>2</sup> t	50 Hz	340	kA <sup>2</sup> s	
	60 Hz	311	KA-5	
V <sub>RRM</sub>	Range	1600 to 2400	V	
TJ		-40 to +190	C°	

### **ELECTRICAL SPECIFICATIONS**

**PRIMARY CHARACTERISTICS** 

I<sub>F(AV)</sub>

Package

Circuit configuration

VOLTAGE RATINGS						
TYPE NUMBER	VOLTAGE CODE	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> MAXIMUM AT T <sub>J</sub> = T <sub>J</sub> MAXIMUM mA		
	16	1600	1700			
VS-SD400N/R	20	2000	2100	15		
	24	2400	2500			



DO-9 (DO-205AB)

400 A

DO-9 (DO-205AB)

Single



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FORWARD CONDUCTION						
PARAMETER	SYMBOL	L TEST CONDITIONS		VALUES	UNITS	
	I <sub>F(AV)</sub>	180° conduction, half sine wave		400	A	
Maximum average forward current				120	°C	
at case temperature				480	A	
				100	°C	
Maximum RMS forward current	I <sub>F(RMS)</sub>	DC at 110 °C case temperature		630		
		t = 10 ms	No voltage	Sinusoidal half wave,	8250	
Maximum peak, one-cycle forward,	I <sub>FSM</sub>	t = 8.3 ms	reapplied		8640	А
non-repetitive surge current		t = 10 ms	100 % V <sub>RRM</sub>		6940	
		t = 8.3 ms	reapplied		7270	
	l <sup>2</sup> t	t = 10 ms	No voltage	initial $T_J = T_J maximum$	340	kA <sup>2</sup> s
Movimum 1 <sup>2</sup> t for fusing		t = 8.3 ms	reapplied		311	
Maximum I <sup>2</sup> t for fusing		t = 10 ms	100 % V <sub>RRM</sub> reapplied		241	
		t = 8.3 ms			220	
Maximum I <sup>2</sup> √t for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied		3400	kA²√s	
Low level value of threshold voltage	V <sub>F(TO)1</sub>	(16.7 % x $\pi$ x I <sub>F(AV)</sub> < I < $\pi$ x I <sub>F(AV)</sub> ), T <sub>J</sub> = T <sub>J</sub> maximum		0.80	v	
High level value of threshold voltage	V <sub>F(TO)2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J maximum$		0.85		
Low level value of forward slope resistance	r <sub>f1</sub>	(16.7 % x $\pi$ x I <sub>F(AV)</sub> < I < $\pi$ x I <sub>F(AV)</sub> ), T <sub>J</sub> = T <sub>J</sub> maximum		0.55	mW	
High level value of forward slope resistance	r <sub>f2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J maximum$		0.51	11174	
Maximum forward voltage drop	V <sub>FM</sub>	$I_{pk} = 1500 \text{ A}, T_J = T_J \text{ maximum}, t_p = 10 \text{ ms sinusoidal wave}$		1.62	v	

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction operating temperature range	Тј		-40 to +190	°C	
Maximum storage temperature range	T <sub>Stg</sub>		-55 to +200		
Maximum thermal resistance, junction to case	R <sub>thJC</sub>			K/W	
Maximum thermal resistance, case to heatsink	R <sub>thCS</sub>				
Maximum allowed mounting torque ± 10 %		Not-lubricated threads	27	Nm	
Approximate weight			250	g	
Case style		See dimensions (link at the end of datasheet	DO-9 (DO-205AB)		

CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS	
180°	0.020	0.013			
120°	0.023	0.023			
90°	0.029	0.031	$T_J = T_J maximum$	K/W	
60°	0.042	0.044			
30°	0.073	0.074			

#### Note

• The table above shows the increment of thermal resistance RthJC when devices operate at different conduction angles than DC

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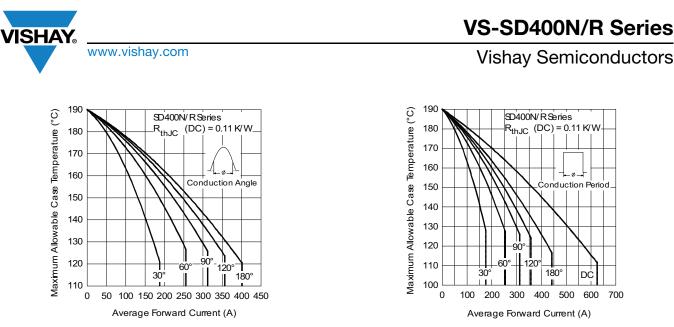


Fig. 1 - Current Ratings Characteristics



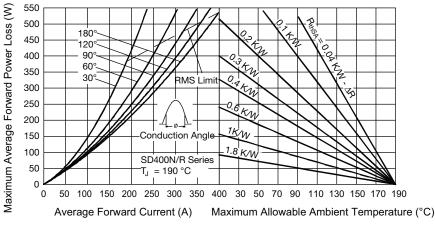
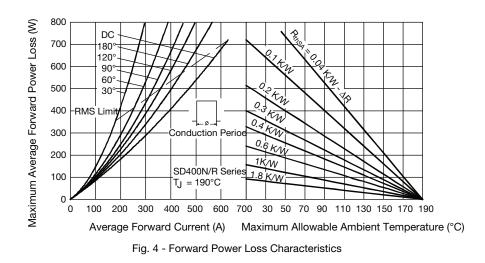


Fig. 3 - Forward Power Loss Characteristics



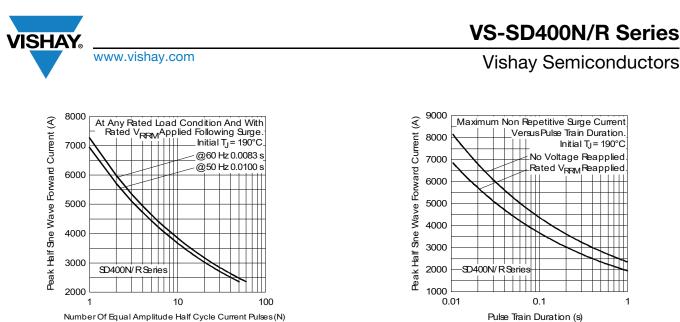


Fig. 5 - Maximum Non-Repetitive Surge Current



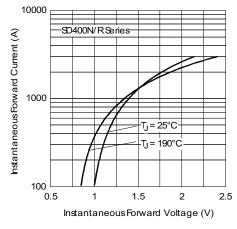


Fig. 7 - Forward Voltage Drop Characteristics

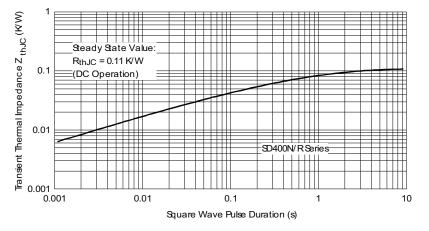


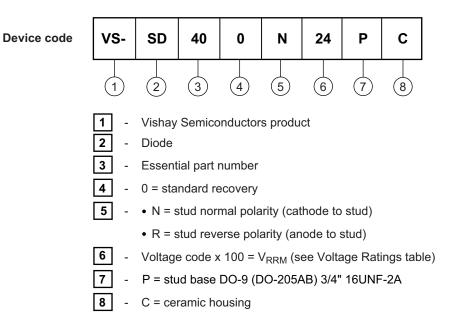
Fig. 8 - Thermal Impedance Z<sub>thJC</sub> Characteristic



### **Vishay Semiconductors**



### **ORDERING INFORMATION TABLE**



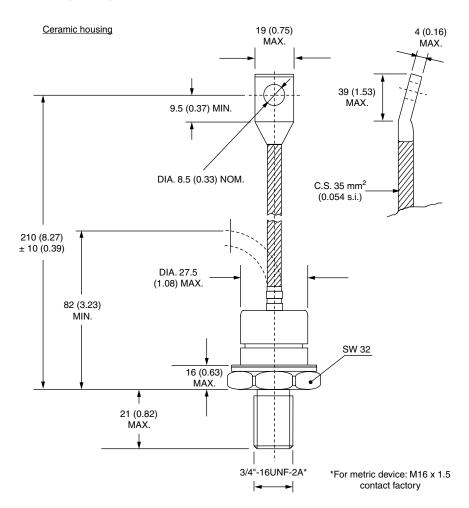
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95301		

Vishay Semiconductors



# DO-205AB (DO-9)

**DIMENSIONS** in millimeters (inches)





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