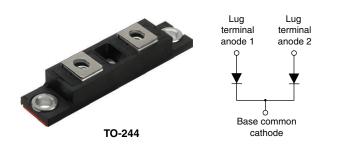
Vishay Semiconductors

# High Performance Schottky Rectifier, 300 A



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PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	300 A			
V <sub>R</sub>	45 V			
Package	TO-244			
Circuit configuration Two diodes common cat				

### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **DESCRIPTION / APPLICATIONS**

The VS-300CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN				
I <sub>F(AV)</sub>	Rectangular waveform	300	А			
V <sub>RRM</sub>		45	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	27 000	А			
V <sub>F</sub>	150 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.56	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-300CNQ045PbF	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	45	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	- 45 V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg						
forward current See fig. 5	per device	I <sub>F(AV)</sub>	(AV) 50 % duty cycle at $T_C = 111$ °C, rectangular waveform		300	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I <sub>FSM</sub> load condition a	Following any rated	27 000		
			10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	2400	
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 18 A, L = 1 mH		150	mJ
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		30	A

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1





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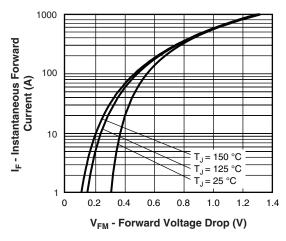
### ELECTRICAL SPECIFICATIONS

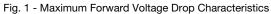
ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		150 A	T,I = 25 °C	0.61	V
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	300 A	1j=25 0	0.77	
See fig. 1		150 A	T 105 %C	0.56	
		300 A	T <sub>J</sub> = 125 °C	0.75	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	15	mA
See fig. 2		T <sub>J</sub> = 125 °C	$v_{\rm R}$ = naleu $v_{\rm R}$	1100	
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		7750	pF
Typical series inductance per leg	L <sub>S</sub>	From top of terminal hole to mounting plane 6.0		6.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V			V/µs

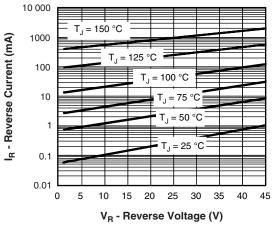
#### Note

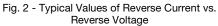
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2  $\,\%$ 

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>	- 55	-	150	°C	
Thermal resistance,	per leg	R <sub>thJC</sub>	-	-	0.28	°C/W	
junction to case	per module		-	-	0.14		
Thermal resistance, case to heatsink		R <sub>thCS</sub>	-	0.10	-		
Weight			-	68	-	g	
			-	2.4	-	OZ.	
Mounting torque			35.4 (4)	-	53.1 (6)		
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf · in (N · m)	
Terminal torque			30 (3.4)	-	44.2 (5)	(	
Vertical pull			-	-	80	المؤرث	
2" lever pull			-	-	35	- lbf · in	









Revision: 09-May-17

2

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## VS-300CNQ045PbF

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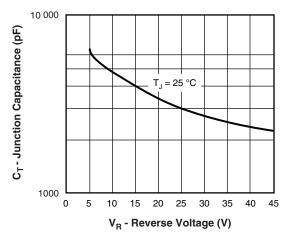


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

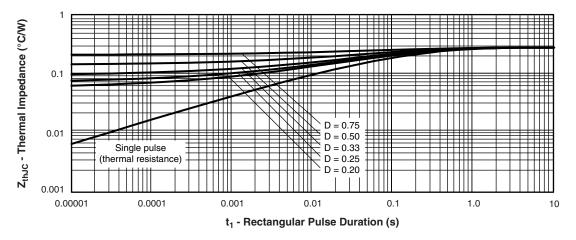
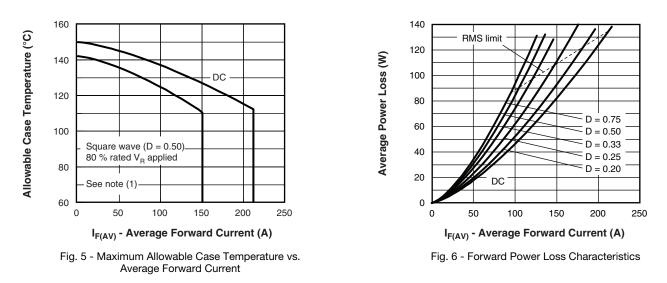


Fig. 4 - Maximum Thermal Impedance  $Z_{\text{thJC}}$  Characteristics



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## VS-300CNQ045PbF

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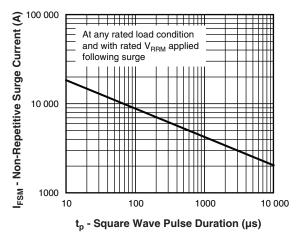


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

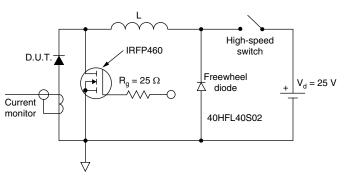
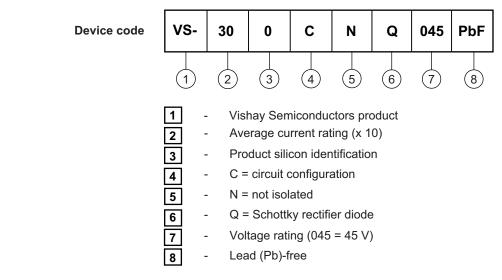


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

#### **ORDERING INFORMATION TABLE**

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#### LINKS TO RELATED DOCUMENTS

Dimensions	www.vishay.com/doc?95021				
Revision: 09-May-17	4 Document Number: 94175				
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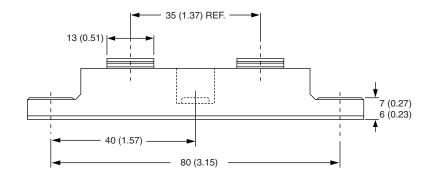


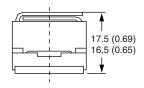


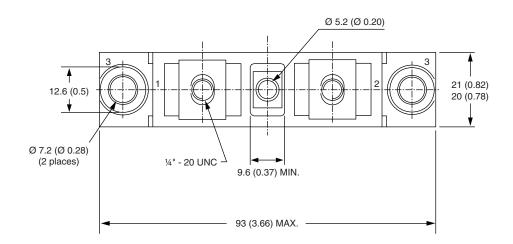
**Vishay Semiconductors** 

**TO-244** 

### **DIMENSIONS** in millimeters (inches)









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