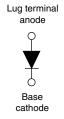


# **High Performance Schottky Rectifier, 240 A**



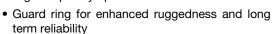


HALF-PAK (D-67)

| PRIMARY CHARACTERISTICS |                 |  |  |
|-------------------------|-----------------|--|--|
| I <sub>F(AV)</sub>      | 240 A           |  |  |
| $V_{R}$                 | 45 V            |  |  |
| Package                 | HALF-PAK (D-67) |  |  |
| Circuit configuration   | Single diode    |  |  |

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation



- Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION**

The VS-241NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS |   |                   |       |  |  |
|-----------------------------------|---|-------------------|-------|--|--|
| SYMBOL                            | CHARACTERISTICS                               | VALUES            | UNITS |  |  |
| I <sub>F(AV)</sub>                | Rectangular waveform                          | 240               | A     |  |  |
| V <sub>RRM</sub>                  |   | 45                | V     |  |  |
| I <sub>FSM</sub>                  | t <sub>p</sub> = 5 µs sine                    | 25 000            | A     |  |  |
| V <sub>F</sub>                    | 240 A <sub>pk</sub> , T <sub>J</sub> = 125 °C | 0.64              | V     |  |  |
| T <sub>J</sub>                    | Range   | Range -55 to +175 |       |  |  |

| VOLTAGE RATINGS                      |                  |                |       |  |
|--------------------------------------|------------------|----------------|-------|--|
| PARAMETER                            | SYMBOL           | VS-241NQ045PbF | UNITS |  |
| Maximum DC reverse voltage           | $V_{R}$          | 45             | V     |  |
| Maximum working peak reverse voltage | V <sub>RWM</sub> | 45             | V     |  |

| ABSOLUTE MAXIMUM RATINGS                   |                    |  |  |        |       |
|--|--------------------|--|--|--------|-------|
| PARAMETER                                  | SYMBOL             | TEST CONDITIONS  |  | VALUES | UNITS |
| Maximum average forward current See fig. 5 | I <sub>F(AV)</sub> | 50 % duty cycle at T <sub>C</sub> = 144 °C, rectangular waveform   |  | 240    |       |
| Maximum peak one cycle                     |                    | 5 μs sine or 3 μs rect. pulse  | Following any rated load condition and with rated V <sub>RRM</sub> applied | 25 000 | Α     |
| non-repetitive surge current<br>See fig. 7 | IFSM               | I TIT ME SING OF A ME FACT THISE I   |  | 3450   |       |
| Non-repetitive avalanche energy            | E <sub>AS</sub>    | T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 26 A, L = 1 mH   |  | 324    | mJ    |
| Repetitive avalanche current               | I <sub>AR</sub>    | Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_B$ typical  48 |  | 48     | Α     |



| ELECTRICAL SPECIFICATIONS                     |                                |   |                                       |        |       |
|---|--------------------------------|---|---------------------------------------|--------|-------|
| PARAMETER                                     | SYMBOL                         | TEST CONDITIONS   |                                       | VALUES | UNITS |
| Maximum forward voltage drop<br>See fig. 1    | V <sub>FM</sub> <sup>(1)</sup> | 240 A   | T <sub>J</sub> = 25 °C                | 0.80   | V     |
|   |                                | 480 A   |                                       | 1.11   |       |
|   |                                | 240 A   | T <sub>J</sub> = 125 °C               | 0.64   |       |
|   |                                | 480 A   |                                       | 0.86   |       |
| Maximum reverse leakage current<br>See fig. 2 | I <sub>RM</sub> (1)            | T <sub>J</sub> = 25 °C  | V <sub>R</sub> = Rated V <sub>R</sub> | 20     | mA    |
|   |                                | T <sub>J</sub> = 125 °C   |                                       | 1120   | IIIA  |
| Maximum junction capacitance                  | C <sub>T</sub>                 | V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C |                                       | 14 800 | pF    |
| Typical series inductance                     | L <sub>S</sub>                 | From top of terminal hole to mounting plane                                   |                                       | 5.0    | nΗ    |
| Maximum voltage rate of change                | dV/dt                          | Rated V <sub>R</sub>  |                                       | 10 000 | V/µs  |

#### Note

 $^{(1)}$  Pulse width  $< 500 \ \mu s$ 

| THERMAL - MECHANICAL SPECIFICATIONS       |                       |                                   |                                      |            |                     |  |
|---|-----------------------|-----------------------------------|--------------------------------------|------------|---------------------|--|
| PARAMETER                                 |                       | SYMBOL                            | TEST CONDITIONS                      | VALUES     | UNITS               |  |
| Maximum junction and st temperature range | orage                 | T <sub>J</sub> , T <sub>Stg</sub> |                                      | -55 to 175 | °C                  |  |
| Maximum thermal resistar                  | nce, junction to case | R <sub>thJC</sub>                 | DC operation<br>See fig. 4           | 0.19       | °C/W                |  |
| Typical thermal resistance                | e, case to heatsink   | R <sub>thCS</sub>                 | Mounting surface, smooth and greased | 0.05       |                     |  |
| Approximate weight                        |                       |                                   |                                      | 30         | g                   |  |
|   |                       |                                   |                                      | 1.06       | oz.                 |  |
| Mounting torque                           | minimum               |                                   | Non-lubricated threads               | 3 (26.5)   |                     |  |
|   | maximum               |                                   |                                      | 4 (35.4)   | N ⋅ m<br>(lbf ⋅ in) |  |
| Terminal torque                           | minimum               |                                   |                                      | 3.4 (30)   |                     |  |
|   | maximum               |                                   |                                      | 5 (44.2)   |                     |  |
| Case style                                |                       |                                   |                                      | HALF-PA    | C module            |  |

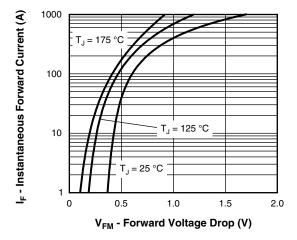


Fig. 1 - Maximum Forward Voltage Drop Characteristics

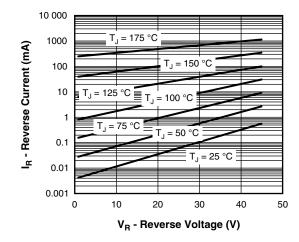


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

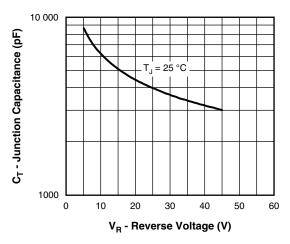


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

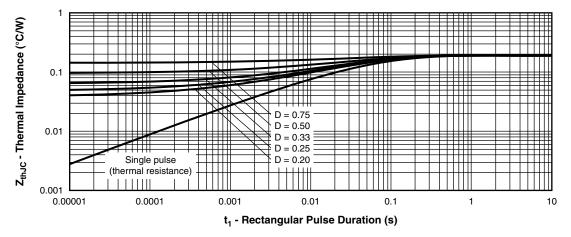


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

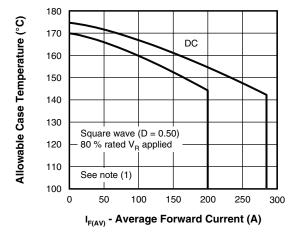


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

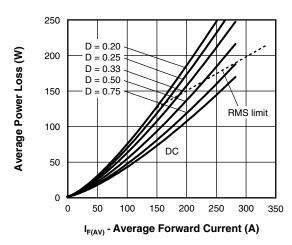


Fig. 6 - Forward Power Loss Characteristics

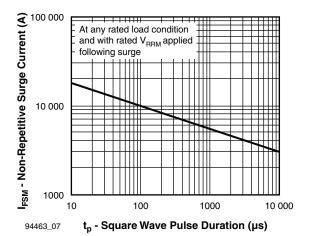


Fig. 7 - Maximum Non-Repetitive Surge Current

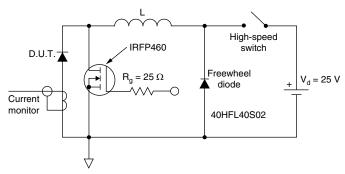


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = rated V<sub>R</sub>

#### **ORDERING INFORMATION TABLE**

VS-045 **Device code** 24 1 Ν Q PbF 2 (3)(4) (5) (6 Vishay Semiconductors product Average current rating (x 10) Product silicon identification N = not isolated Q = Schottky rectifier diode Voltage rating (045 = 45 V) Lead (Pb)-free

| LINKS TO RELATED DOCUMENTS |                          |  |  |  |
|----------------------------|--------------------------|--|--|--|
| Dimensions                 | www.vishay.com/doc?95020 |  |  |  |



### **D-67 HALF-PAK**

### **DIMENSIONS** in millimeters (inches)









### **Legal Disclaimer Notice**

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