Vishay Semiconductors

# High Performance Schottky Rectifier, 3 A

Anode

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SMC (DO-214AB)

### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	3.0 A		
V <sub>R</sub>	100 V		
V <sub>F</sub> at I <sub>F</sub>	0.62 V		
I <sub>RM</sub>	5 mA at 125 °C		
T <sub>J</sub> max.	175 °C		
E <sub>AS</sub>	3.0 mJ		
Package	SMC (DO-214AB)		
Circuit configuration	Single		

### FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **DESCRIPTION / APPLICATIONS**

The VS-30BQ100HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

### **MECHANICAL DATA**

Case: SMC (DO-214AB) Molding compound meets UL 94 V-0 flammability rating Terminals: matte tin plated leads, solderable per J-STD-002

Polarity: color band denotes cathode end

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
I <sub>F(AV)</sub>	Rectangular waveform	3.0	A
V <sub>RRM</sub>		100	V
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	800	A
V <sub>F</sub>	3.0 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.62	V
TJ	Range	-55 to +175	°C

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-30BQ100HM3	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	100	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at T <sub>L</sub> = 148 °C, rectangular waveform		3.0	
		50 % duty cycle at $T_L$ = 138 °C, rectangular waveform		4.0	
Maximum peak one cycle	I	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	800	A
non-repetitive surge current	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	70	
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25 \text{ °C}, I_{AS} = 1.0 \text{ A}, L = 6 \text{ mH}$		3.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by TJ maximum VA = 1.5 x VR typical0.5A		A	

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	3 A	T 05 %C	0.79	V
		6 A	T <sub>J</sub> = 25 °C	0.90	
		3 A	T.I = 125 °C	0.62	
		6 A	IJ = 125 C	0.70	
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 25 °C	V - Poted V	0.5	mA
		T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	5.0	
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		115	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		3.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

Note

<sup>(1)</sup> Pulse width = 300  $\mu$ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +175	°C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>		12	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	46	
Approvimeto weight			0.24	g
Approximate weight			0.008	oz.
Marking device		Case style SMC (DO-214AB) 3J		J

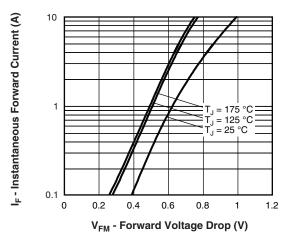
Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ (1)

(2) Mounted 1" square PCB



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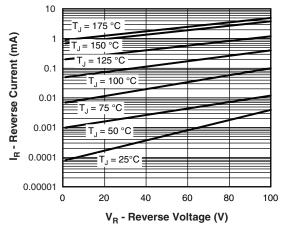


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs.Reverse Voltage (Per Leg)

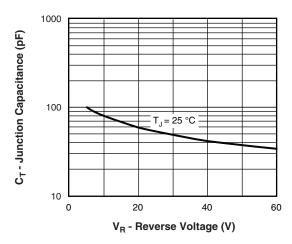


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

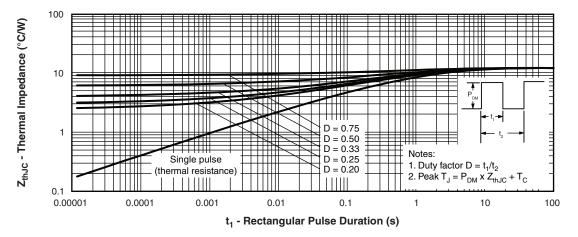
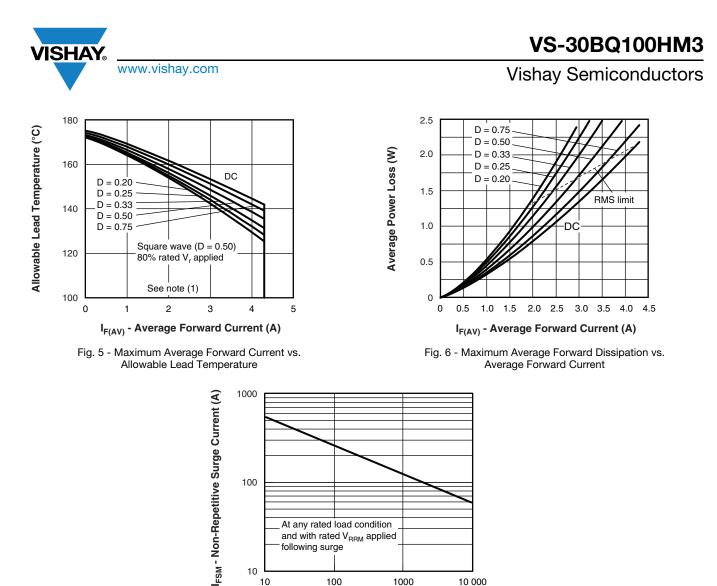


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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At any rated load condition and with rated V<sub>RRM</sub> applied

100

t<sub>n</sub> - Square Wave Pulse Duration (µs) Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

1000

10 000

following surge

10 10

#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
  - $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \ \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

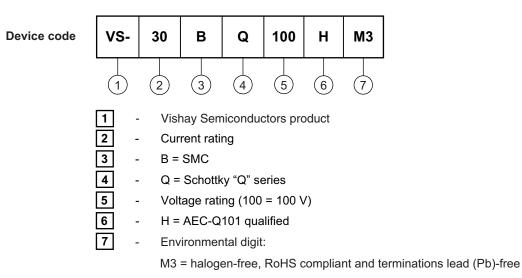
# VS-30BQ100HM3

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ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-30BQ100HM3/9AT	9AT	3500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95402	
Part marking information	www.vishay.com/doc?95403	
Packaging information	www.vishay.com/doc?95404	
SPICE model	www.vishay.com/doc?96932	

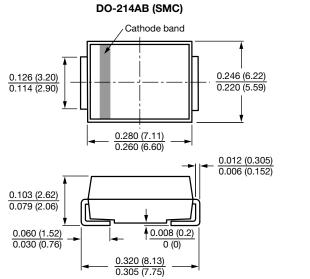


# **Outline Dimensions**

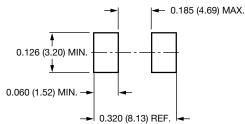
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### **DIMENSIONS** in inches (millimeters)



Mounting Pad Layout





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