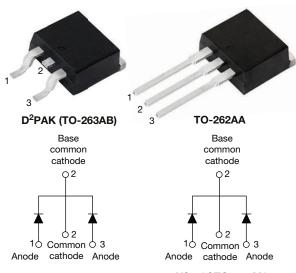


## High Performance Schottky Rectifier, 2 x 6 A



VS-12CTQ...S-M3

VS-12CTQ ... - 1-M3

PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	2 x 6 A							
V <sub>R</sub>	35 V, 40 V, 45 V							
V <sub>F</sub> at I <sub>F</sub>	0.53 V							
I <sub>RM</sub> max.	7.0 mA at 125 °C							
T <sub>J</sub> max.	175 °C							
E <sub>AS</sub>	8 mJ							
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA							
Circuit configuration	Common cathode							

### FEATURES

- 175 °C T<sub>J</sub> operation
- Center tap configuration
- Low forward voltage drop



COMPLIANT

- High purity, high temperature epoxy **FREE** encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245  $^\circ\mathrm{C}$
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-12CTQ... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform	12	А						
V <sub>RRM</sub>	Range	35 to 45	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	690	А						
V <sub>F</sub>	6 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.53	V						
TJ	Range	-55 to +175	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-12CTQ035S-M3 VS-12CTQ035-1-M3	VS-12CTQ040S-M3 VS-12CTQ040-1-M3	VS-12CTQ045S-M3 VS-12CTQ045-1-M3	UNITS				
Maximum DC reverse voltage	aximum DC reverse voltage V <sub>R</sub>		40	45	V				
Maximum working peak reverse voltage	V <sub>RWM</sub>	35	40	45	v				

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## Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS					
Maximum average forward per leg		50 % duty cycle at $T_{C}$ = 160 °C, rectangular waveform		6	٨					
current, see fig. 5 per device	I <sub>F(AV)</sub>	$30\%$ duty cycle at $T_{\rm C} = 100\%$	, rectarigular wavelorm	12	A					
Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated	690						
surge current per leg, see fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	140	A					
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.20 A, L = 11.10 mH		8	mJ					
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		1.20	А					

ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS				
		6 A	T,I = 25 °C	0.60	v				
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	12 A	1j=25 C	0.73					
See fig. 1	V FM (*)	6 A	T.I = 125 °C	0.53					
		12 A	1j=125 C	0.64					
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	$T_J = 25 \ ^{\circ}C$	$V_{\rm B}$ = Rated $V_{\rm B}$	0.8	mA				
See fig. 2	IRM \''	T <sub>J</sub> = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	7.0					
Threshold voltage	V <sub>F(TO)</sub>	T T movimum		0.35	V				
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		18.23	mΩ				
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 1	400	pF					
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm fi	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs				

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 175	°C					
Maximum thermal resistance, junction to case per leg		Р	DC operation See fig. 4	3.50						
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.75	°C/W					
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50						
Approvimento weight				2	g					
Approximate weight				0.07	oz.					
Mounting torque	minimum			6 (5)	kgf · cm					
Mounting torque	maximum			12 (10)	(lbf · in)					
				12CTC	0355					
Marking device			Case style D <sup>2</sup> PAK (TO-263AB)	12CTQ040S						
				12CTC	045S					
				12CTQ	035-1					
			Case style TO-262AA	12CTQ040-1						
				12CTQ045-1						

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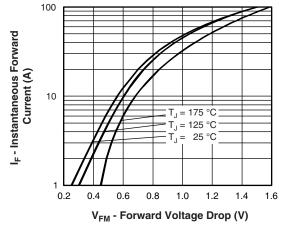


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

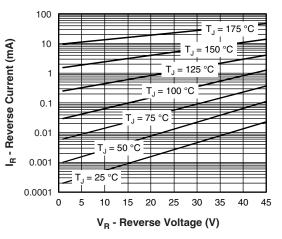


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

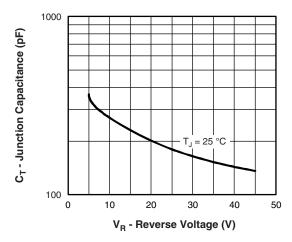
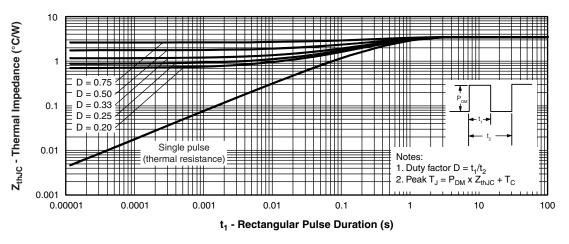


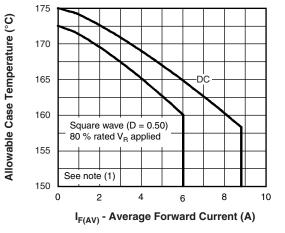
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

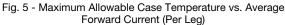




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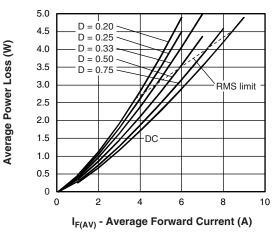


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

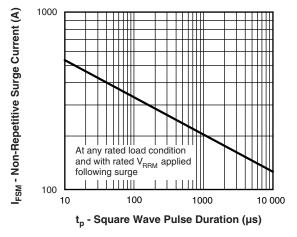


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

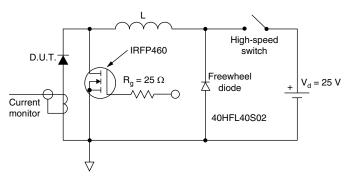


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}$ /D) (see fig. 6);

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 $Pd_{REV}$  = inverse power loss =  $V_{R1} \; x \; I_{R} \; (1$  - D);  $I_{R} \; at \; V_{R1}$  = 80 % rated  $V_{R}$ 

### **ORDERING INFORMATION TABLE**

Device code	VS-	12	С	т	Q	045	S	TRL	-M3
	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)
	1 ·	· Visl	nay Sem	nicondu	ctors pr	oduct			
	2 -	· Cur	rent rati	ng (12 A	<del>\</del> )				
	3 -	· Circ	cuit cont	figuratio	n: C = c	commor	n cathoo	de	
	4 -	• T =	TO-220	)					
	5 -	Sch	ottky "O	Q" serie	S	035 =	35 V		
	6 -	· Vol	tage rati	ngs —		040 =			
	7 -	• S	= D <sup>2</sup> PA	K (TO-2	63AB)	045 =	45 V		
		• -1	= TO-2	62AA					
	8 -	• N	one = tu	ıbe					
		• TI	RL = tap	e and r	eel (left	oriented	d - for D	<sup>2</sup> PAK (1	FO-263/
		• TI	RR = tap	be and r	eel (righ	nt oriente	ed - for	D <sup>2</sup> PAK	(TO-26
	9 -	-M3	3 = halo	gen-free	e, RoHS	-compl	iant, an	d termiı	nation le

ORDERING INFORMATION	ORDERING INFORMATION									
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION								
VS-12CTQ035S-M3	50	Antistatic plastic tubes								
VS-12CTQ035STRL-M3	800	13" diameter plastic tape and reel								
VS-12CTQ035STRR-M3	800	13" diameter plastic tape and reel								
VS-12CTQ045S-M3	50	Antistatic plastic tubes								
VS-12CTQ045STRL-M3	800	13" diameter plastic tape and reel								
VS-12CTQ045STRR-M3	800	13" diameter plastic tape and reel								

LINKS TO RELATED DOCUMENTS								
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164						
Dimensions	TO-262AA	www.vishay.com/doc?96165						
Part marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444						
	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?96424						

# **Outline Dimensions**



D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches

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SHA



SYMBOL	MILLIMETERS		INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

Revision: 08-Jul-15

1

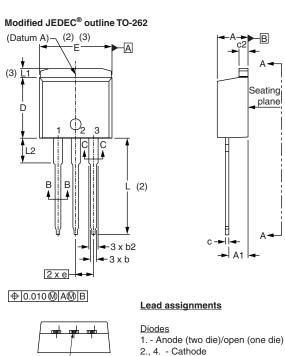
## **Outline Dimensions**



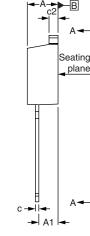
**Vishay Semiconductors** 

**TO-262** 

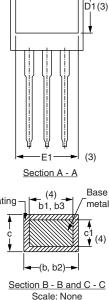
#### **DIMENSIONS** in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 \_ 1.65 0.065 3 \_ 3.36 0.132 0.146 L2 3.71

3. - Anode

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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