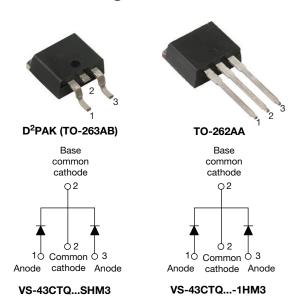
VS-43CTQ...SHM3, VS-43CTQ...-1HM3 Series

Vishay Semiconductors

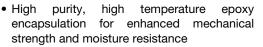
High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS						
I _{F(AV)}	2 x 20 A					
V_{R}	80 V, 100 V					
V _F at I _F	0.67 V					
I _{RM} max.	11 mA at 125 °C					
T _J max.	175 °C					
E _{AS}	7.50 mJ					
Package	D ² PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

FEATURES

- 175 °C T_J operation
- Center tap configuration
- Low forward voltage drop





FREE

- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified meets JESD 201 class 1A whisker test
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I _{F(AV)}	Rectangular waveform	40	А					
V _{RRM}		80/100	V					
I _{FSM}	t _p = 5 μs sine	850	Α					
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.67	V					
TJ	Range	-55 to +175	°C					

VOLTAGE RATINGS							
PARAMETER SYMBOL VS-43CTQ080SHM3 VS-43CTQ100SHM3 VS-43CTQ100-1HM3 UNITS							
Maximum DC reverse voltage	V _R	80	100	V			
Maximum working peak reverse voltage	V_{RWM}	00	100	V			



VS-43CTQ...SHM3, VS-43CTQ...-1HM3 Series

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ABSOLUTE MAXIMUM RATINGS								
PARAMETER	PARAMETER		TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current	per leg	I _{F(AV)}	50 % d d a a da a l T a 405 % a a da a da a a a fa a a		50 0/ d L do - 1 T		20	
See fig. 5			50 % duty cycle at T_C = 135 °C, rectangular waveform		40			
	Maximum peak one cycle non-repetitive surge current per leg See fig. 7		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	850	Α		
			10 ms sine or 6 ms rect. pulse					
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH		7.50	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		0.50	Α		

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS				
		20 A	T _{.I} = 25 °C	0.81			
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	1j=25 C	0.98	V		
See fig. 1	V _{FM} (·)	20 A	T _{.I} = 125 °C	0.67			
		40 A	1 1J = 125 C	0.81			
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_B = \text{rated } V_B$	1	mA		
See fig. 2		T _J = 125 °C	V _R = rated V _R	11			
Threshold voltage	V _{F(TO)}	T T manyimum		0.71	V		
Forward slope resistance	r _t	$T_J = T_J$ maximum		0.43	mΩ		
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal rang	1480	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 mr	8.0	nH			
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHA	THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and stora temperature range	ige	T _J , T _{Stg}		-55 to +175	°C			
Maximum thermal resistance junction to case per leg	€,	R _{thJC}	DC anaration	2.0				
	Maximum thermal resistance, junction to case per package		DC operation	1.0	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50				
Approximate weight				2	g			
Approximate weight				0.07	OZ.			
Maunting torque	minimum			6 (5)	kgf · cm			
Mounting torque	Mounting torque — maximum			12 (10)	(lbf · in)			
			Case style D ² PAK (TO-263AB)	43CTQ080SH				
Marking device			Case style D-PAK (10-203AB)	43CTQ100SH				
			C	43CTQ080-1H				
			Case style TO-262AA	43CTQ100-1H				

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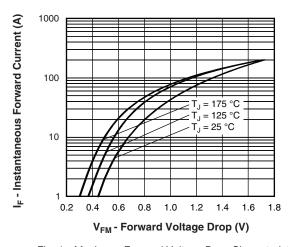


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

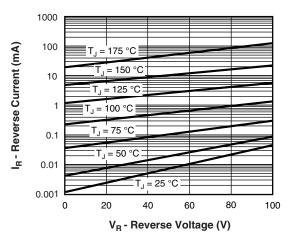


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

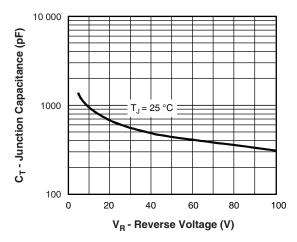


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

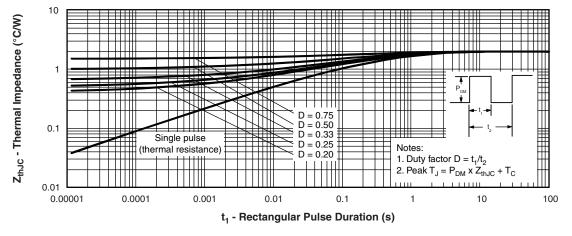


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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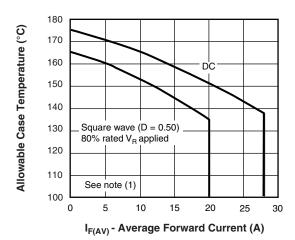


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

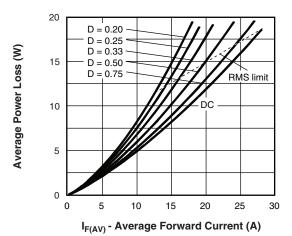


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

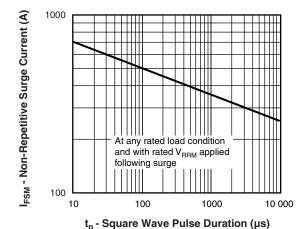


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

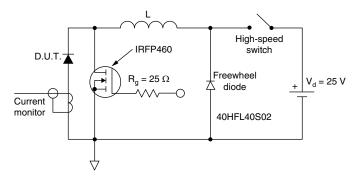


Fig. 8 - Unclamped Inductive Test Circuit

Note

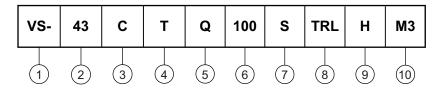
 $\begin{array}{ll} \mbox{(1)} & \mbox{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ \mbox{Pd} = \mbox{forward power loss} = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ \mbox{Pd}_{REV} = \mbox{inverse power loss} = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ at \ V_{R1} = 10 \ V \\ \end{array}$

VS-43CTQ...SHM3, VS-43CTQ...-1HM3 Series

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 A)

3 - Circuit configuration: C = Common cathode

4 - T = TO-220

5 - Schottky "Q" series

6 - Voltage ratings - 080 = 80 V 100 = 100 V

7 - • S = D²PAK

• -1 = TO-262

8 - • None = Tube

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

9 - H = AEC-Q101 qualified

- M3 = Halogen-free, RoHS-compliant and termination lead (Pb)-free

ORDERING INFORMATION								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-43CTQ080SHM3	50	1000	Antistatic plastic tubes					
VS-43CTQ080STRRHM3	800	800	13" diameter reel					
VS-43CTQ080STRLHM3	800	800	13" diameter reel					
VS-43CTQ080-1HM3	50	1000	Antistatic plastic tubes					
VS-43CTQ100SHM3	50	1000	Antistatic plastic tubes					
VS-43CTQ100STRRHM3	800	800	13" diameter reel					
VS-43CTQ100STRLHM3	800	800	13" diameter reel					
VS-43CTQ100-1HM3	50	1000	Antistatic plastic tubes					

LINKS TO RELATED DOCUMENTS						
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?95046				
Difficusions	TO-262AA	www.vishay.com/doc?95419				
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444				
	TO-262AA	www.vishay.com/doc?95443				
Packaging information		www.vishay.com/doc?95032				
SPICE model		www.vishay.com/doc?95065				



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES SYMBOL	MILLIM	ETERS	INC	HES	NOTES		
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOIES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

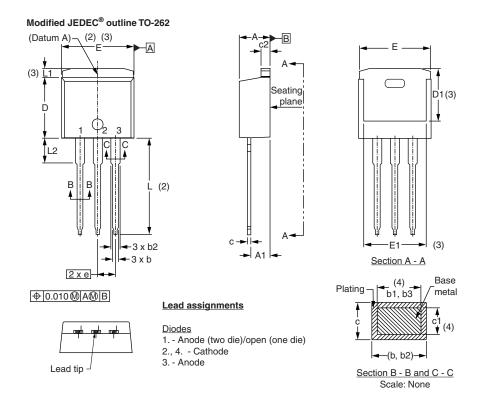
- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	INCHES			
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		
Α	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54	BSC	0.10	D BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.36	3.71	0.132	0.146			

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019 1 Document Number: 95419



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