

Hyperfast Rectifier, 2 x 4 A FRED Pt[®]

eSMP[®] Series



SMPC (TO-277A)



LINKS TO ADDITIONAL RESOURCES


[3D Models](#)

| PRIMARY CHARACTERISTICS | |
|-------------------------|----------------|
| $I_{F(AV)}$ | 2 x 4 A |
| V_R | 200 V |
| V_F at I_F | 0.72 V |
| t_{rr} (typ.) | 25 ns |
| T_J max. | 175 °C |
| Package | SMPC (TO-277A) |
| Circuit configuration | Common cathode |

FEATURES

- Hyperfast recovery time, reduced Q_{rr} , and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT
HALOGEN
FREE

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

| ABSOLUTE MAXIMUM RATINGS | | | | |
|---|----------------|-------------------|-------------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| Peak repetitive reverse voltage | V_{RRM} | | 200 | V |
| Average rectified forward current | $I_{F(AV)}$ | $T_{Sp} = 160$ °C | 8 | A |
| per device | | | 4 | |
| Non-repetitive peak surge current | I_{FSM} | $T_J = 25$ °C | 130 | |
| per diode | | | 70 | |
| Operating junction and storage temperatures | T_J, T_{Stg} | | -55 to +175 | °C |



| ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified) | | | | | | |
|---|----------------------------------|--|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Breakdown voltage, blocking voltage | V _{BR} , V _R | I _R = 100 μA | 200 | - | - | V |
| Forward voltage, per diode | V _F | I _F = 4 A | - | 0.89 | 0.95 | |
| | | I _F = 4 A, T _J = 150 °C | - | 0.72 | 0.78 | |
| Reverse leakage current, per diode | I _R | V _R = V _R rated | - | - | 2 | μA |
| | | T _J = 150 °C, V _R = V _R rated | - | 6 | 80 | |
| Junction capacitance | C _T | V _R = 200 V | - | 17 | - | pF |

| DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified) | | | | | | |
|--|------------------|--|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Reverse recovery time | t _{rr} | I _F = 1.0 A, di/dt = 50 A/μs, V _R = 30 V | - | 25 | - | ns |
| | | I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A | - | - | 25 | |
| | | T _J = 25 °C | - | 18 | - | |
| | | T _J = 125 °C | - | 27 | - | |
| Peak recovery current | I _{RRM} | T _J = 25 °C | - | 2 | - | A |
| | | T _J = 125 °C | - | 3.6 | - | |
| Reverse recovery charge | Q _{rr} | T _J = 25 °C | - | 18 | - | nC |
| | | T _J = 125 °C | - | 50 | - | |

| THERMAL - MECHANICAL SPECIFICATIONS | | | | | | |
|--|-----------------------------------|---------------------------|--------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNITS |
| Maximum junction and storage temperature range | T _J , T _{Stg} | | -55 | - | 175 | °C |
| Thermal resistance, junction to mount, per leg | R _{thJM} | | - | 2.5 | 3.5 | °C/W |
| Thermal resistance, junction to ambient, per leg | R _{thJA} | | - | 80 | - | °C/W |
| Approximate weight | | | 0.1 | | | g |
| | | | 0.0035 | | | oz. |
| Marking device | | Case style SMPC (TO-277A) | QCH2 | | | |

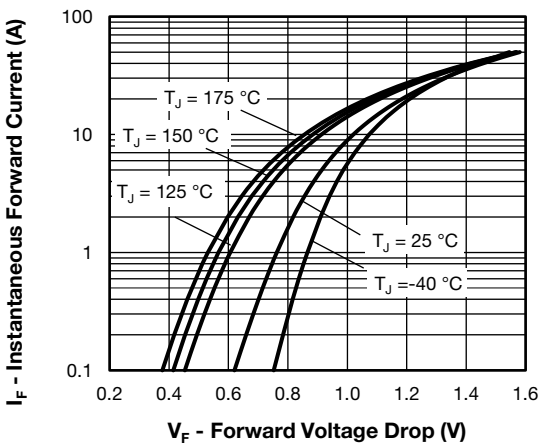


Fig. 1 - Typical Forward Voltage Drop Characteristics

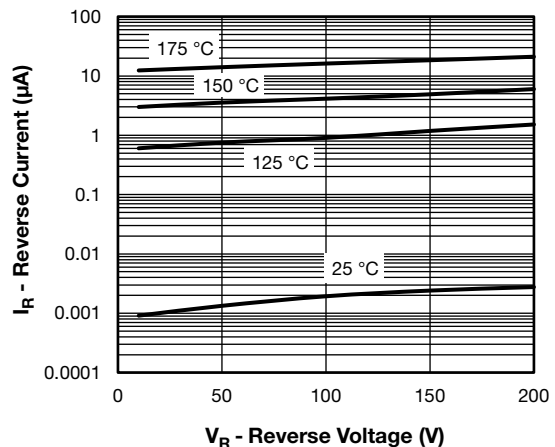


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

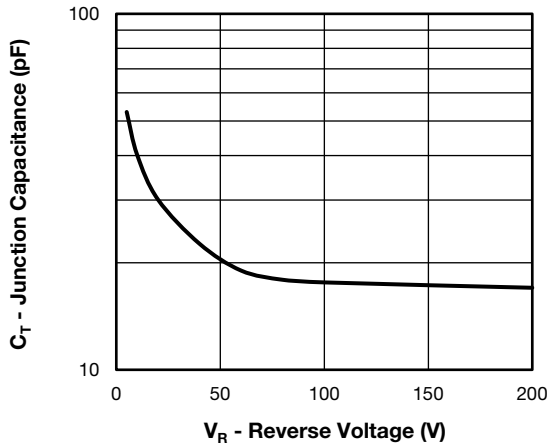


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

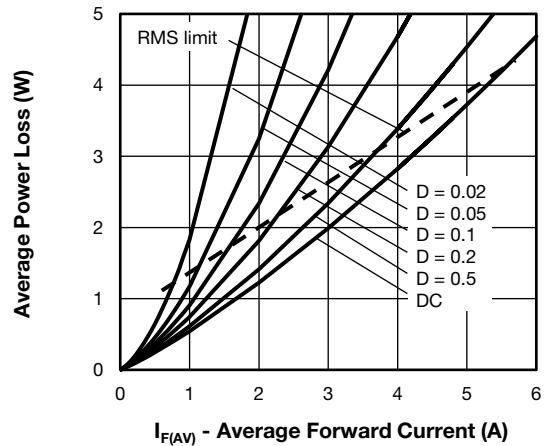


Fig. 5 - Forward Power Loss Characteristics

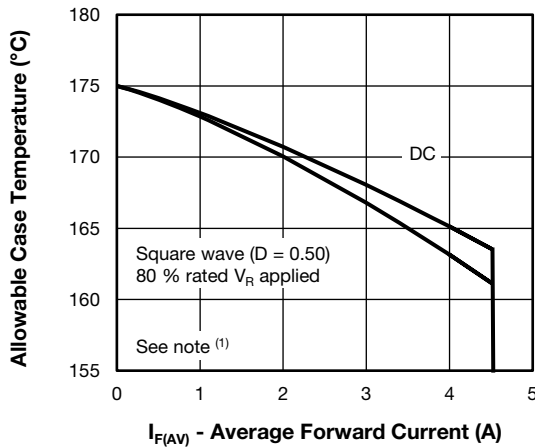


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

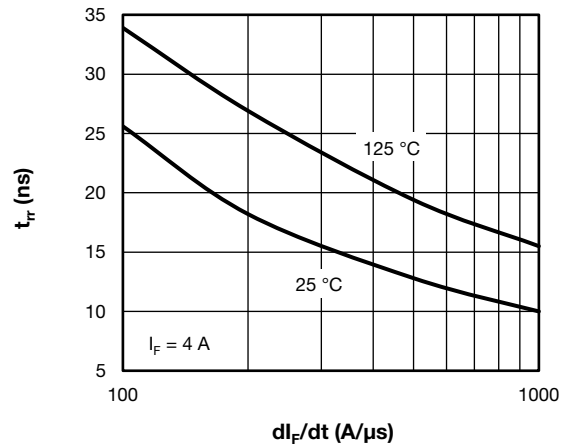


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

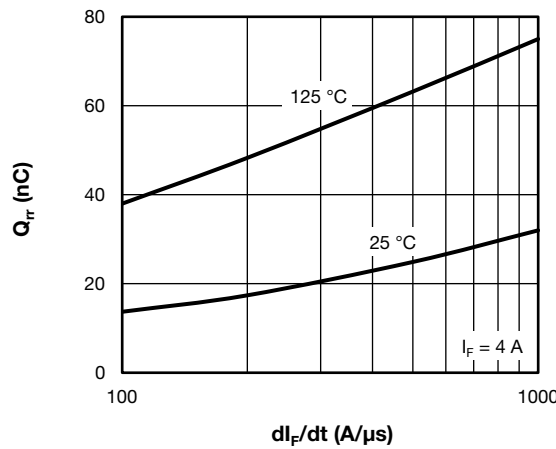


Fig. 7 - Typical Stored Charge vs. dI_F/dt

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 5);
 P_{dREV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = rated V_R

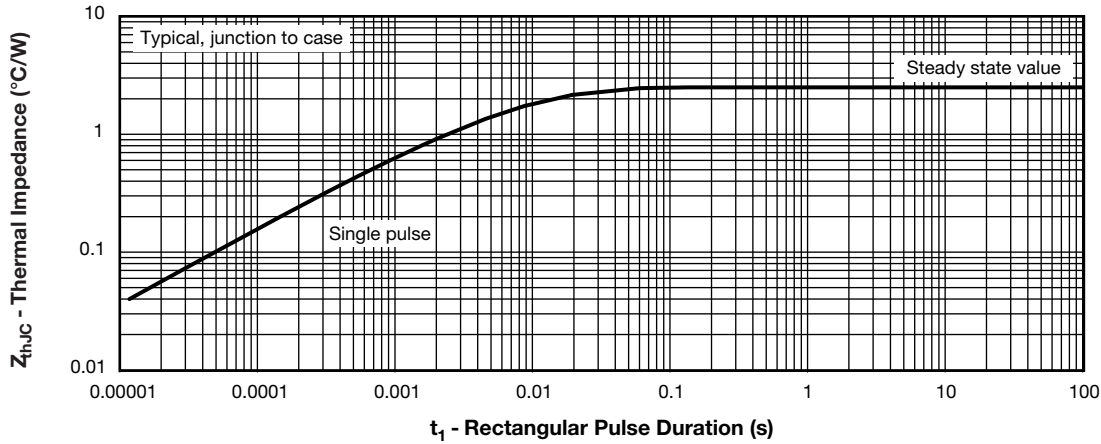


Fig. 8 - Typical Transient Thermal Impedance, Junction to Case

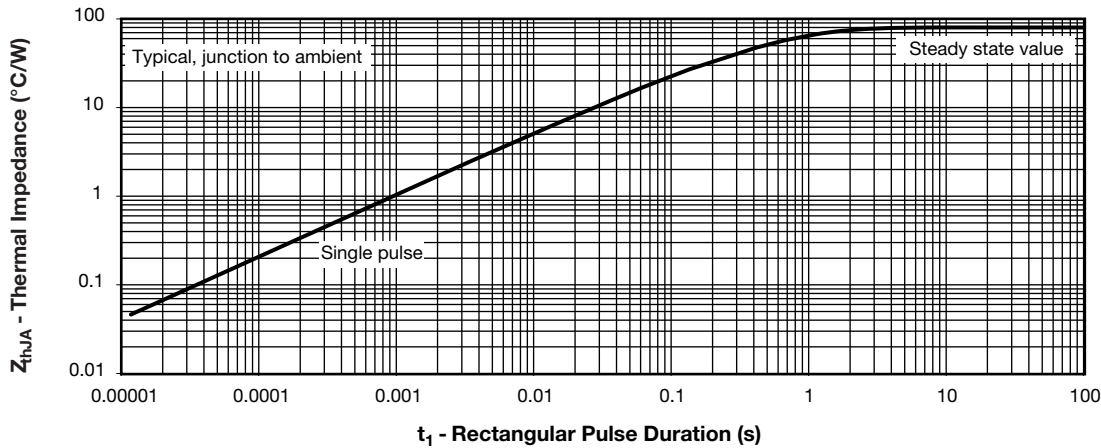
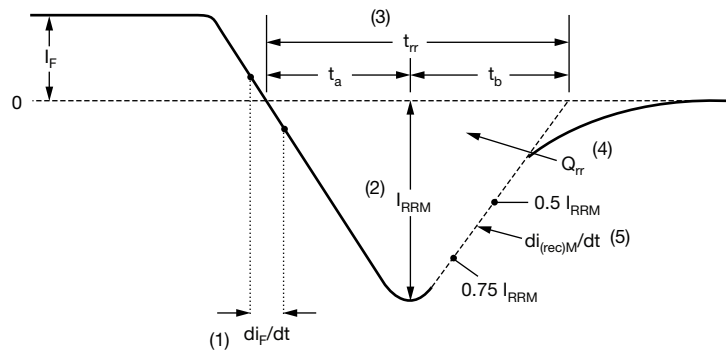


Fig. 9 - Typical Transient Thermal Impedance, Junction to Ambient



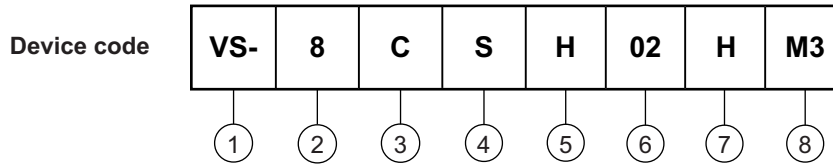
- (1) di_F/dt - rate of change of current through zero crossing
- (2) I_{RRM} - peak reverse recovery current
- (3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through $0.75 I_{RRM}$ and $0.50 I_{RRM}$ extrapolated to zero current.
- (4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}
- (5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

Fig. 10 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE



- 1** - Vishay Semiconductors product
- 2** - Current rating (8 = 8 A)
- 3** - Circuit configuration:
C = common cathode
- 4** - S = SMPC package
- 5** - Process type,
H = hyper fast recovery
- 6** - Voltage code (02 = 200 V)
- 7** - H = AEC-Q101 qualified
- 8** - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

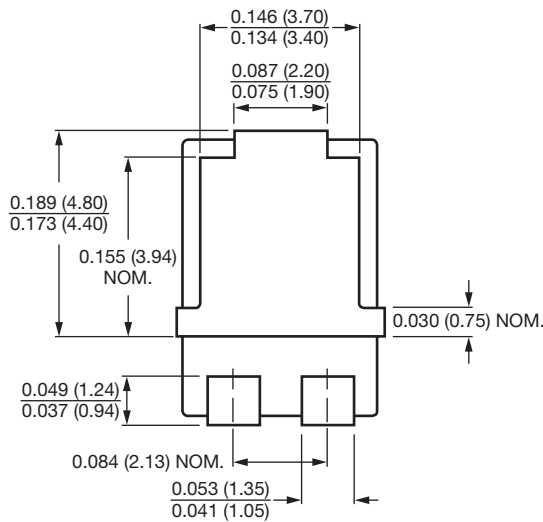
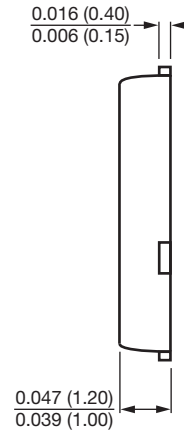
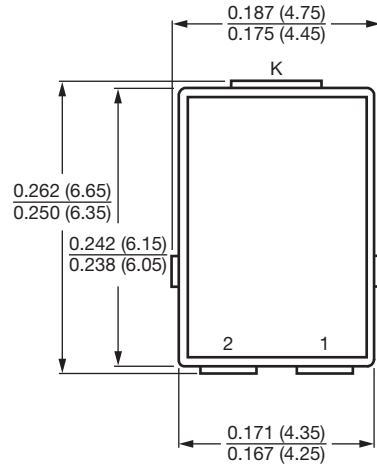
| ORDERING INFORMATION (Example) | | | |
|--------------------------------|-------------------|------------------------|------------------------------------|
| PREFERRED P/N | QUANTITY PER REEL | MINIMUM ORDER QUANTITY | PACKAGING DESCRIPTION |
| VS-8CSH02HM3/86A | 1500 | 1500 | 7" diameter plastic tape and reel |
| VS-8CSH02HM3/87A | 6500 | 6500 | 13" diameter plastic tape and reel |

| LINKS TO RELATED DOCUMENTS | |
|----------------------------|--|
| Dimensions | www.vishay.com/doc?95570 |
| Part marking information | www.vishay.com/doc?95565 |
| Packaging information | www.vishay.com/doc?88869 |
| SPICE model | www.vishay.com/doc?96095 |

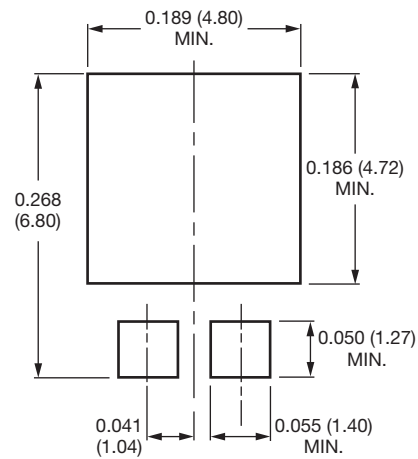


TO-277A (SMPC)

DIMENSIONS in inches (millimeters)



Mounting Pad Layout



Conform to JEDEC® TO-277A



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