VS-50EPU12LHN3

Vishay Semiconductors

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Ultrafast Rectifier, 50 A FRED Pt[®]



PRIMARY CHARACTERISTICS								
I _{F(AV)}	50 A							
V _R	1200 V							
V _F at I _F at 125 °C	1.95 V							
t _{rr}	57 ns							
T _J max.	175 °C							
Package	TO-247AD 2L							
Circuit configuration	Single							

FEATURES

- · Ultrafast and soft recovery
- Optimized forward voltage drop
- 175 °C maximum operating junction temperature
- Polyimide passivation
- Rugged design
- Good thermal performance
- AEC-Q101 gualified available
- Meets JESD 201 class 1A whisker test
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

Ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, recovery time, and soft recovery. Polyimide passivated, planar structure, and the platinum doped life time control guarantee, ruggedness, reliability characteristics, and solid value proposition for efficiency and thermal performance.

These devices are intended for use in boost stage in the AC/DC section of SMPS, high frequency output rectification of battery charger, inverters for solar inverters, or as freewheeling diodes in motor drive.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Repetitive peak reverse voltage	V _{RRM}		1200	V					
Average rectified forward current	I _{F(AV)}	T _C = 138 °C, D = 0.50	50						
Non-repetitive peak surge current	I _{FSM}	T_{C} = 25 °C, t_{p} = 10 ms, sine wave	400	A					
Repetitive peak forward current	I _{FRM}		100						
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C					

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)									
PARAMETER	MIN.	TYP.	MAX.	UNITS					
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 500 μA	1200	-	-				
Forward voltage	V _F	I _F = 50 A	-	- 2.05 2.55		V			
Forward voltage		$I_F = 50 \text{ A}, T_J = 125 \text{ °C}$	-	1.95	2.37				
Reverse leakage current	L	$V_{R} = V_{R}$ rated	-	-	330				
neverse leakage current	I _R	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	580	μΑ			
Junction capacitance	CT	V _R = 200 V	-	55	-	pF			
Series inductance	L _S	Measured to lead 5 mm from package body	-	8	-	nH			

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS			
		$I_F = 1.0 \text{ A}, \text{ d}_F/\text{d}t = 10$	00 A/µs, V _R = 30 V	-	57	-				
Reverse recovery time	t _{rr}	T _J = 25 °C		-	262	-	ns			
		T _J = 125 °C		-	473	-				
Pools recovery ourrent	I _{RRM}	$T_J = 25 \ ^\circ C$	l _F = 50 A dl _F /dt = 100 A/µs	-	9.8	-	A			
Peak recovery current		T _J = 125 °C	$V_{\rm R} = 390 \text{ V}$	-	17	-				
	0	T _J = 25 °C		-	1280	-	5			
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	4056	-	nC			

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Thermal resistance, junction to case	R _{thJC}		-	0.2	0.28					
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	31	34	°C/W				
Thermal resistance, case to heat sink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.22	0.32					
Weight			-	6	-	g				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Maximum junction and storage temperature range	TJ, T _{Stg}		-55	-	175	°C				
Marking device		Case style: TO-247AD 2L		50EPU	J12LH					

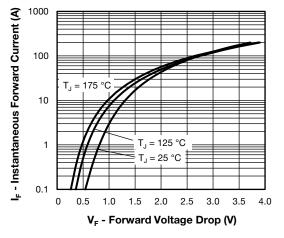


Fig. 1 - Typical Forward Voltage Drop Characteristics

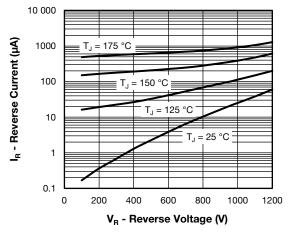


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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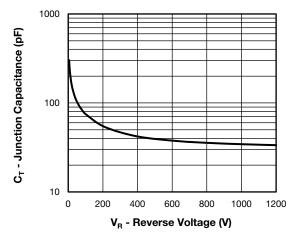


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

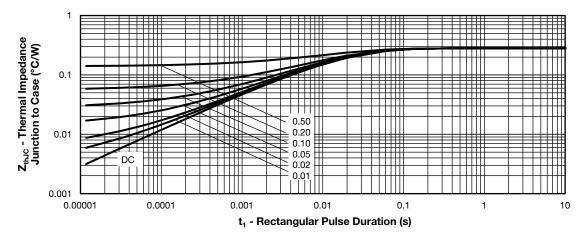
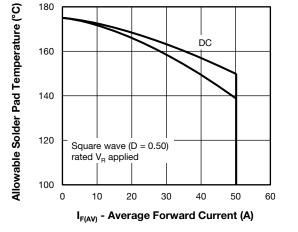
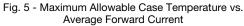


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics





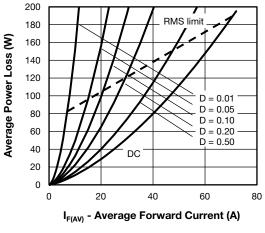


Fig. 6 - Forward Power Loss Characteristics

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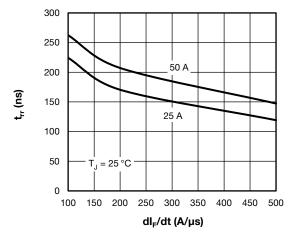


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

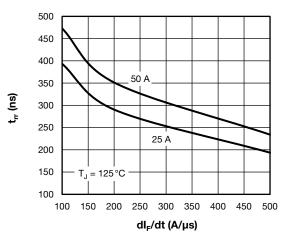


Fig. 8 - Typical Reverse Recovery Time vs. dl_F/dt

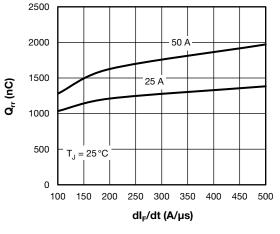
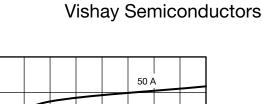
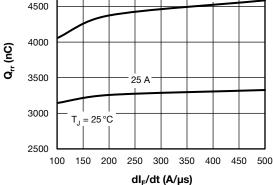


Fig. 9 - Typical Stored Charge vs. dl_F/dt

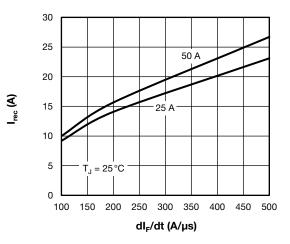


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Fig. 10 - Typical Stored Charge vs. dl_F/dt





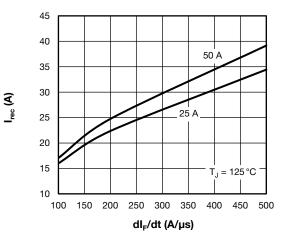


Fig. 12 - Typical Reverse Current vs. dI_F/dt

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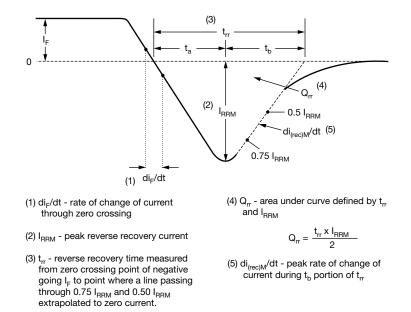


Fig. 13 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

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Device code	VS-	50	Е	Р	U	12	L	Н	N3
		2	3	4	5	6	7	8	9
	1 -		•	niconduc	•	oduct			
	 2 - Current rating (50 = 50 A) 3 - Circuit configuration: E = single diode 								
	4 - P = TO-247 package								
	5 - Process type:								
	U = ultrafast recovery6 - Voltage rating (12 = 1200 V)								
	7 - L = long lead								
	8 - H = AEC-Q101 qualified								
	9 -	Env	vironmer	ntal digit	:				
		N3	= halog	en-free,	RoHS-c	compliar	nt, and t	otally le	ad (Pb)

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-50EPU12LHN3	25	500	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95536					
Part marking information	www.vishay.com/doc?95648					

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TO-247AD 2L

DIMENSIONS in millimeters and inches



Section C - C, D - D

(b, b2)

(4)

View	<u>/ B</u>

SYMBOL	MILLIN	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			E	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102			E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098			е	5.46	BSC	0.215	5 BSC	
b	0.99	1.40	0.039	0.055			ØК	0.2	254	0.0	010	
b1	0.99	1.35	0.039	0.053			L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	
D2	0.51	1.35	0.020	0.053				•		•		•

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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