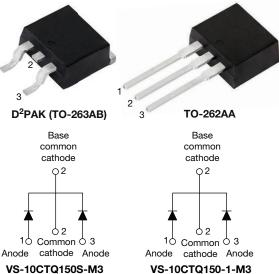
Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 5 A



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VS-10CTQ150S-M3

SHAY

PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 5 A							
V _R	150 V							
V _F at I _F	0.93 V							
I _{RM}	7 mA at 125 °C							
T _J max.	175 °C							
E _{AS}	5 mJ							
Package	D ² PAK (TO-263AB), TO-262AA							
Circuit configuration	Common cathode							

FEATURES

- 175 °C T_J operation
- · Center tap configuration
- · Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	10	A						
V _{RRM}		150	V						
I _{FSM}	t _p = 5 μs sine	620	А						
V _F	5 A _{pk} , T _J = 125 °C (per leg)	0.73	V						
TJ	Range	-55 to +175	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-10CTQ150S-M3 VS-10CTQ150-1-M3	UNITS					
Maximum DC reverse voltage	V _R	150	V					
Maximum working peak reverse voltage	V _{RWM}	150	V					





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ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS					
Maximum average per leg					50.0/ duty quale at T 155.00 reating year way of arm	5	٨		
forward current, see fig. 5 per device	$I_{F(AV)}$ 50 % duty cycle at T_{C} = 155 °C, rectangular waveform		, rectarigular wavelorm	10	A				
Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load	620	A				
surge current per leg, see fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	115					
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 10 mH	T _J = 25 °C, I _{AS} = 1 A, L = 10 mH						
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximu		1	А				

ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS				
		5 A	T _{.1} = 25 °C	0.93					
Maximum forward voltage drop per leg See fig. 1	V (1)	10 A	1 _J =25 C	1.10	v				
	V _{FM} ⁽¹⁾	5 A	T,I = 125 °C	0.73					
		10 A	1j=125 C	0.86					
Maximum reverse leakage current per	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.05	mA				
leg See fig. 2		T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	7					
Threshold voltage	V _{F(TO)}	T T movimum		0.468	V				
Forward slope resistance	r _t	$T_J = T_J maximum$		28	mΩ				
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range	200	pF					
Typical series inductance per leg	L _S	Measured lead to lead 5 mm	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R 10 000							

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

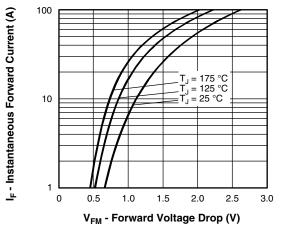
THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C				
Maximum thermal resistance, junction to case per leg		P	DC operation	3.50					
Maximum thermal resistance, junction to case per package		- R _{thJC}	DC operation	1.75	°C/W				
Typical thermal resistance, case to heatsink (only for TO-220)		R _{thCS}	Mounting surface, smooth and greased	0.50					
Approvimate weight				2	g				
Approximate weight				0.07	oz.				
Mounting torque	minimum			6 (5)	kgf ⋅ cm				
Mounting torque	maximum			12 (10)	(lbf ⋅ in)				
Marking davias					Case style D ² PAK (TO-263AB)		50S		
Marking device			Case style TO-262AA	10CTQ1	50-1				

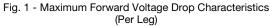
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VS-10CTQ150S-M3, VS-10CTQ150-1-M3

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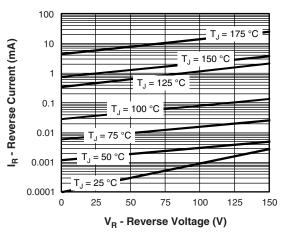


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

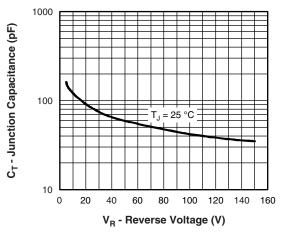


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

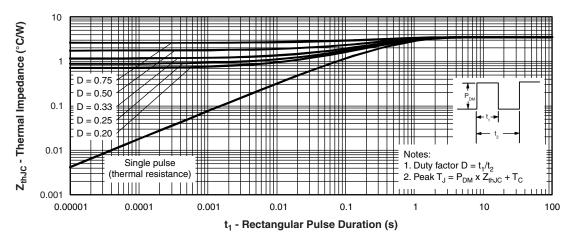


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

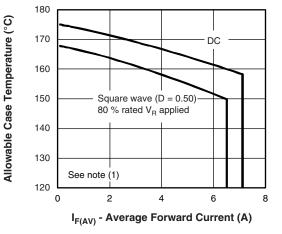
 Revision: 21-Dec-2021
 3
 Document Number: 95729

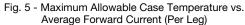
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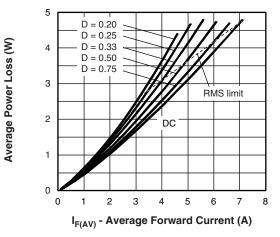


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

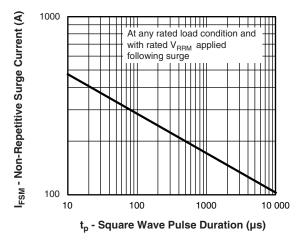


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

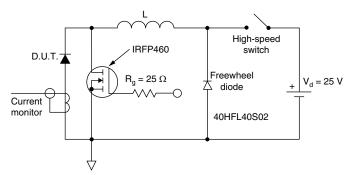


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ \mathsf{(see fig. 6)}; \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{10} \ \mathsf{V} \end{array}$

Revision: 21-Dec-2021

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VS-10CTQ150S-M3, VS-10CTQ150-1-M3

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ORDERING INFORMATION TABLE

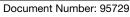
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Device code	vs-	10	С	т	Q	150	S	TRL	-M3
	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)
	1	- Visl	nay Sem	nicondu	ctors pr	oduct			
	2 ·	- Cur	rent rati	ng (10 A	4)				
	3	-							
	4	• T=	TO-220)					
	5	- Sch	nottky "C	Q" serie	S				
	6	- Vol	tage rati	ng (150	= 150 \	/)			
	7	• S	= D ² PA	K (TO-2	63AB)				
		• -1	= TO-2	62AA					
	8 -	• N	one = tu	ıbe (50 j	oieces)				
		• TI	RL = tap	e and r	eel (left	orienteo	d - for D	² PAK (1	FO-263/
	_	• TI	RR = tap	be and r	eel (righ	t orient	ed - for	D ² PAK	(TO-26
	9.	M3	3 = halo	gen-free	e, RoHS	-compl	iant, an	d termiı	nation le

ORDERING INFORMATION (Example)									
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION							
VS-10CTQ150S-M3	50	Antistatic plastic tubes							
VS-10CTQ150STRL-M3	800	13" diameter plastic tape and reel							
VS-10CTQ150STRR-M3	800	13" diameter plastic tape and reel							
VS-10CTQ150-1-M3	50	Antistatic plastic tubes							

LINKS TO RELATED DOCUMENTS								
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164						
Dimensions	TO-262AA	www.vishay.com/doc?96165						
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444						
	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?96424						



Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

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SHA



SYMBOL	MILLIMETERS		INC	HES	NOTES		SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

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Outline Dimensions



Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 _ 1.65 0.065 3 _ 3.36 0.132 0.146 L2 3.71

3. - Anode

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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Document Number: 95419

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