High Performance Schottky Rectifier, 1.0 A





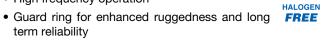
SMB (DO-214AA)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	1.0 A			
V _R	40 V			
V _F at I _F	0.53 V			
I _{RM} max.	4.0 mA at 150 °C			
E _{AS}	3.0 mJ			
T _J max.	150 °C			
Package	SMB (DO-214AA)			
Circuit configuration	Single			

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FEATURES

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation



- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-MBRS140-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.0	А	
V _{RRM}		40	V	
I _{FSM}	t _p = 5 μs sine	380	А	
V _F	1.0 A _{pk} , T _J = 125 °C	0.53	V	
TJ	Range	-55 to +150	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-MBRS140-M3	UNITS		
Maximum DC reverse voltage	V _R	40	V		
Maximum working peak reverse voltage	V _{RWM}	40	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 119 °C, rectangular waveform		1.0	
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	380	А
non-repetitive surge current	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	40	~
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I _{AR}	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$		А	

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COMPLIANT



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.52	0.6	V
Maximum forward voltage drep		2 A		0.70	0.77	
Maximum forward voltage drop		1 A	T _J = 125 °C	0.48	0.53	
		2 A		0.63	0.71	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	-	0.1	mA
		T _J = 125 °C		-	4.0	ША
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		-	80	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		-	2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

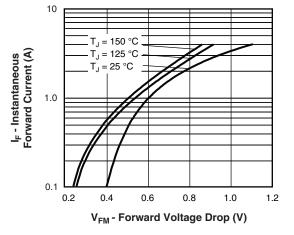
THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation See fig. 4	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	0/10
Approximate weight			0.10	g
			0.003	oz.
Marking device		Case style SMB (DO-214AA)	14	

Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB





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SHAY

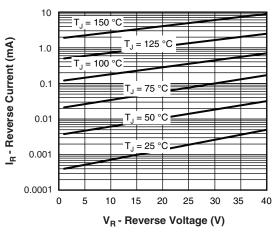
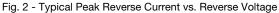


Fig. 1 - Maximum Forward Voltage Drop Characteristics



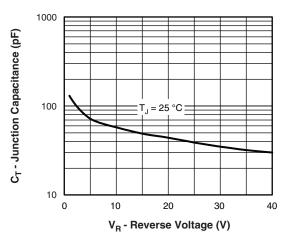


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

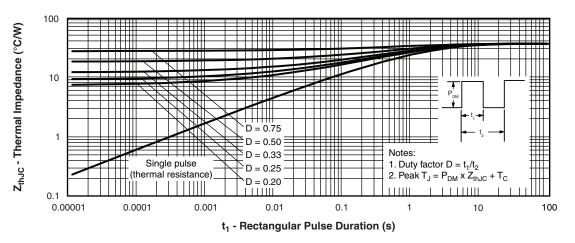
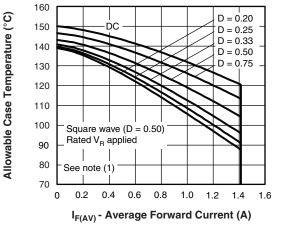


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

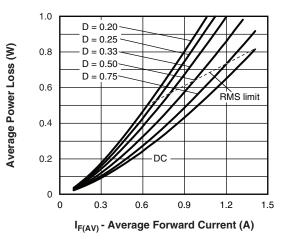


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

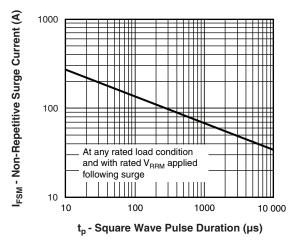


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

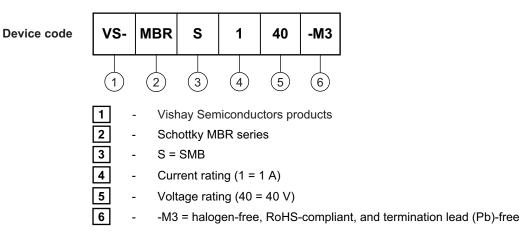
- ⁽³⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
 - $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)				
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-MBRS140-M3/5BT	5BT	3200	13" diameter plastic tape and reel	

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95401		
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		
SPICE model	www.vishay.com/doc?95299		



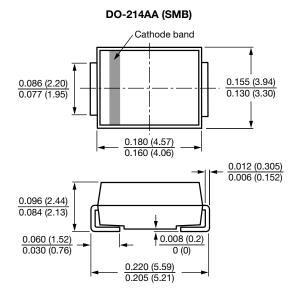


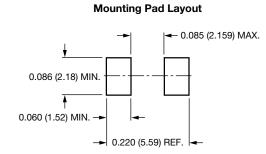
Outline Dimensions

Vishay Semiconductors

SMB

DIMENSIONS in inches (millimeters)







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