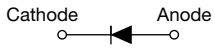
Ultrafast Rectifier, 3 A FRED Pt®

SMB (DO-214AA)



FEATURES

- Ultrafast recovery time, reduced Q_{rr} and soft recovery
- 175 °C maximum operating junction temperature
- For PFC CRM/CCM, snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AUTOMOTIVE GRADE

RoHS COMPLIANT

HALOGEN FREE

LINKS TO ADDITIONAL RESOURCES

3D Models

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	3 A
V_R	600 V
V_F at I_F	0.99 V
t_{rr} typ.	41 ns
T_J max.	175 °C
Package	SMB (DO-214AA)
Circuit configuration	Single

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, ultrafast recovery time, and fast recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SMB (DO-214AA)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002

Polarity: color band denotes the cathode end

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**VALUES**	**UNITS**
Peak repetitive reverse voltage	V_{RRM}		600	V
Average rectified forward current	$I_{F(AV)}$	T_L = 110 °C (1)	3	A
Non-repetitive peak surge current per leg	I_{FSM}	T_J = 25 °C, 6 ms square pulse	55	
Operating junction and storage temperatures	T_J, T_{Stg}		-55 to +175	°C

Note

(1) Mounted on PCB with minimum pad size

ELECTRICAL SPECIFICATIONS (T_J = 25 °C unless otherwise specified)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNITS**
Breakdown voltage, blocking voltage	V_{BR}, V_R	I_R = 100 µA	600	-	-	V
Forward voltage	V_F	I_F = 3 A	-	1.15	1.35	
		I_F = 3 A, T_J = 150 °C	-	0.99	1.2	
Reverse leakage current	I_R	V_R = V_R rated	-	-	3	µA
		T_J = 150 °C, V_R = V_R rated	-	-	100	
Junction capacitance	C_T	V_R = 600 V	-	3.9	-	pF

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DYNAMIC RECOVERY CHARACTERISTICS (T_J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	I_F = 1.0 A, dI_F/dt = 100 A/µs, V_R = 30 V		-	41	-	ns
		I_F = 1.0 A, dI_F/dt = 50 A/µs, V_R = 30 V		-	52	-	
		I_F = 0.5 A, I_R = 1 A, I_{rr} = 0.25 A		-	-	65	
		T_J = 25 °C	I_F = 3 A dI_F/dt = 200 A/µs V_R = 390 V	-	38	-	
		T_J = 125 °C		-	52	-	
Peak recovery current	I_{RRM}	T_J = 25 °C		-	5.6	-	A
		T_J = 125 °C		-	7.3	-	
Reverse recovery charge	Q_{rr}	T_J = 25 °C		-	108	-	nC
		T_J = 125 °C		-	193	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		-55	-	+175	°C
Thermal resistance, junction to mount	R_{thJM} (1)		-	-	18	°C/W
Thermal resistance, junction to ambient	R_{thJA} (1)		-	-	90	
Approximate Weight			0.1			g
			0.003			oz.
Marking device		Case style SMB (DO-214AA)	3U6H			

Note

(1) Mounted on PCB with minimum pad size

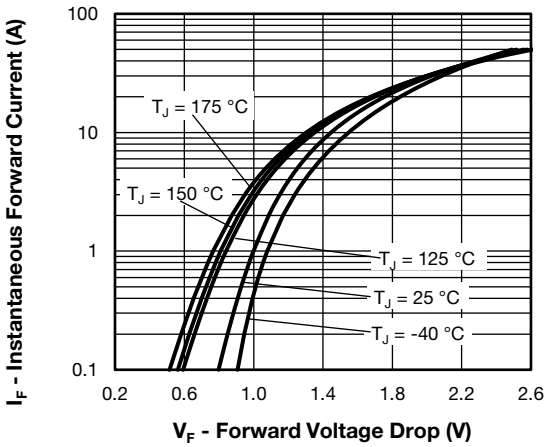

Fig. 1 - Typical Forward Voltage Drop Characteristics

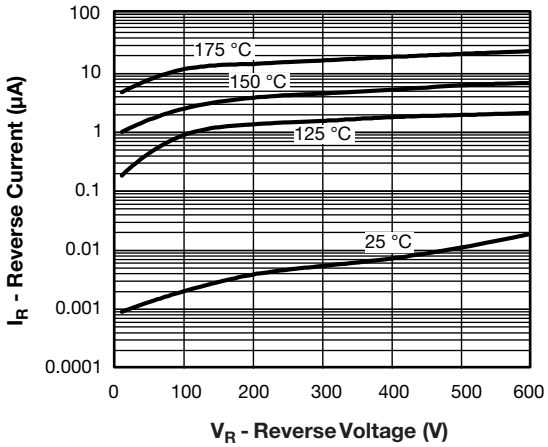

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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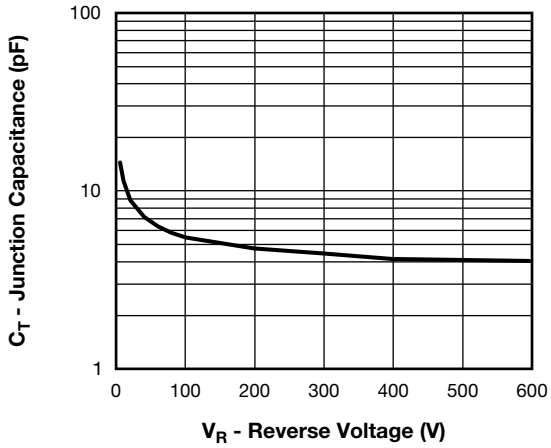

Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

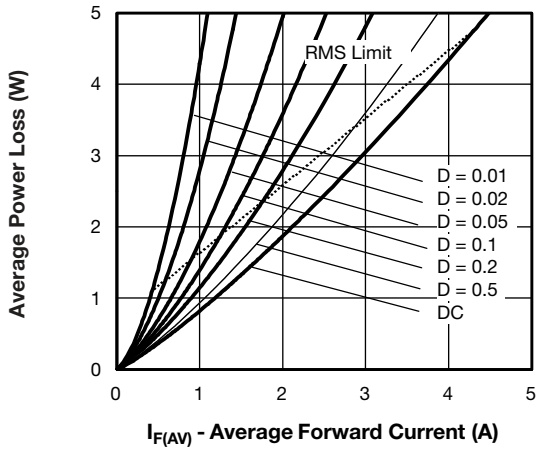

Fig. 5 - Forward Power Loss Characteristics

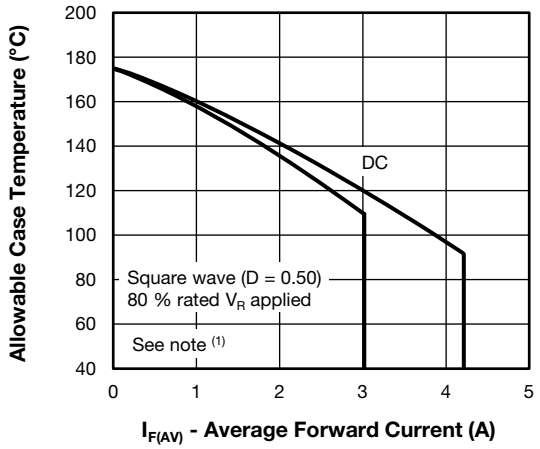

Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

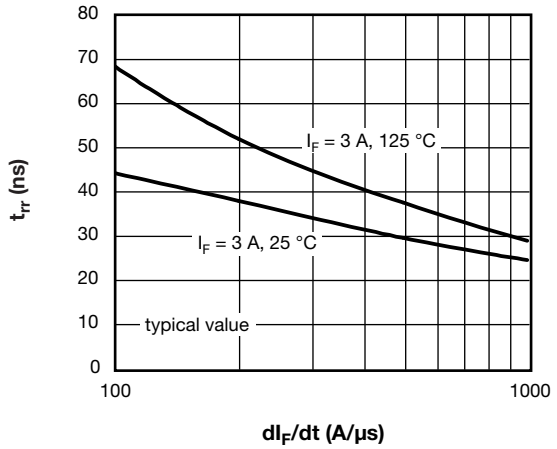

Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

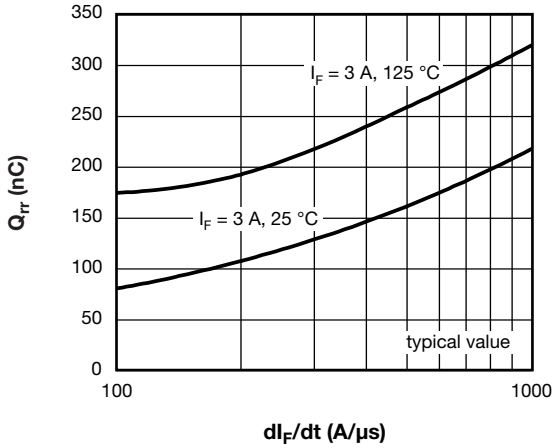

Fig. 7 - Typical Stored Charge vs. dI_F/dt

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}$/D) (see fig. 5);
Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R

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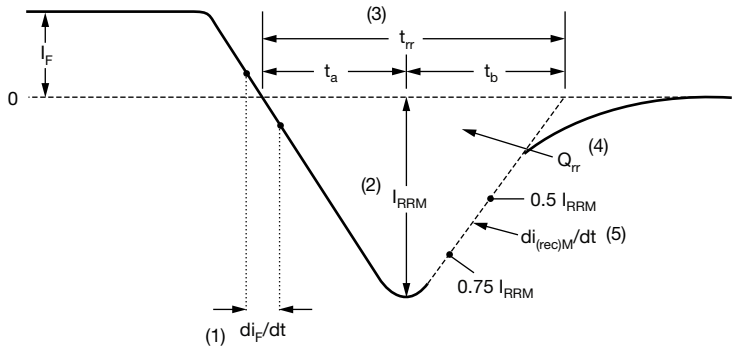

(1) di_F/dt - rate of change of current through zero crossing

(2) I_{RRM} - peak reverse recovery current

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code	VS-	3	E	G	U	06	W	H	M3
	1	2	3	4	5	6	7	8	9

- 1 - Vishay Semiconductors product
- 2 - Current rating (3 = 3 A)
- 3 - Circuit configuration:
  E = single diode
- 4 - G = SMB package
- 5 - Process type,
  U = ultrafast recovery
- 6 - Voltage code (06 = 600 V)
- 7 - W = special
- 8 - H = AEC-Q101 qualified
- 9 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	**PACKAGE CODE**	**MINIMUM ORDER QUANTITY**	**PACKAGING DESCRIPTION**
VS-3EGU06WHM3/5BT	5BT	3200	13"diameter plastic tape and reel

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95401
Part marking information	www.vishay.com/doc?95624
Packaging information	www.vishay.com/doc?95404
SPICE model	www.vishay.com/doc?96667

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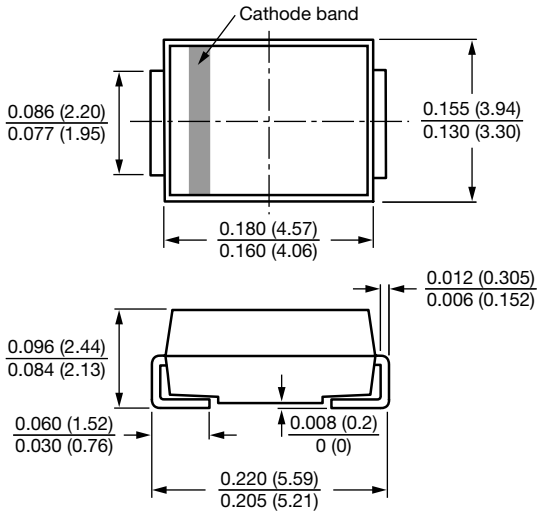
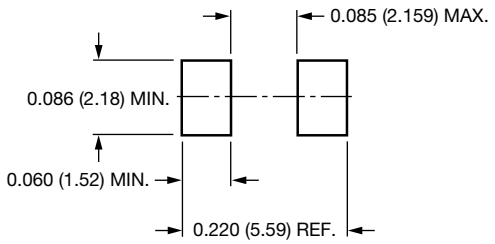
SMB

DIMENSIONS in inches (millimeters)

DO-214AA (SMB)



Mounting Pad Layout



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