

High Performance Schottky Rectifier, 1 A

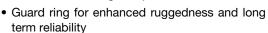


SMA (DO-214AC)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	1 A			
V_R	40 V			
V _F at I _F	0.49 V			
I _{RM}	26 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	3.0 mJ			
Package	SMA (DO-214AC)			
Circuit configuration	Single			

FEATURES







• Small foot print, surface mountable

- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-MBRA140-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNIT			
I _{F(AV)}	Rectangular waveform	1	Α		
V _{RRM}		40	V		
I _{FSM}	$t_p = 5 \mu s sine$	120	Α		
V _F	1.5 A _{pk} , T _J = 125 °C	0.56	V		
T _J	Range	-55 to +150	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-MBRA140-M3	UNITS
Maximum DC reverse voltage	V_R	40	V
Maximum working peak reverse voltage	V_{RWM}	40	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 4		50 % duty cycle at T _L = 123 °C, rectangular waveform On PC board 9 mm ² island (0.013 mm thick copper pad area)		1.5	- A
	I _{F(AV)}	50 % duty cycle at T _L = 132 °C, rectangular waveform On PC board 9 mm ² island (0.013 mm thick copper pad area)		1	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	120	
non-repetitive surge current See fig. 6	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	30	А
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 6 \text{mH}$		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		1.0	А



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		1 A	T _{.1} = 25 °C	0.54	V
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1.5 A	1j = 25 C	0.62	
See fig. 1	1 A T _J = 125 °C	1 A	T _ 105 °C	0.49	
		0.56]		
Maximum reverse leakage current		T _J = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	0.5	- mA
See fig. 2	I _{RM}	T _J = 125 °C	v _R = nateu v _R	26	IIIA
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.36	V
Forward slope resistance	r _t			104	$m\Omega$
Typical junction capacitance	C _T	V _R = 10 V _{DC} , T _J = 25 °C, test signal = 1 MHz		38	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width = 300 $\mu s,$ duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	OZ.
Marking device		Case style SMA (DO-214AC)	1	4

Note

$$^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$

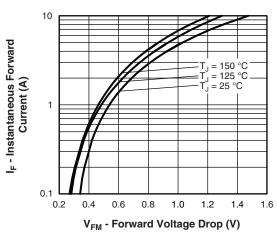


Fig. 1 - Maximum Forward Voltage Drop Characteristics

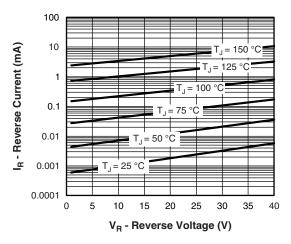


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

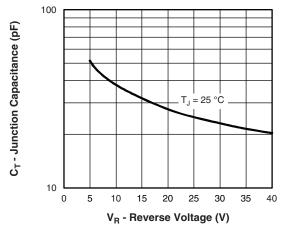


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

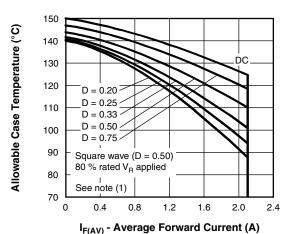


Fig. 4 - Maximum Average Forward Current vs.
Allowable Lead Temperature

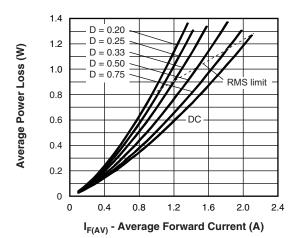


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

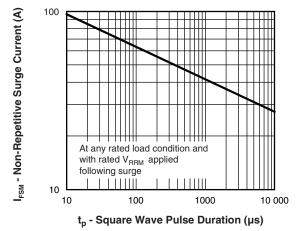


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

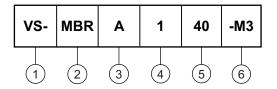
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Schottky MBR series
- 3 A = SMA
- 4 Current rating (1 = 1A)
- 5 Voltage rating (040 = 40 V)
- 6 Environmental digit:

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-MBRA140-M3/5AT	5AT	7500	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95400			
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?96008			



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