VS-GT80DA60U

Vishay Semiconductors

Insulated Gate Bipolar Transistor (Trench IGBT), 600 V, 80 A



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PRIMARY CHARACTERISTICS								
V _{CES}	600 V							
I _C DC	80 A at T _C = 97 °C							
V _{CE(on)} typical at 80 A, 25 °C	1.83 V							
I _{F (DC)}	56 A at T _C = 100 °C							
Speed	8 kHz to 30 kHz							
Package	SOT-227							
Circuit configuration	Single switch with AP diode							

FEATURES

- High speed trench gate field-stop IGBT positive temperature coefficient
- T_{.1} maximum = 175 °C
- FRED Pt® anti-parallel diodes with ultrasoft reverse recovery
- Fully isolated package
- Very low internal inductance (≤ 5 nH typical)
- · Industry standard outline
- UL approved file E78996
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- · Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- Easy to assemble and safe paralleling
- Direct mounting to heatsink
- Plug-in compatible with other SOT-227 packages
- · Lower conduction losses and switching losses
- Low EMI, requires less snubbing

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter voltage	V _{CES}		600	V		
Continuous collector current	1.	T _C = 25 °C	123			
Continuous collector current	Ι _C	$T_{\rm C} = 90 ^{\circ}{\rm C}$	85			
Pulsed collector current	I _{CM}		315	А		
		T _C = 25 °C	85			
Diode continuous forward current	IF	$T_{\rm C} = 90 \ ^{\circ}{\rm C}$	60			
Gate-to-emitter voltage	V _{GE}		± 20	V		
Dever dissignation LCDT	Б	T _C = 25 °C	454			
Power dissipation, IGBT	PD	$T_{\rm C} = 90 \ ^{\circ}{\rm C}$	258	w		
Power dissipation, diode	р	T _C = 25 °C	238	vv		
	PD	$T_{\rm C} = 90 \ ^{\circ}{\rm C}$	135			
Isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V		



RoHS COMPLIANT





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ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{BR(CES)}	$V_{GE} = 0 V, I_C = 2.0 mA$	600	-	-		
		$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 80 \text{ A}$	-	1.83	2.45	v	
Collector to emitter voltage	V _{CE(on)}	V_{GE} = 15 V, I _C = 80 A, T _J = 125 °C	-	2.12	-		
		$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 80 \text{ A}, \text{ T}_{J} = 150^{\circ}\text{C}$	-	2.2	-		
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 1.0 \text{ mA}$	4.6	5.6	7.5		
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)} / \Delta T_J$	V_{CE} = V_{GE} , I_C = 1.0 mA (25 °C to 150 °C)	-	-18.8	-	mV/°C	
		$V_{GE} = 0 V, V_{CE} = 600 V$	-	0.2	100		
Collector to emitter leakage current	I _{CES}	V_{GE} = 0 V, V_{CE} = 600 V, T_{J} = 125 °C	-	51	-	μA	
		V_{GE} = 0 V, V_{CE} = 600 V, T_{J} = 150 °C	-	259	-		
		I _F = 80 A, V _{GE} = 0 V	-	1.92	3.15		
Forward voltage drop, diode	V _{FM}	$I_F = 80 \text{ A}, V_{GE} = 0 \text{ V}, T_J = 125 ^\circ\text{C}$	-	1.61	-	V	
		$I_F = 80 \text{ A}, V_{GE} = 0 \text{ V}, T_J = 150 ^\circ\text{C}$	-	1.54	-]	
Gate to emitter leakage current	I _{GES}	$V_{GE} = \pm 20 \text{ V}$	-	-	± 250	nA	

SWITCHING CHARACTERIS PARAMETER	SYMBOL	TEST CONDIT	MIN.	TYP.	MAX.	UNITS	
Input capacitance	C _{iss}		-	10 800	-		
Output capacitance	C _{oss}	$V_{GE} = 0 V, V_{CE} = 25 V, f =$	-	390	-	pF	
Reverse transfer capacitance	C _{rss}		-	220	-		
Total gate charge (turn-on)	Qq					-	
Gate to emitter charge (turn-on)	Q _{qe}	I _C = 80 A, V _{CC} = 480 V, V	/ _{GF} = 15 V	-	76	_	nC
Gate to collector charge (turn-on)	Q _{gc}			-	184	-	
Turn-on switching loss	E _{on}			-	1.95	-	
Turn-off switching loss	E _{off}			-	1.25	-	mJ
Total switching loss	E _{tot}	$I_{\rm C} = 80$ A, $V_{\rm CC} = 300$ V,		-	3.2	-	
Turn-on delay time	t _{d(on)}	$V_{GE} = 15 \text{ V}, \text{ R}_{g} = 27 \Omega,$	Energy losses include tail and diode recovery.	-	120	-	ns
Rise time	t _r	L = 500 μH, T _J = 25 °C		-	90	-	
Turn-off delay time	t _{d(off)}			-	442	-	
Fall time	t _f			-	35	-	
Turn-on switching loss	E _{on}			-	2.3	-	mJ ns
Turn-off switching loss	E _{off}			-	1.43	-	
Total switching loss	E _{tot}	$I_{\rm C} = 80 \text{ A}, V_{\rm CC} = 300 \text{ V},$		-	3.73	-	
Turn-on delay time	t _{d(on)}	$V_{GE} = 15 \text{ V}, \text{ R}_{q} = 27 \Omega,$		-	124	-	
Rise time	t _r	L = 500 µH, T _J = 125 °C		-	94	-	
Turn-off delay time	t _{d(off)}			-	455	-	
Fall time	t _f			-	43	-	
Diode reverse recovery time	t _{rr}			-	69	-	ns
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/	⁄μs, V _R = 200 V	-	4.9	-	А
Diode recovery charge	Q _{rr}			-	169	-	nC
Diode reverse recovery time	t _{rr}		,	-	139	-	ns
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/ V _B = 200 V, T _J = 125 °C	′μs,	-	12.2	-	А
Diode recovery charge	Q _{rr}	$V_{\rm H} = 200 V, V_{\rm J} = 120 V$	-	856	-	nC	

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THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction temperature range		TJ		-40	-	175	°C
Storage temperature range		T _{Stg}		-40	-	150	°C
Junction-to-case	IGBT	Р		-	-	0.33	
Diode		R _{thJC}		-	-	0.63	°C/W
Case-to-heatsink		R _{thCS}	Flat, greased surface	-	0.1	-	
Weight				-	30	-	g
Mounting torque			Torque to terminal	-	-	1.1 (9.7)	Nm (lbf. in)
Mounting torque			Torque to heatsink	-	-	1.3 (11.5)	Nm (lbf. in)
Case style			S	OT-227			

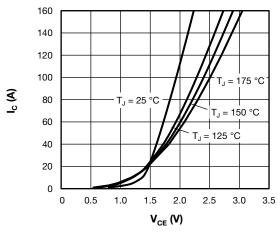


Fig. 1 - Typical IGBT Output Characteristics, V_{GE} = 15 V

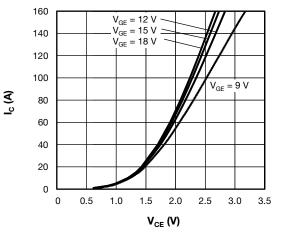
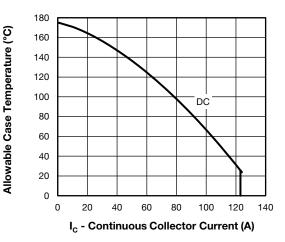


Fig. 2 - Typical IGBT Output Characteristics, T_J = 125 °C





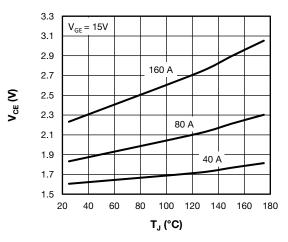
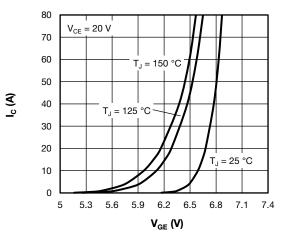


Fig. 4 - Collector to Emitter Voltage vs. Junction Temperature



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Fig. 5 - Typical IGBT Transfer Characteristics

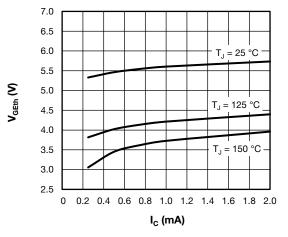


Fig. 6 - Typical IGBT Gate Threshold Voltage

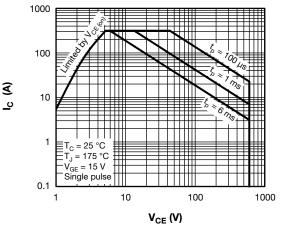


Fig. 7 - IGBT Safe Operating Area

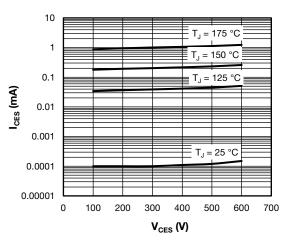
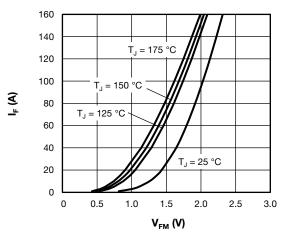
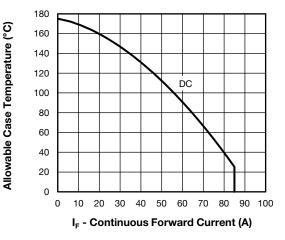


Fig. 8 - Typical IGBT Zero Gate Voltage Collector Current









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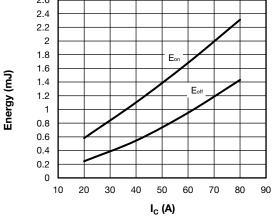


Fig. 11 - Typical IGBT Energy Loss vs. I_C T_J = 125 °C, V_{CC} = 300 V, R_g = 27 $\Omega,$ V_{GE} = 15 V, L = 500 μH

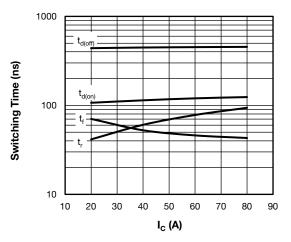
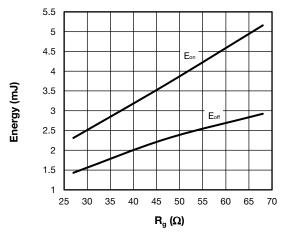
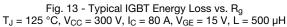


Fig. 12 - Typical IGBT Switching Time vs. I_C T_J = 125 °C, V_{CC} = 300 V, R_g = 27 $\Omega,$ V_{GE} = 15 V, L = 500 μH





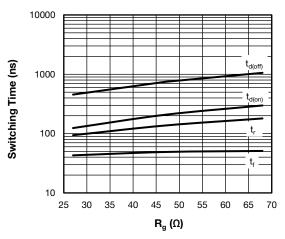
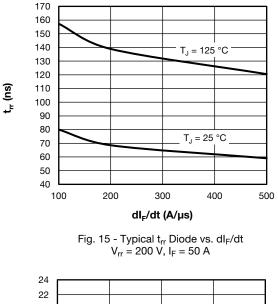


Fig. 14 - Typical IGBT Switching Time vs. R_g T_J = 125 °C, V_{CC} = 300 V, I_C = 80 A, V_{GE} = 15 V, L = 500 μH



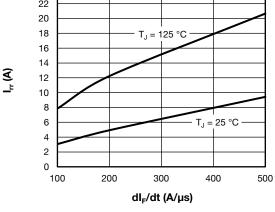


Fig. 16 - Typical I_{rr} Diode vs. dI_F/dt V_{rr} = 200 V, I_F = 50 A

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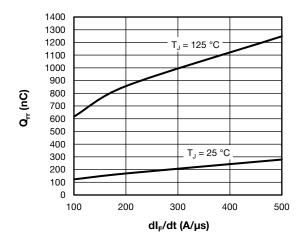


Fig. 17 - Typical Diode Reverse Recovery Charge vs. dl_F/dt V_{rr} = 200 V, l_F = 50 A

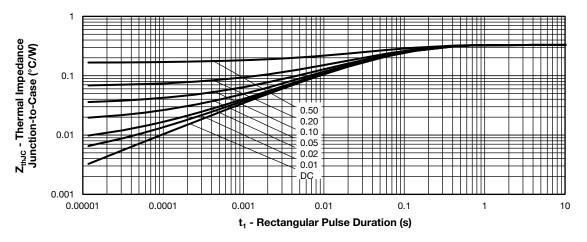


Fig. 18 - Maximum Thermal Impedance Z_{thJC} Characteristics, IGBT

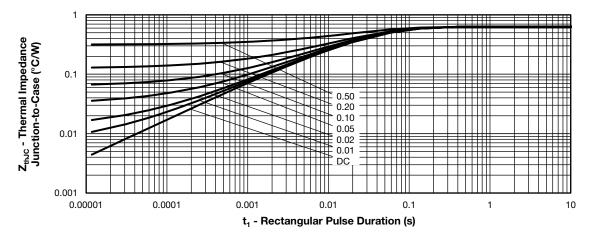


Fig. 19 - Maximum Thermal Impedance Z_{thJC} Characteristics, Diode

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ORDERING INFORMATION TABLE

Device code	vs	S-	G	т	80	D	Α	60	U	
)	2	3	4	5	6	7	8	I
	1	-	Vish	ay Sem	niconduc	tors pro	oduct			
	2	2 - Insulated gate bipolar transistor (IGBT)								
	3	-	T =	T = trench IGBT						
	4	-	Cur	Current rating (80 = 80 A)						
	5	-	Circ	Circuit configuration (D = single switch with antiparallel diode)						
	6	-	Pac	Package indicator (A = SOT-227)						
	7	-	Volt	Voltage rating (60 = 600 V)						
	8	-	Spe	Speed / type (U = ultrafast IGBT)						

CIRCUIT CONFI	GURATION	
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Single switch with AP diode	D	2 (G) O 1, 4 (E)

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95423					
Packaging information	www.vishay.com/doc?95425					



SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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