**Vishay Semiconductors** 

# Ultrafast Rectifier, 50 A FRED Pt®



www.vishay.com

PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	50 A							
V <sub>R</sub>	1200 V							
V <sub>F</sub> at I <sub>F</sub> at 125 °C	1.95 V							
t <sub>rr</sub>	57 ns							
T <sub>J</sub> max.	175 °C							
Package	TO-247AD 2L							
Circuit configuration	Single							

### FEATURES

- Ultrafast and soft recovery
- Optimized forward voltage drop
- 175 °C maximum operating junction temperature
- · Polyimide passivation
- Rugged design
- Good thermal performance
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **DESCRIPTION / APPLICATIONS**

Ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, recovery time, and soft recovery. Polyimide passivated, planar structure, and the platinum doped life time control guarantee, ruggedness, reliability characteristics, and solid value proposition for efficiency and thermal performance.

These devices are intended for use in boost stage in the AC/DC section of SMPS, high frequency output rectification of battery charger, inverters for solar inverters, or as freewheeling diodes in motor drive.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Repetitive peak reverse voltage	V <sub>RRM</sub>		1200	V					
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 138 °C, D = 0.50	50						
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_{C}$ = 25 °C, $t_{p}$ = 10 ms, sine wave	400	А					
Repetitive peak forward current	I <sub>FRM</sub>		100						
Operating junction and storage temperature	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J$ = 25 °C unless otherwise specified)								
PARAMETER SYMBOL TEST CONDITIONS				TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 500 μΑ	1200	-	-			
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 50 A	-	2.05	2.55	V		
		I <sub>F</sub> = 50 A, T <sub>J</sub> = 125 °C	-	1.95	2.37			
Reverse leakage current	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	-	330	μA		
nevelse leakage current		$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	580	μΑ		
Junction capacitance	CT	V <sub>R</sub> = 200 V	-	55	-	pF		
Series inductance	L <sub>S</sub>	Measured to lead 5 mm from package body	-	8	-	nH		

 Revision: 12-Jun-2024
 1
 Document Number: 95928

 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
 DiodesEurope@vishay.com

 THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000





FREE



www.vishay.com

## **Vishay Semiconductors**

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 10$	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$			-			
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	262	-	ns		
		T <sub>J</sub> = 125 °C		-	473	-			
Poole recovery ourrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	l <sub>F</sub> = 50 A dl <sub>F</sub> /dt = 100 A/µs	-	9.8	-	А		
Peak recovery current		T <sub>J</sub> = 125 °C	$V_{\rm R} = 390 \text{ V}$	-	17	-	A		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	1280	-			
		T <sub>J</sub> = 125 °C		-	4056	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Thermal resistance, junction to case	R <sub>thJC</sub>		-	0.2	0.28				
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	31	34	°C/W			
Thermal resistance, case to heat sink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.22	0.32				
Weight			-	6	-	g			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C			
Marking device		Case style: TO-247AD 2L	50EPU12L						

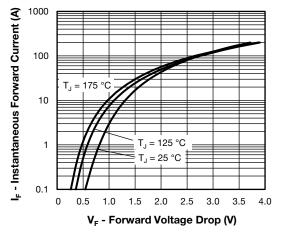


Fig. 1 - Typical Forward Voltage Drop Characteristics

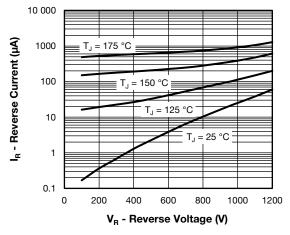


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



### **Vishay Semiconductors**



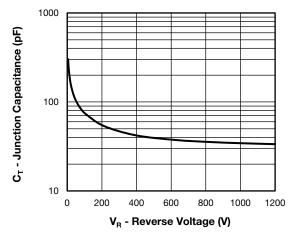


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

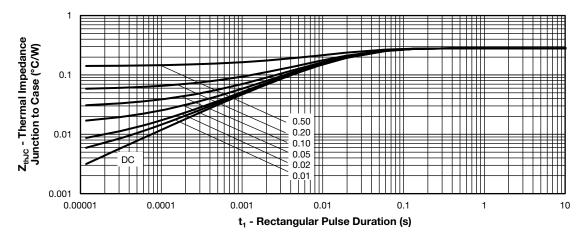


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

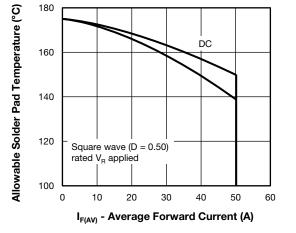


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

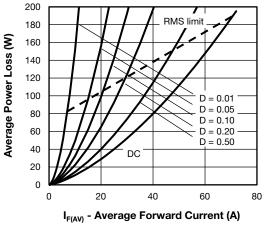


Fig. 6 - Forward Power Loss Characteristics

Revision: 12-Jun-2024

3

Document Number: 95928

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



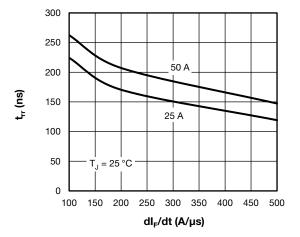


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

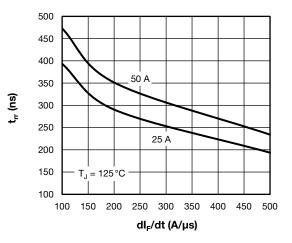


Fig. 8 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

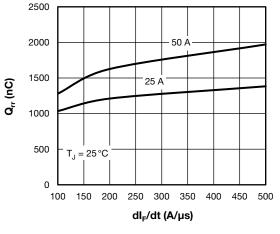


Fig. 9 - Typical Stored Charge vs. dl<sub>F</sub>/dt



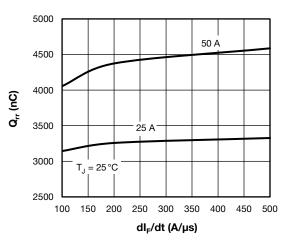


Fig. 10 - Typical Stored Charge vs. dl<sub>F</sub>/dt

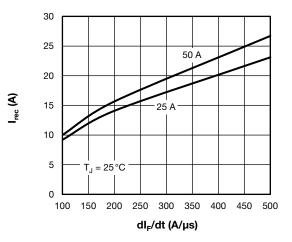


Fig. 11 - Typical Reverse Current vs. dl<sub>F</sub>/dt

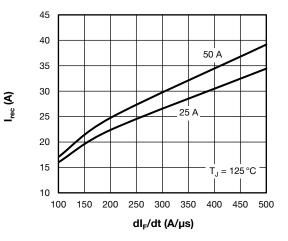


Fig. 12 - Typical Reverse Current vs. dI<sub>F</sub>/dt

Revision: 12-Jun-2024

4

Document Number: 95928

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

## VS-50EPU12L-N3

### **Vishay Semiconductors**

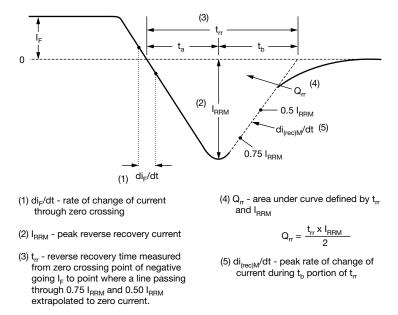
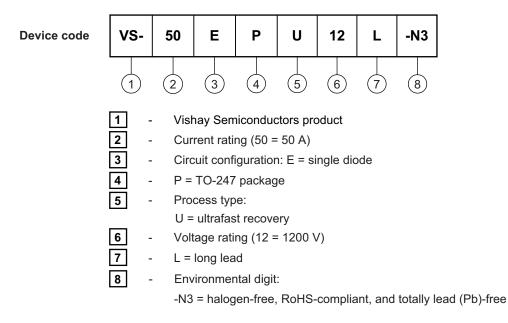


Fig. 13 - Reverse Recovery Waveform and Definitions

### **ORDERING INFORMATION TABLE**

www.vishay.com



ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-50EPU12L-N3	25	500	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS								
Dimensions www.vishay.com/doc?95536								
Part marking information	www.vishay.com/doc?95648							

Revision: 12-Jun-2024

Document Number: 95928

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



**Vishay Semiconductors** 

**TO-247AD 2L** 

#### **DIMENSIONS** in millimeters and inches



Section C - C, D - D

(b, b2)

(4)

View	<u>/ B</u>

SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES		STMDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			E	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102			E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098			е	5.46	BSC	0.215	5 BSC	
b	0.99	1.40	0.039	0.055			ØК	0.2	254	0.0	010	
b1	0.99	1.35	0.039	0.053			L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	
D2	0.51	1.35	0.020	0.053				•		•		•

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

Revision: 28-May-2018 Document Number: 95536 1 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



Vishay

## Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2025 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2025

1