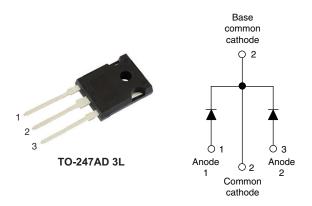
Vishay Semiconductors

www.vishay.com

Ultrafast Rectifier, 2 x 30 A FRED Pt®



PRIMARY CHARACTERISTICS						
I _{F(AV)}	2 x 30 A					
V _R	600 V					
V _F at I _F	1.75 V					
t _{rr} typ.	26 ns					
T _J max.	175 °C					
Package	TO-247AD 3L					
Circuit configuration	Common cathode					

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Designed and qualified according to commercial qualification



HALOGEN

- AEC-Q101 qualified, meets JESD 201 class 1A whisker test
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTIONS / APPLICATIONS

VS-CPU60... series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Repetitive peak reverse voltage	V _{RRM}		600	V			
Average rectified forward current per leg	I _{F(AV)}	T _C = 131 °C	30				
Non-repetitive peak surge current per leg	I _{FSM}	$T_C = 25$ °C, $t_p = 8.3$ ms half sine wave; connecting two anode pins	250	A			
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C			

ELECTRICAL SPECIFICATIONS (T_J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	YMBOL TEST CONDITIONS		TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-			
Ecrycerd voltage	VF	I _F = 30 A	-	1.4	1.75	V		
Forward voltage	۷F	I _F = 30 A, T _J = 150 °C	-	1.1	1.4			
Reverse leakage current	1	$V_{R} = V_{R}$ rated	-	0.02	30			
neverse leakage current	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	30	200	μA		
Junction capacitance	CT	V _R = 600 V	-	20	-	pF		

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST C	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	26	-		
Reverse recovery time	t _{rr}	T _J = 25 °C		-	42	-	ns	
		T _J = 125 °C	I _F = 30 A dI _F /dt = -200 A/μs V _B = 200 V	-	100	-		
Peak recovery current	1	T _J = 25 °C		-	5	-	А	
Feat recovery current	'eak recovery current I _{RRM}	T _J = 125 °C		-	10	-	~	
Reverse recovery charge	Q _{rr}	T _J = 25 °C]	-	125	-	nC	
neverse recovery charge	Qrr	T _J = 125 °C		-	580	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C		
Thermal resistance, junction to case per leg	R _{thJC}		-	0.7	1			
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W		
Thermal resistance, case to heat sink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-			
Weight			-	6.0	-	g		
Weight			-	0.21	-	oz.		
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)		
Marking device		Case style TO-247AD 3L	CPU6006LH					



VS-CPU6006LHN3

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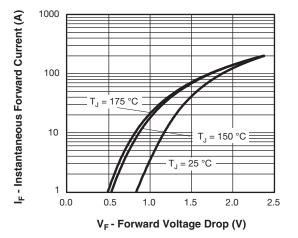


Fig. 1 - Typical Forward Voltage Drop Characteristics

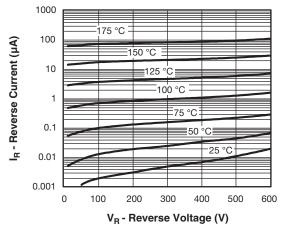


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

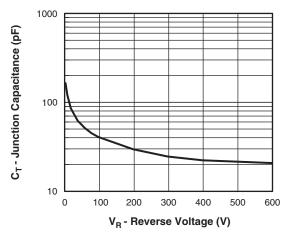


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

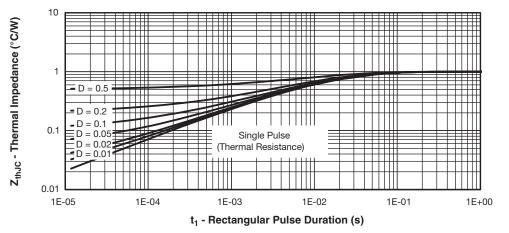
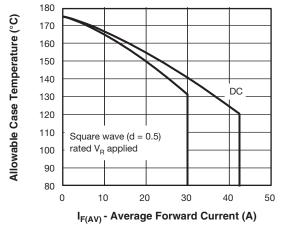
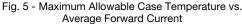


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics







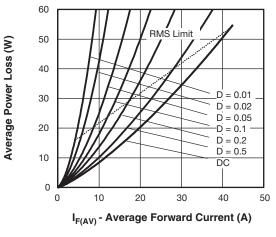


Fig. 6 - Forward Power Loss Characteristics

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
 - $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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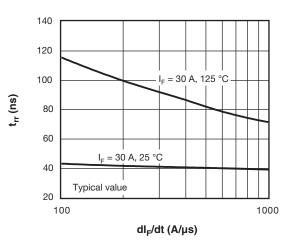


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

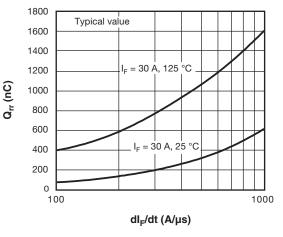


Fig. 8 - Typical Stored Charge vs. dl_F/dt

VS-CPU6006LHN3

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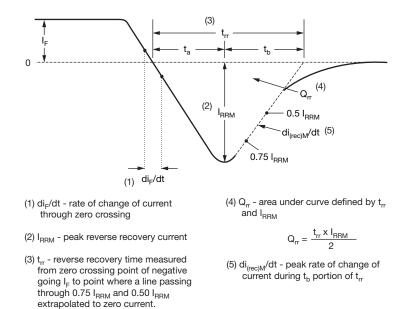


Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

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Device code	VS-	с	Р	U	60	06	L	н	N3
		2	3	4	5	6	7	8	9
	1	- Vis	hay Ser	nicondu	ctors pro	oduct			
	2 - Circuit configuration:								
	C = common cathode								
	3	- P=	TO-247	7					
	4	- U=	ultrafas	st recove	ery time				
	5	- Cu	rrent co	de (60 =	2 x 30 /	۹)			
	6 - Voltage code (06 = 600 V)								
	7 - L = long lead								
	8	- H=	AEC-C	101 qua	alified				
	9	- Env	/ironme	ntal digit					
		N3	= halog	en-free,	RoHS-c	compliar	nt, and t	otally le	ad (Pb)

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-CPU6006LHN3	25	500	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-247AD 3L	www.vishay.com/doc?95626			
Part marking information TO-247AD 3L www.vishay.com/doc?95007					

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TO-247AD 3L

DIMENSIONS in millimeters and inches



View B

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
с	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

(2, 52, 51) (4) Section C - C, D - D, E - E

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	5 BSC	
ØК	0.2	254	0.0	010	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØР	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51	BSC	0.217	' BSC	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

- ⁽³⁾ Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- ⁽⁵⁾ Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- ⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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