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\*DD Vishay Semiconductor Italiana SPA

\*File Name: VS-3EJH01-02.txt

\*This document is intended as a SPICE modeling guideline and does not  
\*constitute a commercial product datasheet. Designers should refer to the  
\*appropriate datasheet of the same number for guaranteed specification  
\*limits.

\* Model fit device @25°C-125°C

\* DEVICE=VS-3EJH01-02, Diode

.MODEL VS-3EJH01-02 D

+ IS= 1.369e-06

+ N= 2.193

+ RS= 4.695e-03

+ IKF= 4.61

+ CJO= 7.317e-11

+ M= 0.374

+ VJ= 0.433

+ ISR= 1.001e-20

+ NR= 4.995

+ BV=235.75

+ IBV= 5.00e-04

\*+ TT= 2.651e-08

\*Add subsequent parameters for temperature behavior:

+ XTI=4

+ TIKF=0.045

+ TRS1=0.0068

\*use follow TT and Vp with LTSpice at 25°C

\*+ TT=14E-9

\*+ Vp=0.20

DEVICE=VS-3EJH01-02, Diode

\*THERMAL MODEL SUBCIRCUIT

\*RC Foster model

.SUBCKT zth-3EJH01-02 Tj Tc

R THERM1 Tj 1 1.912e+00

R THERM2 1 2 2.882e-01

R THERM3 2 3 5.652e+00

R THERM4 3 Tc 4.000e-02

C THERM1 Tj 1 5.403e-03

C THERM2 1 2 6.424e-04

C THERM3 2 3 2.346e-02

C THERM4 3 Tc 4.923e-05

.ENDS zth-3EJH01-02



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