HALOGEN

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### Vishay Semiconductors

# Hyperfast Rectifier, 30 A FRED Pt®



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	30 A			
$V_{R}$	650 V			
V <sub>F</sub> at I <sub>F</sub>	1.4 V			
t <sub>rr</sub> typ.	33 ns			
T <sub>J</sub> max.	175 °C			
Package	TO-220AC 2L			
Circuit configuration	Single			

#### **FEATURES**

- Hyper fast and soft recovery
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION / APPLICATIONS**

Ultra low  $V_F$ , soft-switching hyper fast rectifiers optimized for discontinuous (critical) mode (DCM) power factor correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Repetitive peak reverse voltage	$V_{RRM}$		650	V	
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 120 °C	30	۸	
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	210	Α	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	650	-	-	.,
Forward voltage V <sub>F</sub>	I <sub>F</sub> = 30 A	-	1.8	2.1	V	
	I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.4	1.6		
Deverage legisers overwent		V <sub>R</sub> = V <sub>R</sub> rated	-	0.02	30	
Reverse leakage current I <sub>R</sub>	I'R	$T_J = 150 ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	50	300	μA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 650 V	-	22	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t <sub>rr</sub>	T <sub>J</sub> = 25 °C	$I_F = 1 \text{ A}$ $dI_F/dt = 100 \text{ A/}\mu\text{s}$ $V_R = 30 \text{ V}$	-	37	-	. ns	
	T <sub>J</sub> = 25 °C		-	33	-		
		T <sub>J</sub> = 125 °C		=	88	-	]
Peak recovery current I <sub>RRM</sub>	,	T <sub>J</sub> = 25 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 1000 \text{ A/}\mu\text{s}$ $V_R = 400 \text{ V}$	-	18	-	Α
	IRRM	T <sub>J</sub> = 125 °C		-	30	-	A
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C		=	450	-	nC
	T <sub>J</sub> = 125 °C		-	1350	-	IIC	

THERMAL - MECHANICAL SPECIFICATIONS (T <sub>J</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C
Thermal resistance, junction to case	R <sub>thJC</sub>		-	1.0	1.3	
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	-	0.5	
Weight			-	2.0	-	g
vveignt		-	0.07	-	OZ.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220AC 2L		ETH	3007	

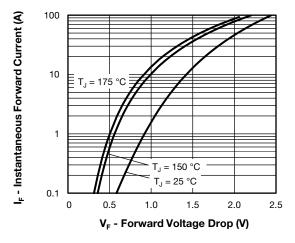


Fig. 1 - Typical Forward Voltage Drop Characteristics

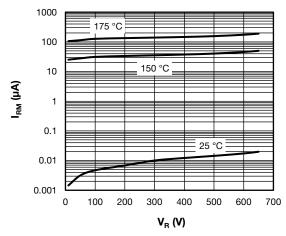


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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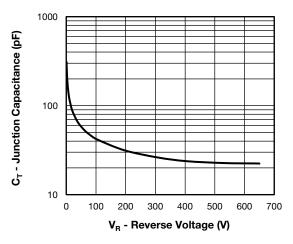


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

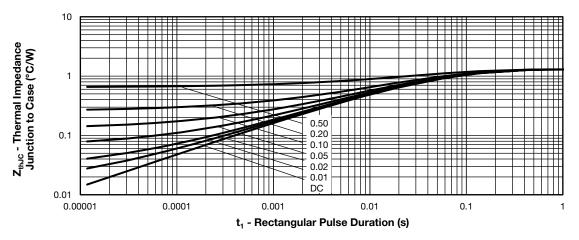


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

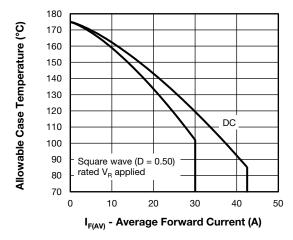


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

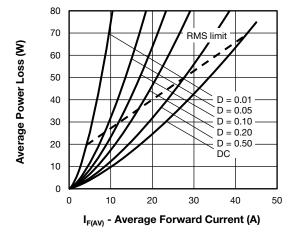


Fig. 6 - Forward Power Loss Characteristics

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = rated V_R$ 



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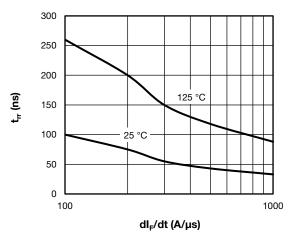


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

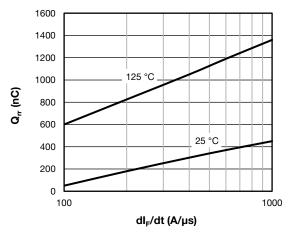
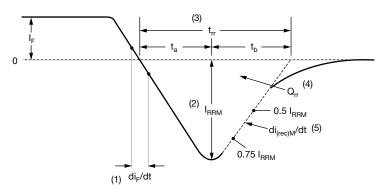


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm l_F$  to point where a line passing through 0.75  $\rm l_{RRM}$  and 0.50  $\rm l_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

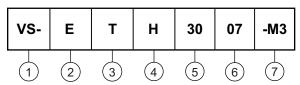
Fig. 9 - Reverse Recovery Waveform and Definitions



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### **ORDERING INFORMATION TABLE**

Device code



- 1 Vishay Semiconductors product
- 2 E = single diode
- 3 Package:

T = TO-220AC

- H = hyper fast recovery
- 5 Current rating (30 = 30 A)
- 6 Voltage rating (07 = 650 V)
- 7 Environmental digit:

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION	
VS-ETH3007-M3	50	Antistatic plastic tube	

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96156			
Part marking information	www.vishay.com/doc?95391			
SPICE model	www.vishay.com/doc?96531			



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