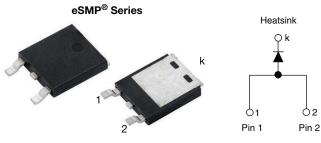


# Hyperfast Rectifier, 8 A FRED Pt®



SlimDPAK (TO-252AE)

#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	8 A			
$V_R$	600 V			
V <sub>F</sub> at I <sub>F</sub>	1.3 V			
t <sub>rr</sub> (typ.)	16 ns			
T <sub>J</sub> max.	175 °C			
Package	SlimDPAK (TO-252AE)			
Circuit configuration	Single			

#### **FEATURES**

 Hyperfast recovery time, reduced Q<sub>rr</sub> and soft recovery



- For PFC CRM / CCM operation
- · Low forward voltage drop, low power losses
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters, or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

### **MECHANICAL DATA**

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating

Base PN/-M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	$V_{RRM}$		600	V	
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 144 °C	8	۸	
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C, 10 ms sine pulse wave	90	A	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	600	-	-	
Conveyed veltage	W	I <sub>F</sub> = 8 A	-	1.6	2.40	V
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	1.3	1.7	
Develop legicage guirrent		$V_R = V_R$ rated	-	-	20	
Reverse leakage current	I <sub>R</sub>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	-	500	μA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	12	-	pF



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	21	-	
		$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	16	-	
Reverse recovery time	t <sub>rr</sub>	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{RR} = 0.25 \text{ A}$		-	-	25	ns
		T <sub>J</sub> = 25 °C	I <sub>F</sub> = 8 A dI <sub>F</sub> /dt = 500 A/μs V <sub>R</sub> = 400 V	-	25	-	
		T <sub>J</sub> = 125 °C		-	65	-	
Peak recovery current	_	T <sub>J</sub> = 25 °C		-	4.9	-	А
	IRRM	T <sub>J</sub> = 125 °C		-	8.2	-	
Reverse recovery charge	0	T <sub>J</sub> = 25 °C		-	90	-	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	260	-	110

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C
Thermal resistance, junction to mount	$R_{thJM}$		-	-	2.2	°C/W
Weight			-	0.20	ı	g
Marking device		Case style SlimDPAK (TO-252AE)	8EVH06			

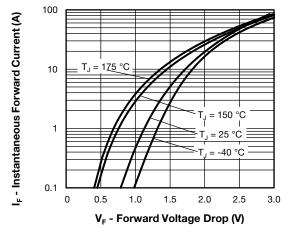


Fig. 1 - Typical Forward Voltage Drop Characteristics

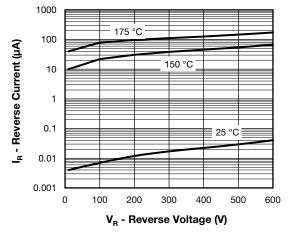


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

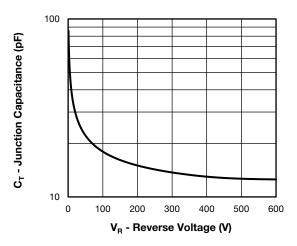


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

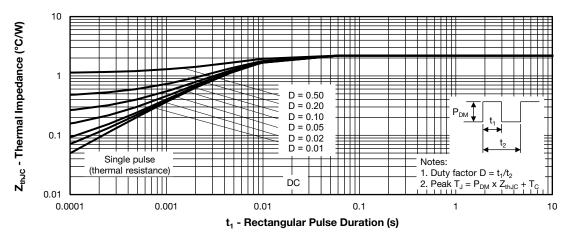


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

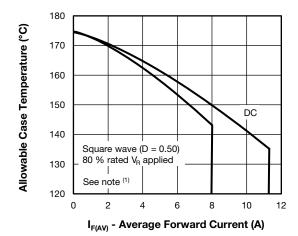


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

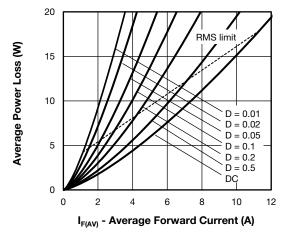


Fig. 6 - Forward Power Loss Characteristics

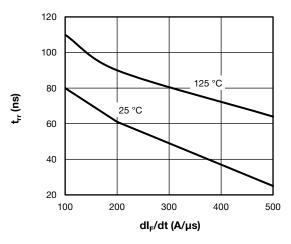
#### Note

<sup>(1)</sup> Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = rated V<sub>R</sub>



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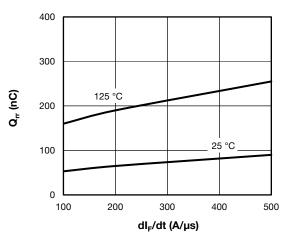
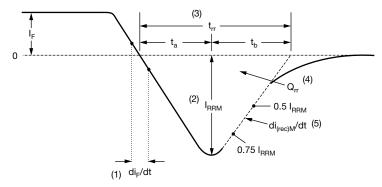


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $\mathrm{I}_{\mathrm{RRM}}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

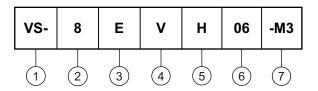
(5) di<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 9 - Reverse Recovery Waveform and Definitions



### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (8 = 8 A)

Circuit configuration:

E = single die

4 - V = SlimDPAK

5 - Process type:

H = hyperfast recovery

- Voltage code (06 = 600 V)

7 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

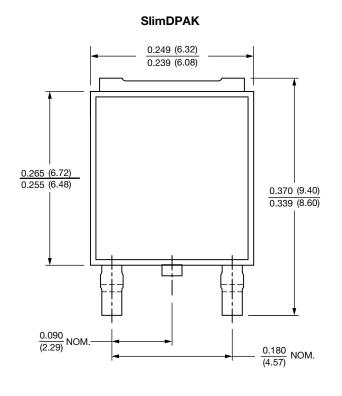
ORDERING INFORMATION (Example)						
PREFERRED P/N	ERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE BASE QUANTITY PACKAGING DESCRIPTION					
VS-8EVH06-M3/I	0.20	I	4500	13"diameter plastic tape and reel		

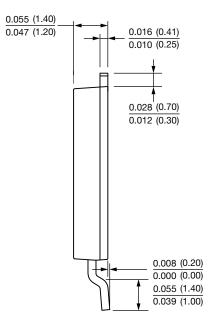
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?96081</u>				
Part marking information	www.vishay.com/doc?96085			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?97200			



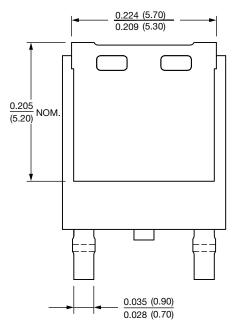
### **SlimDPAK**

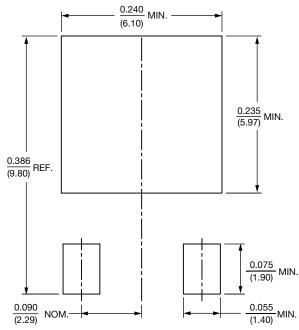
### **DIMENSIONS** in inches (millimeters)





#### **Mounting Pad Layout**







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