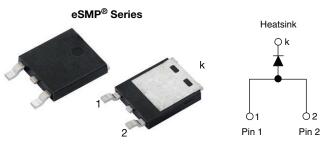


# Hyperfast Rectifier, 8 A FRED Pt®



SlimDPAK (TO-252AE)

#### **LINKS TO ADDITIONAL RESOURCES**



| PRIMARY CHARACTERISTICS          |                     |  |  |  |
|----------------------------------|---------------------|--|--|--|
| I <sub>F(AV)</sub>               | 8 A                 |  |  |  |
| V <sub>R</sub>                   | 600 V               |  |  |  |
| V <sub>F</sub> at I <sub>F</sub> | 1.4 V               |  |  |  |
| t <sub>rr</sub> (typ.)           | 14 ns               |  |  |  |
| T <sub>J</sub> max.              | 175 °C              |  |  |  |
| Package                          | SlimDPAK (TO-252AE) |  |  |  |
| Circuit configuration            | Single              |  |  |  |

#### **FEATURES**

• Hyperfast recovery time, extremely Q<sub>rr</sub> recovery

• For PFC CCM operation

• Low forward voltage drop, low power losses

• Low leakage current

ROHS COMPLIANT HALOGEN FREE

 Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters, or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

### **MECHANICAL DATA**

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating

Base PN/-M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

| ABSOLUTE MAXIMUM RATINGS                    |                                   |                                      |             |       |  |
|---|-----------------------------------|--------------------------------------|-------------|-------|--|
| PARAMETER                                   | SYMBOL                            | TEST CONDITIONS                      | VALUES      | UNITS |  |
| Peak repetitive reverse voltage             | $V_{RRM}$                         |                                      | 600         | V     |  |
| Average rectified forward current           | I <sub>F(AV)</sub>                | T <sub>C</sub> = 140 °C              | 8           | ^     |  |
| Non-repetitive peak surge current           | I <sub>FSM</sub>                  | $T_J = 25$ °C, 10 ms sine pulse wave | 80          | A     |  |
| Operating junction and storage temperatures | T <sub>J</sub> , T <sub>Stg</sub> |                                      | -55 to +175 | °C    |  |

| <b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified) |                    |  |      |      |      |       |  |
|--|--------------------|--|------|------|------|-------|--|
| PARAMETER  | SYMBOL             | TEST CONDITIONS  | MIN. | TYP. | MAX. | UNITS |  |
| Breakdown voltage, blocking voltage  | $V_{BR}$ , $V_{R}$ | I <sub>R</sub> = 100 μA  | 600  | -    | -    |       |  |
| Forward voltage  | V <sub>F</sub>     | I <sub>F</sub> = 8 A   | -    | 2.3  | 3.0  | V     |  |
| i orward voitage   |                    | I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C                  | -    | 1.4  | 1.8  |       |  |
| Reverse leakage current  | I <sub>R</sub>     | $V_R = V_R$ rated  | -    | -    | 20   |       |  |
|  |                    | T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated | -    | =.   | 500  | μA    |  |
| Junction capacitance   | C <sub>T</sub>     | V <sub>R</sub> = 600 V   | -    | 12   | -    | pF    |  |



| <b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified) |                  |  |  |      |      |      |       |
|---|------------------|--|--|------|------|------|-------|
| PARAMETER   | SYMBOL           | TEST CONDITIONS  |  | MIN. | TYP. | MAX. | UNITS |
|   |                  | $I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$  |  | -    | 20   | -    |       |
|   |                  | $I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$ |  | -    | 14   | -    |       |
| Reverse recovery time   | t <sub>rr</sub>  | $I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{RR} = 0.25 \text{ A}$            |  | -    | -    | 18   | ns    |
|   |                  | T <sub>J</sub> = 25 °C   | I <sub>F</sub> = 8 A<br>dI <sub>F</sub> /dt = 500 A/μs<br>V <sub>R</sub> = 400 V | -    | 20   | -    |       |
|   |                  | T <sub>J</sub> = 125 °C  |  | -    | 54   | -    |       |
| Peak recovery current   | I <sub>RRM</sub> | T <sub>J</sub> = 25 °C   |  | -    | 4.0  | -    | ^     |
|   |                  | T <sub>J</sub> = 125 °C  |  | -    | 8.0  | -    | A     |
| Reverse recovery charge   | 0                | T <sub>J</sub> = 25 °C   |  | -    | 60   | -    | nC    |
|   | $Q_{rr}$         | T <sub>J</sub> = 125 °C  |  | -    | 200  | -    | 110   |

| THERMAL - MECHANICAL SPECIFICATIONS            |                                   |                                |      |      |      |       |
|--|-----------------------------------|--------------------------------|------|------|------|-------|
| PARAMETER                                      | SYMBOL                            | TEST CONDITIONS                | MIN. | TYP. | MAX. | UNITS |
| Maximum junction and storage temperature range | T <sub>J</sub> , T <sub>Stg</sub> |                                | -55  | -    | 175  | °C    |
| Thermal resistance, junction to mount          | $R_{thJM}$                        |                                | -    | -    | 2.2  | °C/W  |
| Weight   |                                   |                                | -    | 0.20 | -    | g     |
| Marking device                                 |                                   | Case style SlimDPAK (TO-252AE) |      | 8EV  | 'X06 | •     |

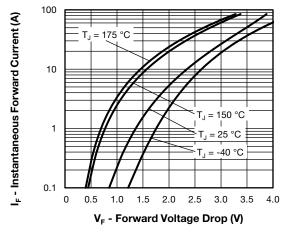


Fig. 1 - Typical Forward Voltage Drop Characteristics

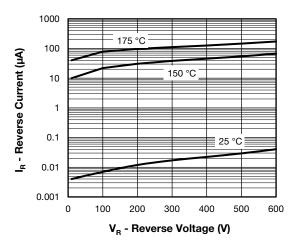


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

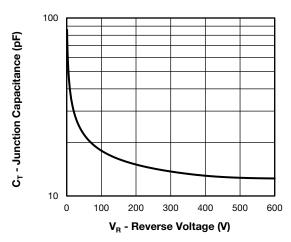


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

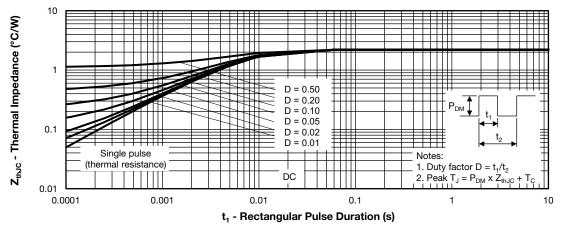


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

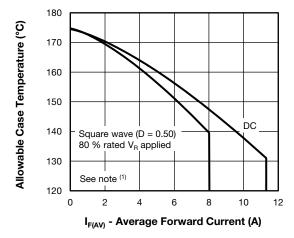


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

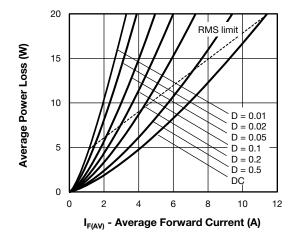


Fig. 6 - Forward Power Loss Characteristics

#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = rated V<sub>R</sub>



### www.vishay.com

# Vishay Semiconductors

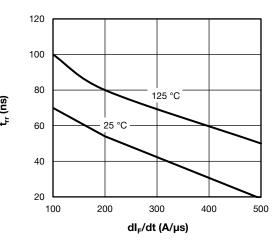


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

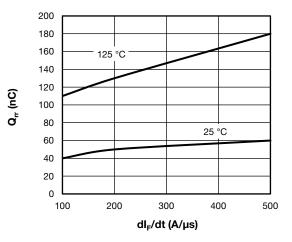
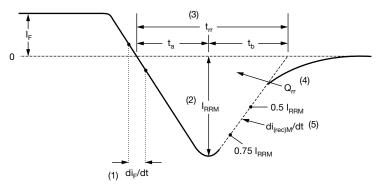


Fig. 8 - Typical Stored Charge vs. dl<sub>E</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_{rr}$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

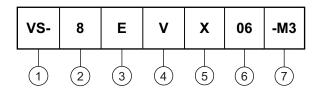
(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 9 - Reverse Recovery Waveform and Definitions



### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (8 = 8 A)

3 - Circuit configuration:

E = single die

4 - V = SlimDPAK

5 - Process type:

X = hyperfast rectifier

6 - Voltage code (06 = 600 V)

7 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

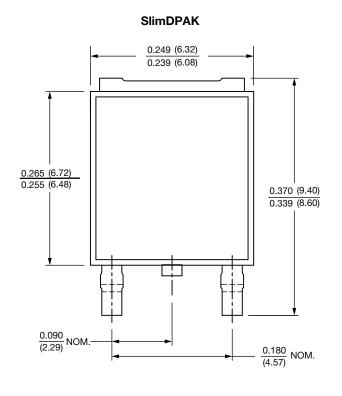
| ORDERING INFORMATION (Example) |                 |                        |               |                                   |  |  |
|--------------------------------|-----------------|------------------------|---------------|-----------------------------------|--|--|
| PREFERRED P/N                  | UNIT WEIGHT (g) | PREFERRED PACKAGE CODE | BASE QUANTITY | PACKAGING DESCRIPTION             |  |  |
| VS-8EVX06-M3/I                 | 0.20            | I                      | 4500          | 13"diameter plastic tape and reel |  |  |

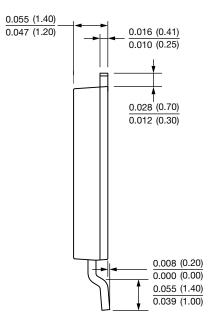
| LINKS TO RELATED DOCUMENTS                 |                          |  |  |  |
|--|--------------------------|--|--|--|
| Dimensions <u>www.vishay.com/doc?96081</u> |                          |  |  |  |
| Part marking information                   | www.vishay.com/doc?96085 |  |  |  |
| Packaging information                      | www.vishay.com/doc?88869 |  |  |  |



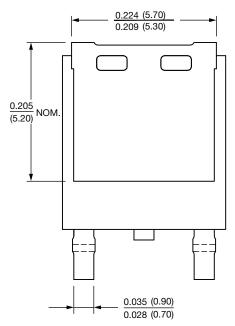
## **SlimDPAK**

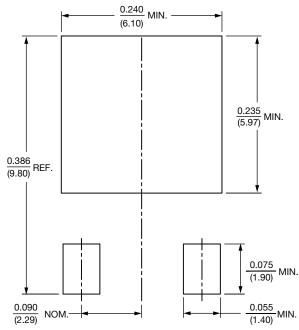
### **DIMENSIONS** in inches (millimeters)





### **Mounting Pad Layout**







# **Legal Disclaimer Notice**

Vishay

### **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.