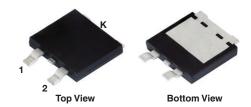
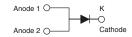


Hyperfast Rectifier, 16 A FRED Pt®

eSMP® Series SMPD (TO-263AC)





LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	16 A			
V_R	600 V			
V_F at I_F (T_J = 150 °C)	1.24 V			
t _{rr}	30 ns			
T _J max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Single			

FEATURES

• Hyperfast recovery time, reduced Q_{rr}, and soft recovery



• 175 °C maximum operating junction temperature

COMPLIANT HALOGEN **FREE**

- For PFC CRM, snubber operation
- · Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, lighting, in the AC/DC section of SMPS, freewheeling and clamp diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		600	V
Average rectified forward current	I _{F(AV)} (1)	T _C = 127 °C	16	۸
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C, 10 ms sine pulse	160	_ ^

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-	
Forward voltage V _F	W	I _F = 16 A	-	1.65	2.15	V
	I _F = 16 A, T _J = 150 °C	-	1.24	1.65		
Reverse leakage current	I _R	$V_R = V_R$ rated	-	-	20	
		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	500	- μΑ
Junction capacitance	C _T	V _R = 600 V	-	16	-	pF

⁽¹⁾ Mounted on infinite heatsink



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	30	-	
B		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	30	
Reverse recovery time	t _{rr}	T _J = 25 °C	I _F = 16 A, dI _F /dt = 500 A/μs, V _R = 400 V	-	43	1	ns
		T _J = 125 °C		-	92	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	7.7	-	Α
		T _J = 125 °C		-	13.8	1	^
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	150	-	μC
Theverse recovery charge		T _J = 125 °C		-	600	-	μΟ

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	+175	°C
Thermal resistance, junction to mount	R_{thJM}		-	1.2	1.7	°C/W
Approximate weight				0.55		g
Marking device		Case style SMPD (TO-263AC)		16EI	DH06	

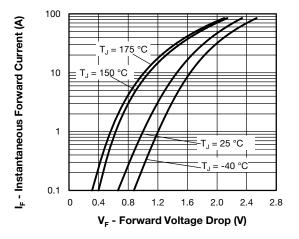


Fig. 1 - Typical Forward Voltage Drop Characteristics

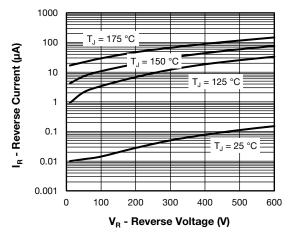


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



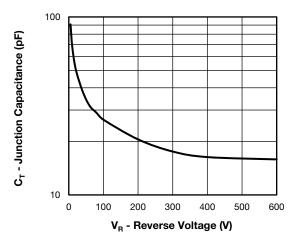


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

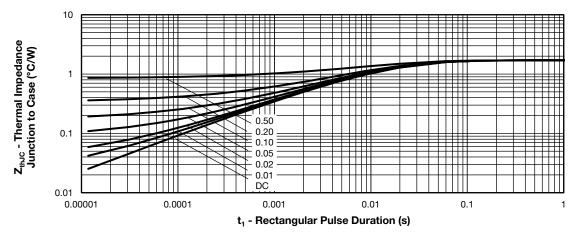


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

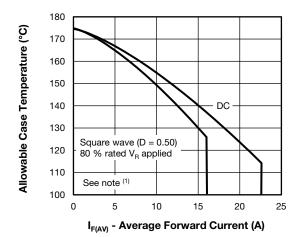


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

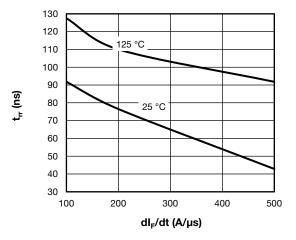


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 5); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = rated V_R$

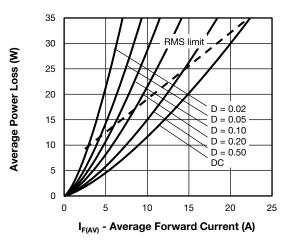


Fig. 7 - Forward Power Loss Characteristics

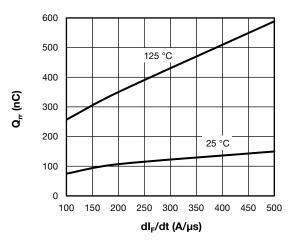
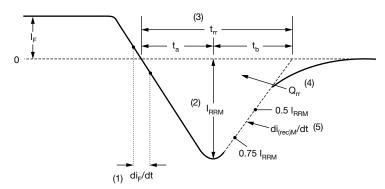


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

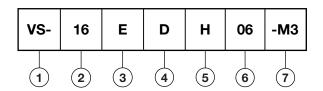
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (16 A)

3 - Circuit configuration:

E = single die

4 - D = SMPD package

5 - Process type,

H = hyperfast recovery

6 - Voltage code (06 = 600 V)

7 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

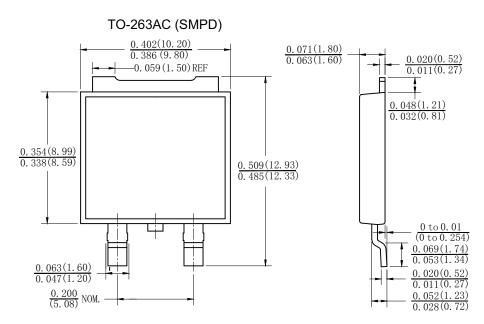
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION				
VS-16EDH06-M3/I	2000	2000	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95604				
Part marking information	www.vishay.com/doc?95566			
Packaging information	www.vishay.com/doc?88869			

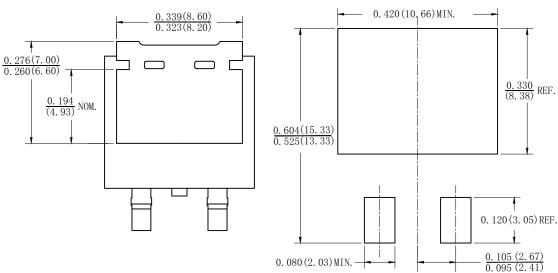


TO-263AC (SMPD)

DIMENSIONS in inches (millimeters)



Mounting Pad Layout





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Vishay

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