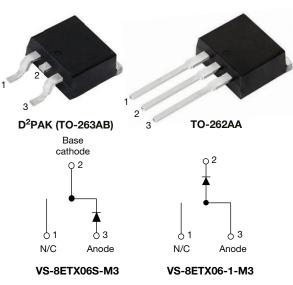
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VS-8ETX06S-M3, VS-8ETX06-1-M3

Vishay Semiconductors

Hyperfast Rectifier, 8 A FRED Pt[®]



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I _{F(AV)}	8 A				
V _R	600 V				
V _F at I _F	1.4 V				
t _{rr} typ.	15 ns				
T _J max.	175 °C				
Package	D ² PAK (TO-263AB), TO-262AA				
Circuit configuration	Single				

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: D²PAK (TO-263AB), TO-262AA

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage	V _{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 143 °C	8	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	110	А
Peak repetitive forward current	I _{FM}		18	
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C	unless otherwise specified	(k			
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	600	-	-	
Forward voltage	N	I _F = 8 A	-	2.3	3.0	V
Forward voltage	V _F	I _F = 8 A, T _J = 150 °C	-	1.4	1.7	
		$V_R = V_R$ rated	-	0.03	50	
Reverse leakage current	I _R	$T_J = 150 \ ^\circ C$, $V_R = V_R$ rated	-	35	500	μA
Junction capacitance	CT	V _R = 600 V	-	17	-	pF
Series inductance	L _S	Measured lead to lead 5 mm	-	8.0	-	nH

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COMPLIANT

HALOGEN

FREE



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DYNAMIC RECOVERY CHA	RACTERIS	TICS (T _C = 25 °C	C unless otherwis	e specifie	ed)		
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 1$	00 A/µs, V _R = 30 V	-	15	19	
Reverse recovery time	+	$I_F = 8 \text{ A}, \text{ d}I_F/\text{d}t = 10$	00 A/µs, V _R = 30 V	-	16	24	nc
	t _{rr}	T _J = 25 °C		-	17	-	- ns
		T _J = 125 °C		-	40	-	
Peak recovery current	1	T _J = 25 °C	I _F = 8 A dI _F /dt = 200 A/μs	-	2.3	-	А
Feak recovery current	I _{RRM}	T _J = 125 °C	$V_{\rm B} = 390 \text{ V}$	-	4.5	-	~
Reverse recovery charge	0	T _J = 25 °C	VR - 000 V	-	20	-	nC
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	100	-	nc
Reverse recovery time	t _{rr}		I _F = 8 A	-	31	-	ns
Peak recovery current	I _{RRM}	T _J = 125 °C	dI _F /dt = 600 A/µs	-	12	-	А
Reverse recovery charge	Q _{rr}		V _R = 390 V	-	195	-	nC

THERMAL - MECHANICAL	SPECIFIC/	ATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction-to-case per leg	R _{thJC}		-	1.4	2	
Thermal resistance, junction-to-ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking davias		Case style D ² PAK (TO-263AB)	8ETX06S			
Marking device		Case style TO-262AA		8ET>	(06-1	

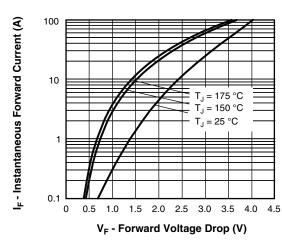


Fig. 1 - Typical Forward Voltage Drop Characteristics

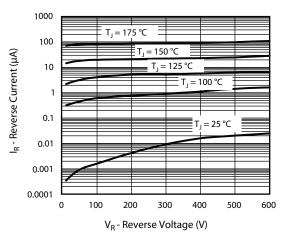


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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VS-8ETX06S-M3, VS-8ETX06-1-M3

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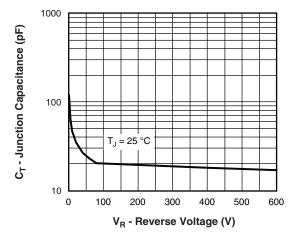
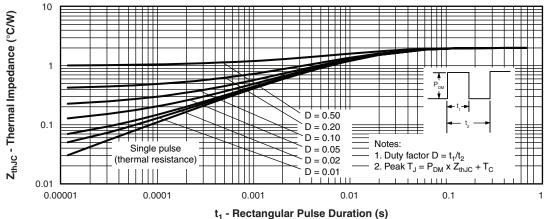
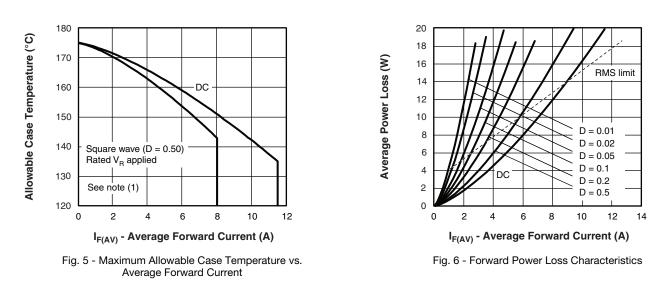


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



t₁ - Nectaligular Pulse Duration (s)

Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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VS-8ETX06S-M3, VS-8ETX06-1-M3

300

250

200

150

100

50

0

100

Q_{rr} (nC)

V_R = 390 V T_{.1} = 125 °C

T₁ = 25 °C



16 A

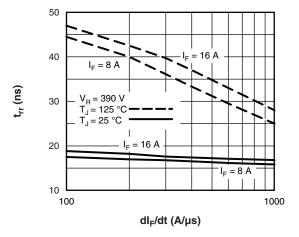
dl_F/dt (A/µs)

Fig. 8 - Typical Stored Charge vs. dl_F/dt

= 16 A

8 A

1000



www.vishay.com

Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt



SHA

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R

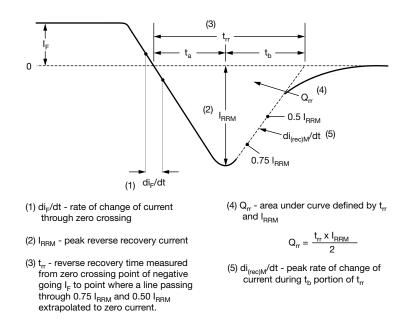


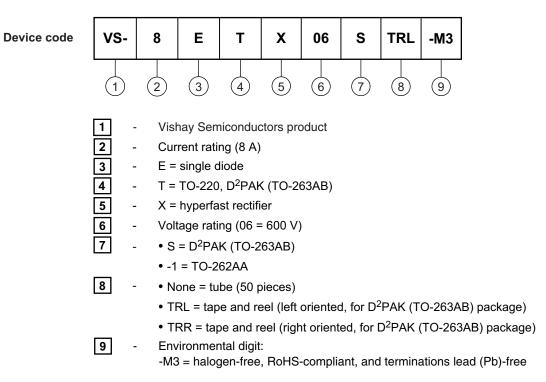
Fig. 9 - Reverse Recovery Waveform and Definitions



VS-8ETX06S-M3, VS-8ETX06-1-M3

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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Exan	nple)	
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-8ETX06S-M3	50	Antistatic plastic tubes
VS-8ETX06STRL-M3	800	13" diameter plastic tape and reel
VS-8ETX06STRR-M3	800	13" diameter plastic tape and reel
VS-8ETX06-1-M3	50	Antistatic plastic tubes

LINKS TO RELATED DOCUMENTS				
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164		
Dimensions	TO-262AA	www.vishay.com/doc?96165		
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444		
Part marking information	TO-262AA	www.vishay.com/doc?95443		
Packaging information		www.vishay.com/doc?96424		
SPICE model		www.vishay.com/doc?95393		

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D²PAK

DIMENSIONS in millimeters and inches



ota	ted	90	°C
<u>S</u>	cale	<u>ə:</u> 8	:1

SYMBOL	MILLIM	ETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
с	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STNDUL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	2.54 BSC		0.100 BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

(3) Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Datum A and B to be determined at datum plane H

(6) Controlling dimension: inches

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

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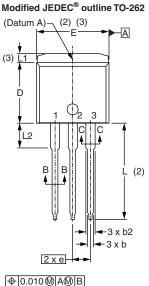
Outline Dimensions

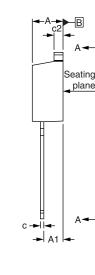


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TO-262AA

DIMENSIONS in millimeters and inches





F D1 (3) (3) Section A - A Base (4) Plating b1. b3 metal ≰ c1 (4) -(b, b2)-Section B - B and C - C Scale: None





Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode

Lead assignments

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100) BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

 ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the second dimensioner of the second dimensis and the second dimensioner of the second dimensioner of the the outmost extremes of the plastic body (3)

Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only (5)

Controlling dimension: inches

(6) Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)

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