High Performance Schottky Rectifier, 2 x 10 A **FEATURES**

- 150 °C T_J operation
- Center tap D²PAK (TO-263AB) and TO-262AA RoHS packages COMPLIANT HALOGEN
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

-65 to +150

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform (per device)	20	٨		
I _{FRM}	$T_{\rm C} = 135 \ ^{\circ}{\rm C}$ (per leg)	20	A		
V _{RRM}		35/45	V		
I _{FSM}	t _p = 5 μs sine	1060	A		
V _F	10 A _{pk} , T _J = 125 °C	0.57	V		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-MBRB2035CT-M3 VS-MBR2035CT-1-M3	VS-MBRB2045CT-M3 VS-MBR2045CT-1-M3	UNITS
Maximum DC reverse voltage	V _R	35	45	V
Maximum working peak reverse voltage	V _{RWM}		40	V

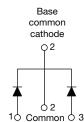
VS-MBR20..CT-1-M3

Range

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 10 A			
V _R	35 V, 45 V			
V _F at I _F	0.72 V			
I _{RM} max.	15 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	8 mJ			
Package	D ² PAK (TO-263AB), TO-262AA			
Circuit configuration	Common cathode			

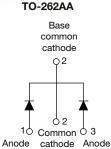
D²PAK (TO-263AB)

SHAY



Anode cathode Anode

VS-MBRB20..CT-M3



VS-MBRB20..CT-M3, VS-MBR20..CT-1-M3

Vishay Semiconductors

Revision: 21-Dec-2021

TJ

°C

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000

1





FREE



www.vishay.com

2

3



Vishay Semiconductors

ABSOLUTE MAXIMUM RATI	NGS				
PARAMETER	SYMBOL	1	TEST CONDITIONS	VALUES	UNITS
Maximum average per leg		T _C = 135 °C, rate	d V-	10	
forward current per device	I _{F(AV)}	1C = 155 C, fate	u v _R	20	
Peak repetitive forward current per leg	I _{FRM}	Rated V _R , square	wave, 20 kHz, T _C = 135 °C	20	
Non repetitive peak ourge ourgent		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	1060	A
Non-repetitive peak surge current	IFSM	Surge applied at single phase, 60 l	rated load conditions halfwave, Hz	150	
Non-repetitive avalanche energy per leg	E _{AS}	$T_{J} = 25 \text{ °C}, I_{AS} = 25 \text{ °C}$	2 A, L = 4 mH	8	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying Frequency limited V _A = 1.5 x V _R typi	, ,	2	А

ELECTRICAL SPECIFICAT	IONS				
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		VALUES	UNITS
		20 A	T _J = 25 °C	0.84	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	10 A	T _ 125 °C	0.57	V
		20 A	– T _J = 125 °C	0.72	
Maximum instantaneous	I _{RM} ⁽¹⁾	T _J = 25 °C	Rated DC voltage	0.1	m۸
reverse current	IRM (1)	T _J = 125 °C	haled DC vollage	15	mA
Threshold voltage	V _{F(TO)}			0.354	V
Forward slope resistance	r _t	$T_J = T_J$ maximum		17.6	mΩ
Maximum junction capacitance	CT	V _R = 5 V _{DC} (test signal rang	ge 100 kHz to 1 MHz), 25 °C	600	pF
Typical series inductance	L _S	Measured from top of term	inal to mounting plane	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range	TJ		-65 to 150	ъ	
Maximum storage temperature range	T _{Stg}		-65 to 175	U	
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation	2.0	°C/W	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50	C/W	
Approvimeto weight			2	g	
Approximate weight			0.07	oz.	
Mounting torgueminimum		Non-lubricated threads	6 (5)	kgf ⋅ cm	
maximum		Non-Inducated threads	12 (10)	(lbf ⋅ in)	
		Case style D ² PAK (TO-263AB)	MBRB2	2035CT	
Marking davias		Case style D-PAK (TO-263AB)	MBRE		
Marking device		Copp style TO 26244	MBR20	35CT-1	
		Case style TO-262AA	MBR20	45CT-1	

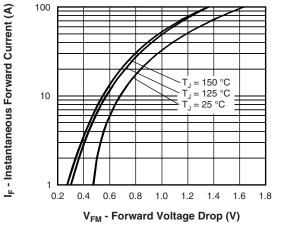
Revision: 21-Dec-2021

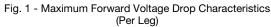
Document Number: 96405



VS-MBRB20..CT-M3, VS-MBR20..CT-1-M3

Vishay Semiconductors





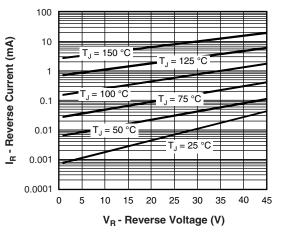


Fig. 2 - Typical Values of Reverse Current vs.Reverse Voltage (Per Leg)

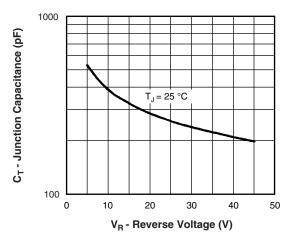
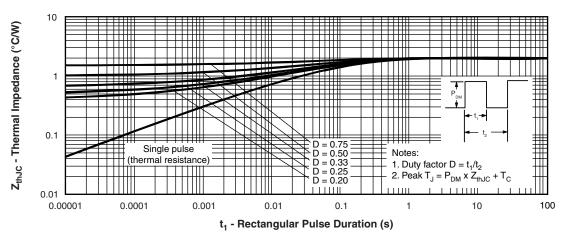


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)



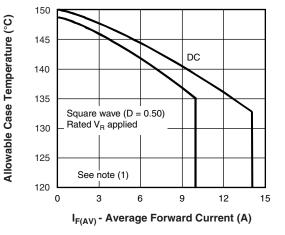


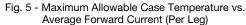
Revision: 21-Dec-2021 3 Document Number: 96405 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



VS-MBRB20..CT-M3, VS-MBR20..CT-1-M3

Vishay Semiconductors





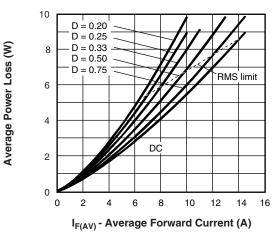


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

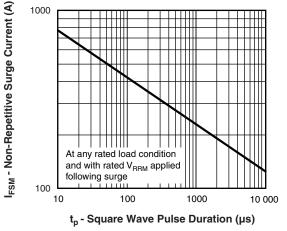


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

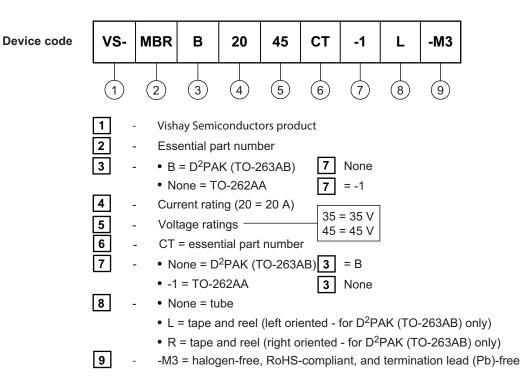
Note

 $^{(1)} \mbox{ Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \mbox{ forward power loss = } I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ Pd_{REV} = \mbox{ inverse power loss = } V_{R1} \ x \ I_R \ (1 - D); \ I_R \ at \ V_{R1} = \ rated \ V_R$



Vishay Semiconductors

ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)					
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION			
VS-MBRB2035CT-M3	50	Antistatic plastic tubes			
VS-MBRB2045CTL-M3	800	13" diameter plastic tape and reel			
VS-MBRB2045CT-M3	50	Antistatic plastic tubes			
VS-MBRB2045CTR-M3	800	13" diameter plastic tape and reel			
VS-MBR2035CT-1-M3	50	Antistatic plastic tubes			
VS-MBR2045CT-1-M3	50	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS					
Dimensions D ² PAK (TO-263AB)		www.vishay.com/doc?96164			
Dimensions —	TO-262AA	www.vishay.com/doc?96165			
Part marking information —	D ² PAK (TO-263AB)	www.vishay.com/doc?95444			
Part marking information —	TO-262AA	www.vishay.com/doc?95443			
Packaging information		www.vishay.com/doc?96424			
SPICE model		www.vishay.com/doc?95504			

Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



ota	ted	90	°C
<u>S</u>	cale	<u>ə:</u> 8	:1

SYMBOL	MILLIM	ETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
с	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STNDUL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	2.54 BSC		0.100 BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

(3) Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Datum A and B to be determined at datum plane H

(6) Controlling dimension: inches

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 13-Jul-17

1

Document Number: 96164

For technical questions within your region: DiodesAmericas@vishav.com, DiodesAsia@vishav.com, DiodesEurope@vishav.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



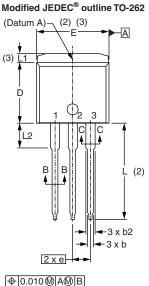
Outline Dimensions

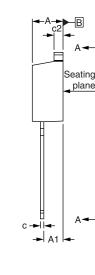


Vishay Semiconductors

TO-262AA

DIMENSIONS in millimeters and inches





F D1 (3) (3) Section A - A Base (4) Plating b1. b3 metal ≰ c1 (4) -(b, b2)-Section B - B and C - C Scale: None





Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode

Lead assignments

CVMPOI	MILLIN	MILLIMETERS		HES	NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100) BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

 ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the second dimensioner of the second dimensis of the second dimensioner of the second dimensioner of the the outmost extremes of the plastic body (3)

Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only (5)

Controlling dimension: inches

(6) Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)

Revision: 30-Nov-17

1



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2025 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2025

1