RoHS

COMPLIANT HALOGEN

FREE



Vishay Semiconductors

Hyperfast Rectifier, 8 A FRED Pt®



TO-220 FullPAK 2L



VS-8ETX06FP-N3

PRIMARY CHARACTERISTICS					
I _{F(AV)}	8 A				
V _R	600 V				
V _F at I _F	1.4 V				
t _{rr} (typ.)	15 ns				
T _J max.	175 °C				
Package	TO-220 FullPAK 2L				
Circuit configuration	Single				

FEATURES

- · Hyperfast recovery time
- Benchmark ultra low forward voltage drop
- 175 °C operating junction temperature
- · Low leakage current
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recover time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage	V_{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 106 °C	8	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	110	Α
Repetitive peak forward current	I _{FM}		18	
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	.,
Forward voltage V _F	I _F = 8 A	-	2.3	3.0	V	
	VF	I _F = 8 A, T _J = 150 °C	-	1.4	1.7	
Poverse leakage ourrent	Deverage legisage gurrent	$V_R = V_R$ rated	-	0.3	50	
Reverse leakage current I _R	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	35	500	μA	
Junction capacitance	C _T	$V_{R} = 600 \text{ V}$	-	17	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	1	nΗ



DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t _{rr}		$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	15	19	
	+	$I_F = 8 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		=	16	24	
neverse recovery time	t _{rr}	T _J = 25 °C	I _F = 8 A dI _F /dt = 200 A/μs V _R = 390 V	-	17	-	ns
		T _J = 125 °C		-	40	-	
Dools recovery as week	1	T _J = 25 °C		=	2.3	-	A
Peak recovery current	I _{RRM}	T _J = 125 °C		-	4.5	-	
Poverse receivent charge	0	T _J = 25 °C		-	20	-	nC
Reverse recovery charge Q _r	Q _{rr}	T _J = 125 °C		-	100	-	11C
Reverse recovery time	t _{rr}	T _J = 125 °C	I _F = 8 A dI _F /dt = 600 A/μs	-	31	-	ns
Peak recovery current	I _{RRM}			-	12	-	Α
Reverse recovery charge	Q_{rr}		V _R = 390 V	-	195	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction-to-case	R _{thJC}		ı	3.4	4.3	°C/W
Thermal resistance, junction-to-ambient per leg	R _{thJA}	Typical socket mount	-	-	70	
Thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK 2L		8ETX	06FP	

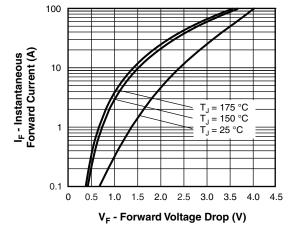


Fig. 1 - Typical Forward Voltage Drop Characteristics

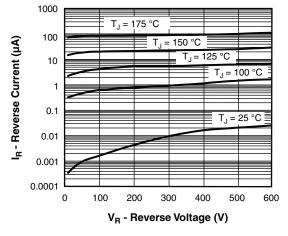


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

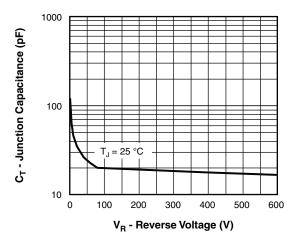


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

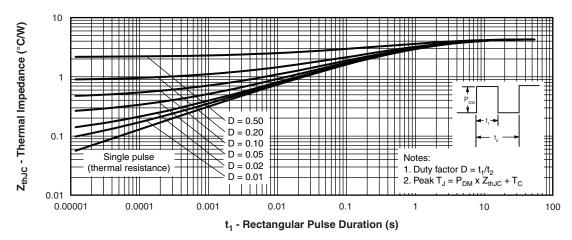


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

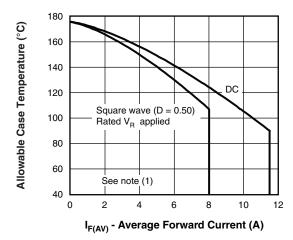


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

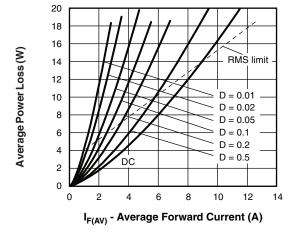


Fig. 6 - Forward Power Loss Characteristics

Note

⁽¹⁾ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 5); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R



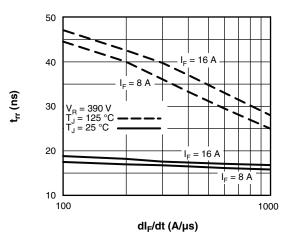


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

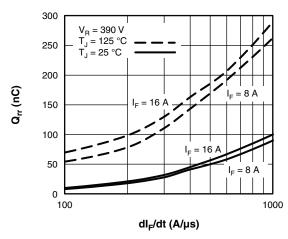
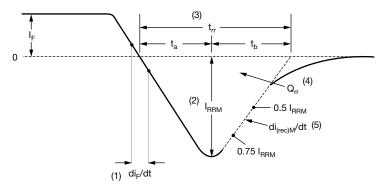


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

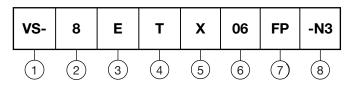
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (8 = 8 A)

3 - E = single

- T = TO-220, D²PAK (TO-263AB)

5 - X = hyperfast rectifier

6 - Voltage rating (06 = 600 V)

7 - FP = TO-220 FullPAK 2L

8 - Environmental digit:

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

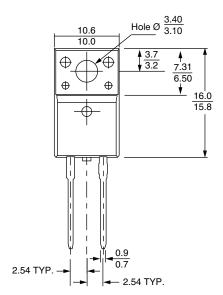
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-8ETX06FP-N3	50	1000	Antistatic plastic tube		

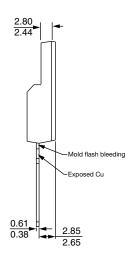
LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96157
Part marking information	www.vishay.com/doc?95392

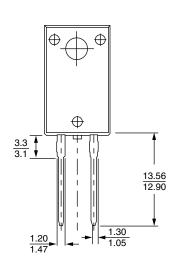


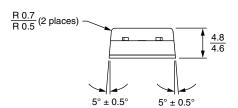
2L TO-220 FullPAK

DIMENSIONS in millimeters









Bottom view



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Vishay

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